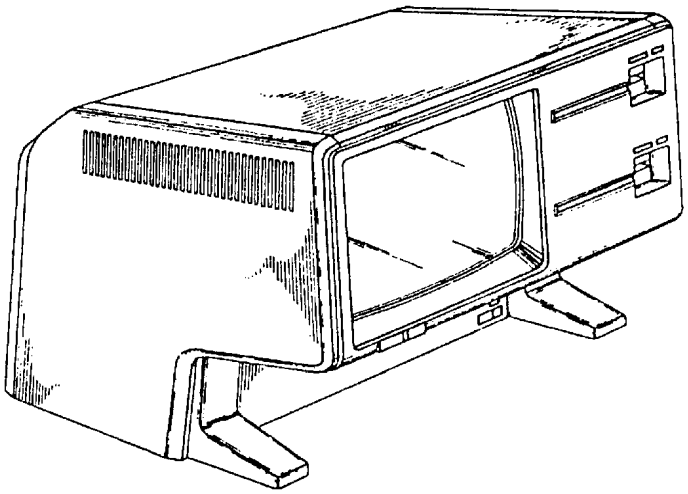
Apple Lisa Computer Device Drivers Manual

(Alpha Draft)



11 January 1998

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Chapter 1
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1.1 INTRODUCTION TO CONFIGURABLE DRIVERS

Configurable drivers are drivers that can be incorporated into the Operating System without rebooting it. With this feature, you simply inform the Operating System of the location of the new device, plug it in, and use it. Configurable drivers are compiled and linked as independent programs; once loaded into memory, they function as an integral part of the Operating System. This manual tells you how to write configurable drivers for the Lisa.

Every peripheral device designed for use with the Lisa must be supported by a *device driver* that communicates with the device and with the Operating System (OS). Figure 1-1 shows the Operating System environment for device drivers; the figure also indicates where in this manual or related manuals you can find information about the interfaces between the device, the driver, the Operating System, and the application program.

If the controller for a device can simultaneously support several devices by demultiplexing interrupts, it must have a *demultiplexing driver* that dispatches interrupts to the device drivers. Section 1.5 describes communication between drivers.

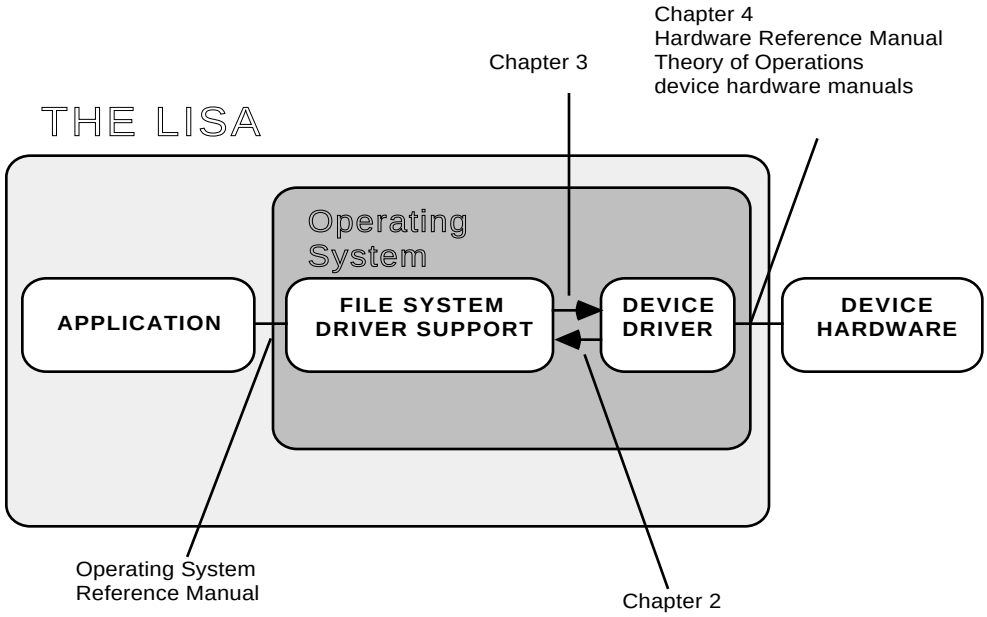


FIGURE 1-1

Figure 1-1. Operating System Environment for Drivers

Driver operations vary with the type of device being supported. A driver for a sequential device must be able to transmit and receive byte streams of arbitrary length. For a disk device with a directory, the driver must be read and write contiguous 512-byte blocks and manage the 24-byte distributed directory record stored with every disk block. Disk drivers must also handle bad-block sparing. Demultiplexing drivers need to determine which device is the source of an interrupt and pass the interrupt to the appropriate device driver.

Because the Lisa Operating System is a multitasking system, a device may receive I/O requests from more than one process. Therefore its driver must manage I/O Request Block queues and interact with the OS Scheduler and the Memory Manager.

1.2 INVOKING A DRIVER THROUGH THE OS

When an application program requests I/O, a device driver is invoked by the OS, usually from the File System. The File System manages I/O to devices and files by calling upon device drivers and demultiplexing drivers to handle I/O requests for their devices.

A configurable driver is not linked with the OS. It is linked as if it were a standalone, executable program with an internal driver *function subroutine* which is passed a single *parameter record*. The driver function may in turn call other Pascal or assembler subroutines, including OS subroutines named in a **USES** statement, as required. The value returned by the main driver function is the error code returned by the driver. The parameter record passed to the driver contains a function code that specifies the operation to be performed by the driver. A different variant set of parameters is associated with each *function code* so that each driver operation may have its own set of parameters. (For a skeleton driver written in Pascal, see Figure 6-1.)

The Operating System may invoke the driver to handle a hardware interrupt for its device, an interrupt of an alarm that the driver set, or a Memory Management request to restart an operation begun by the driver. In each case, a specific function code is used to inform the driver which operation it must perform. Figure 1-2 shows these operations on the left and their corresponding driver function codes on the right. An application program may also request an I/O operation such as reading, writing, or mounting a device. The OS passes the application's request to the driver by means of the function codes shown in Figure 1-3. (For a description of the function codes, see Table 3-1.)

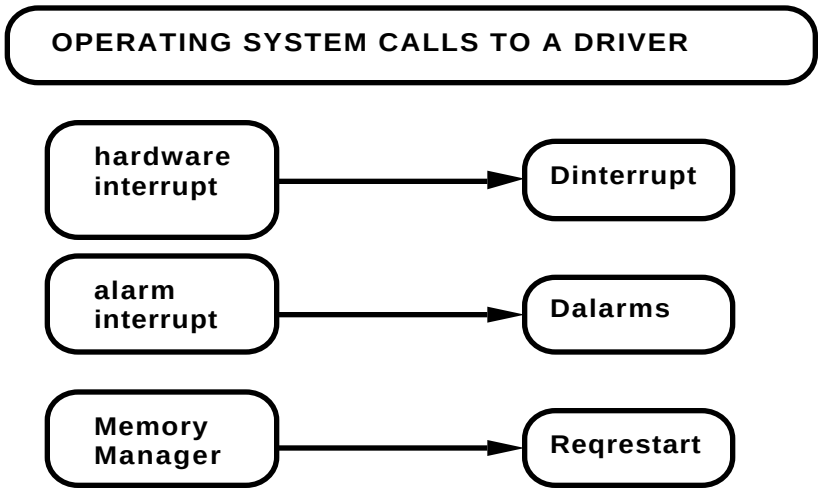


FIGURE 1-2

Figure 1-2. How a Driver is Invoked by the Operating System

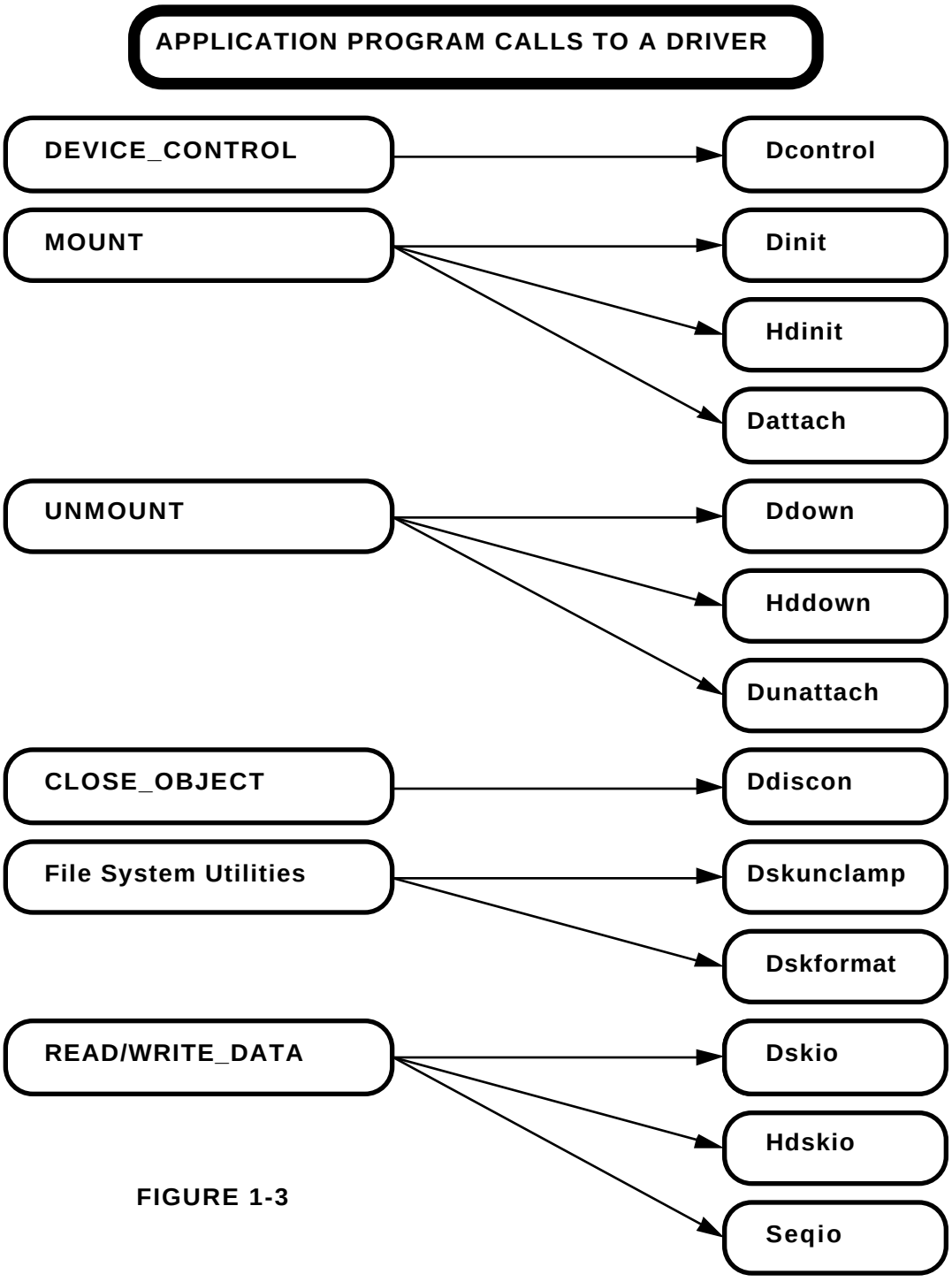


FIGURE 1-3

Figure 1-3. How a Driver is Invoked by an Application Program

1.3 INITIALIZING AND LOADING CONFIGURABLE DRIVERS

Configurable drivers are not usually made resident in memory until they are needed by an application. When a device is mounted, its driver is loaded from disk into memory and the driver's data structures are initialized. However, the driver writer specifies whether the driver for a device should be loaded into memory when the OS is booted (see Chapter 5, Specifying Characteristics for a New Driver).

1.4 COMMUNICATIONS THROUGH THE REQUEST BLOCKS

The Operating System creates an *I/O Request Block* to communicate information about each I/O operation. Every Request Block is linked to both the Process Control Block of the requesting process and to the Device Control Block for the device. A single device can therefore be shared among several processes. In a multitasking operating system like the Lisa's, the process that initiates an I/O transfer may not be executing when the I/O transfer actually occurs; the Request Block provides the driver with information about the I/O operation to be performed. The driver may enqueue the request if its device is busy with a previous request; otherwise it initiates the requested operation immediately. The driver then returns to the OS without waiting for the requested operation to complete.

READ_DATA and **WRITE_DATA** (File System procedures described in the *Operating System Reference Manual for the Lisa*) operate synchronously; that is, they do not return control to the application that calls them until all of the data has been read or written. Instead they call **BLK_REQ** to block the current process until their request has been completed. Thus different processes can overlap requests to the same device (because drivers are written to operate asynchronously), but read and write requests from a single process execute strictly in a synchronous fashion. An application consisting of several processes could achieve asynchronicity by allowing processing to continue in one of the non-blocked processes during each I/O transfer requested by another process.

1.5 COMMUNICATION BETWEEN DRIVERS

Demultiplexing drivers serve as interrupt demultiplexors, while device drivers actually perform I/O transfers. A hierarchy of drivers may exist: a demultiplexing driver may call either a device driver or another demultiplexing driver below it in the hierarchy. The lowest level is always a device driver.

Driver hierarchy is illustrated in Figure 1-4, using the Serial Communications Controller as an example. Interrupts are received by the Serial Communications Controller (SCC) driver at the highest level of the hierarchy. The SCC driver determines whether the interrupt is for the Asynchronous RS232 driver on Port A of the SCC or for the Applebus driver on Port B. RS232 is a sequential device; Applebus is a multi-drop, daisy-chained bus capable of controlling several different types of devices -- in this case, a printer and a disk. The Applebus driver determines which device on the bus is interrupting and invoked the driver for that device.

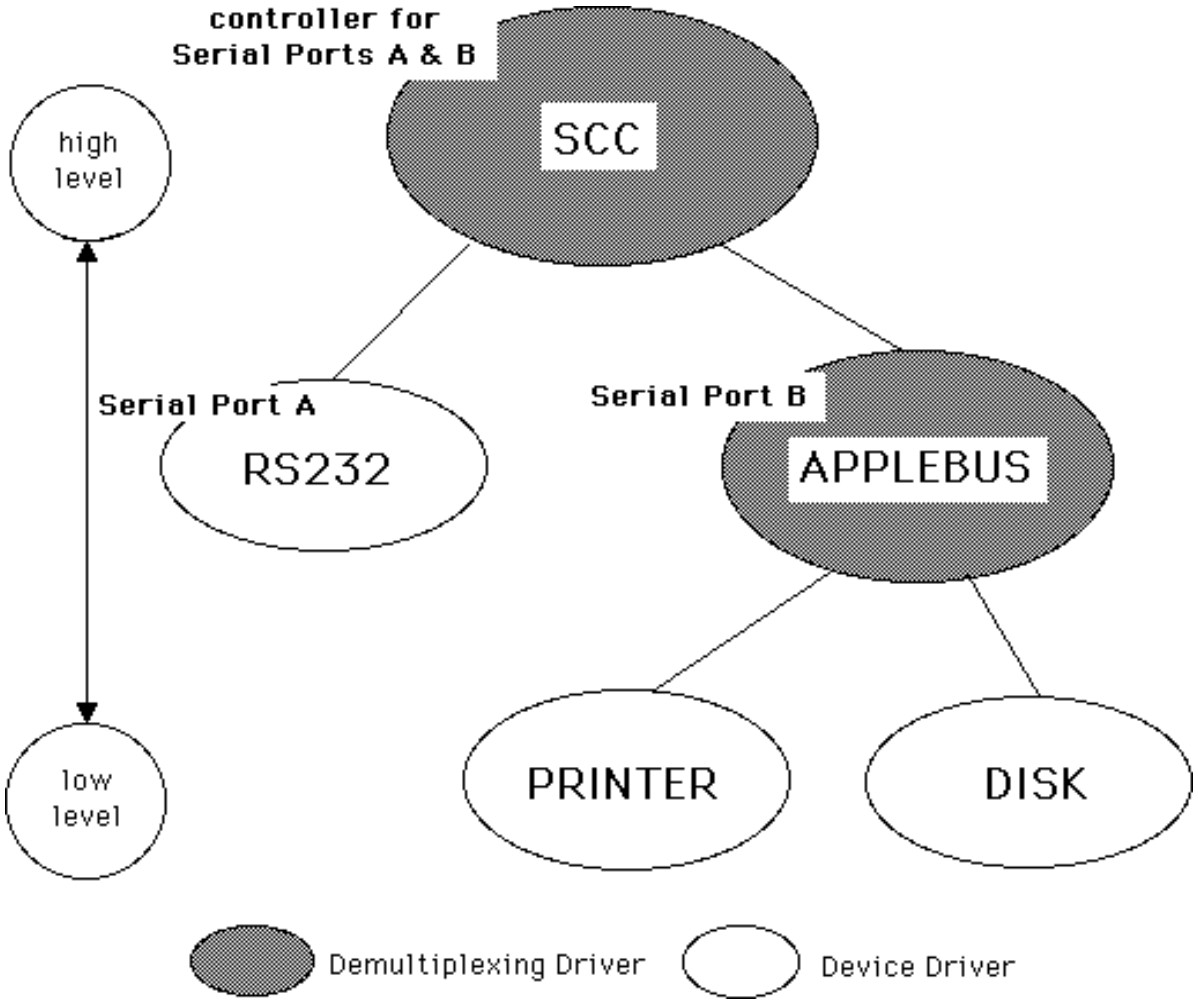


Figure 1-4. Communication between Drivers.

The low-level drivers in this example are the Asynchronous RS232 driver, the printer driver, and the disk driver. Low-level drivers actually transfer data to or from the device. Higher-level drivers correspond to components in the hardware configuration that multiplex interrupts from several devices. Whenever a device driver is loaded into memory, the OS attaches it to each higher driver associated with it so that the higher driver can keep track of the devices it is responsible for.

Chapter 2
CALLS FROM A DRIVER TO THE OS

CALLS FROM A DRIVER TO THE OS

2.1 UTILITIES AND STANDARD DATA STRUCTURES

Driversubs is a Pascal unit that enables drivers to access OS utility routines to perform the operations described in this chapter. These routines provide services such as memory management, real-time management, I/O Request Block management, and disk driver support.

Driverdefs is a Pascal unit that provides definitions of data types for use in drivers. All of the Pascal identifiers used in this manual refer to Request Blocks and other standard driver data structures are defined in **Driverdefs**. Each device in the OS has a global *Device Configuration Record*. The Device Configuration Record (**Devrec**) is defined in the **Driverdefs** unit. Each time a driver is called, the address of the appropriate Device Configuration Record is passed to the driver.

The object code for **Driverdefs** and **Driversubs** was supplied with your Lisa Operating System. For a listing of the source code for these units, see Appendix A. For information on how to make these units a part of your driver, see Chapter 6.

Figures 2-1 through 2-3 show the relationship between the data structures used by a driver. (Figure 2-1 applies to sequential device drivers; Figure 2-2 applies to disk device drivers; Figure 2-3 applies to demultiplexing drivers and non-File System drivers.) All of the figures follow the same format. The center column shows the Device Configuration Record (and, for disk drivers, its extension). This record, one per device, is built by the Operating System during startup. The right-hand column shows the Device Control Block, which is allocated by the driver during its initialization, either at startup or when the device is mounted. You design the format of the Device Control Block to meet the needs of the particular device your driver supports. The left-hand column shows the data structures that are allocated during processing when a request is made for a particular operation. The Parameter Record contains the driver function code (described in Chapter 3) and other information applicable to the specific function to be performed. The name of each data structure is shown in capital letters above its symbol. Below it in parentheses is the name by which the data structure is known in **Driverdefs**. Each black rectangle represents a pointer field; the arrow shows the data structure to which it points. The **Driverdefs** name of the pointer field is shown to the left of the rectangle.

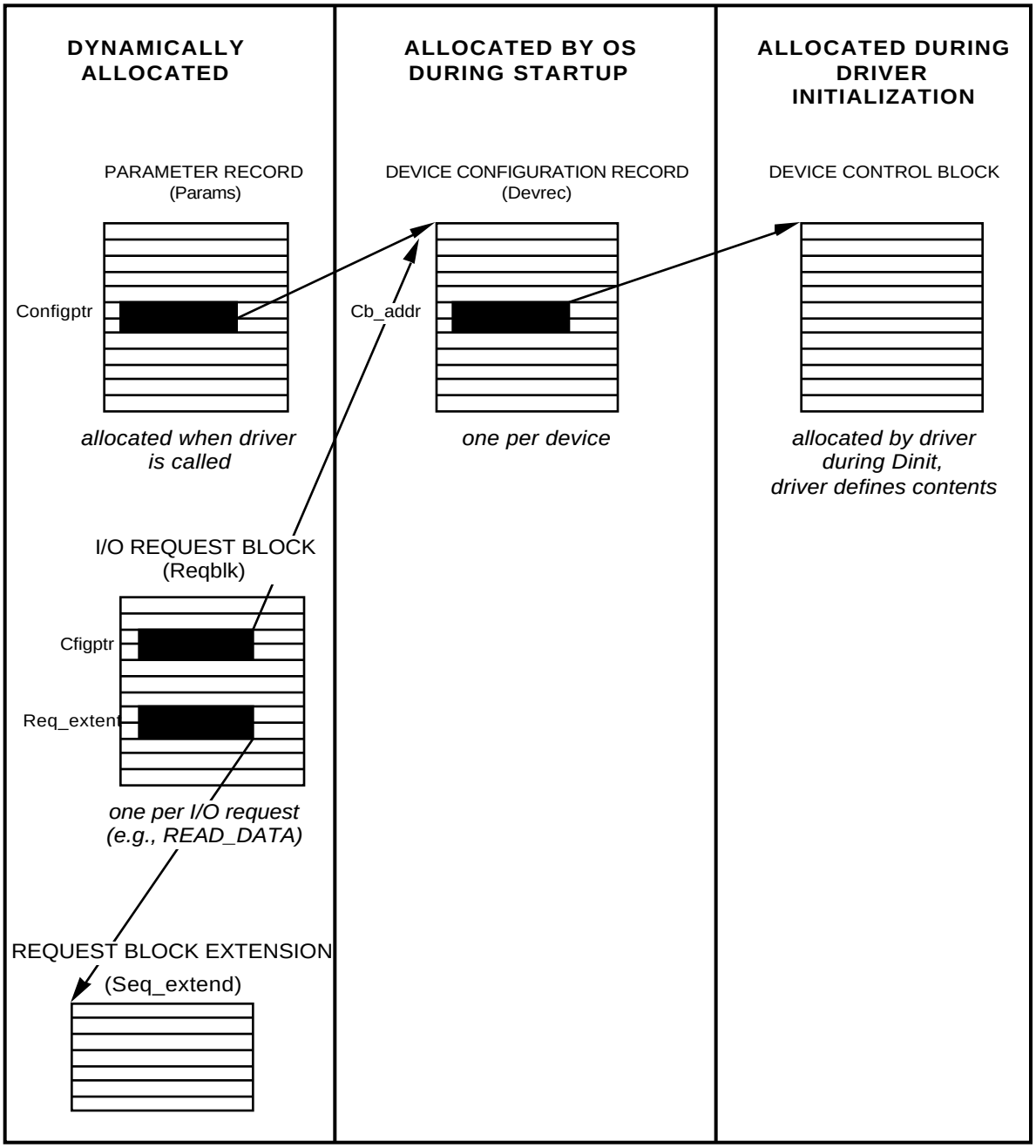


FIGURE 2-1

Figure 2-1. Data Structures for Sequential Device Drivers.

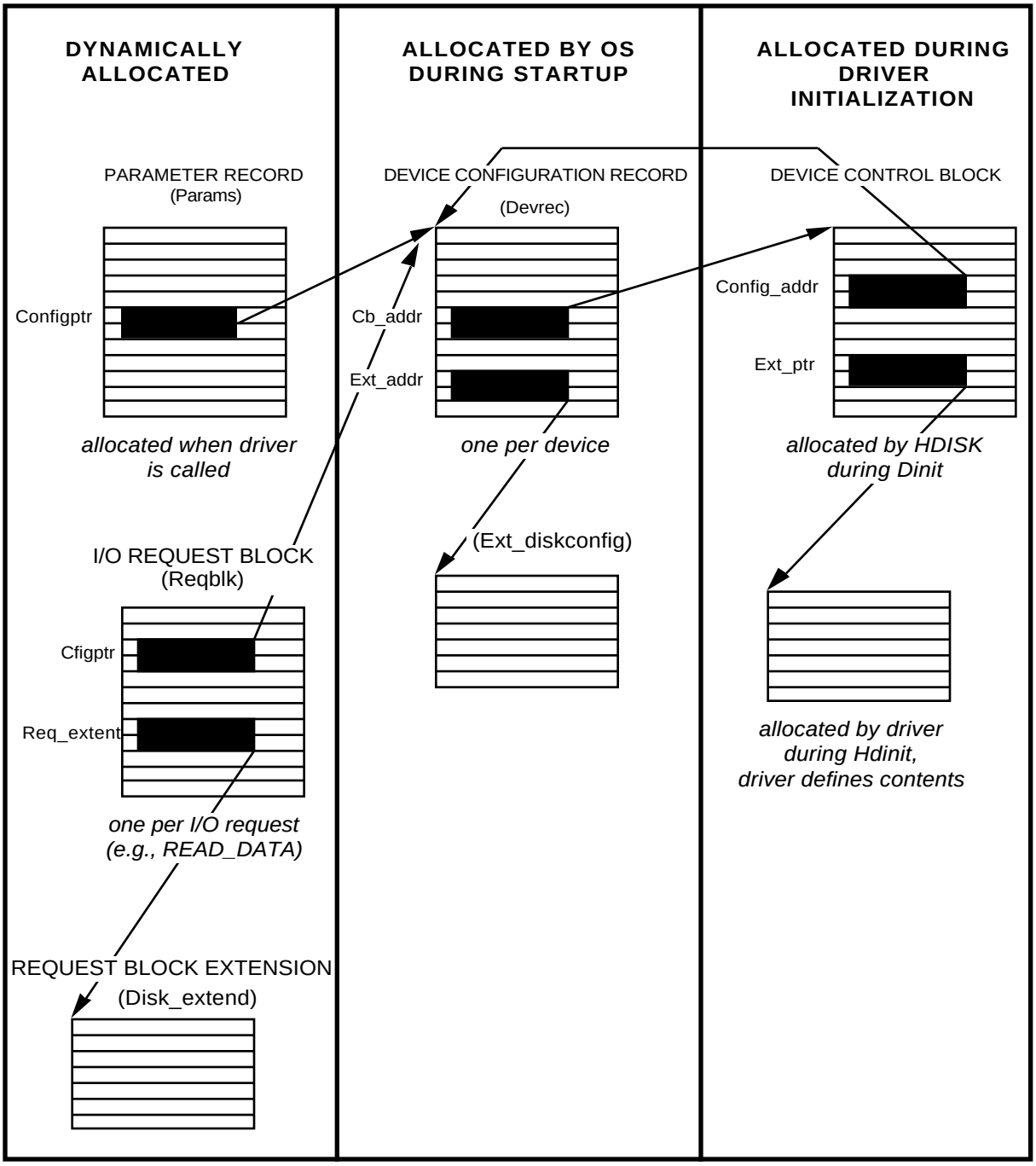


FIGURE 2-2

Figure 2-2. Data Structures for Disk Device Drivers.

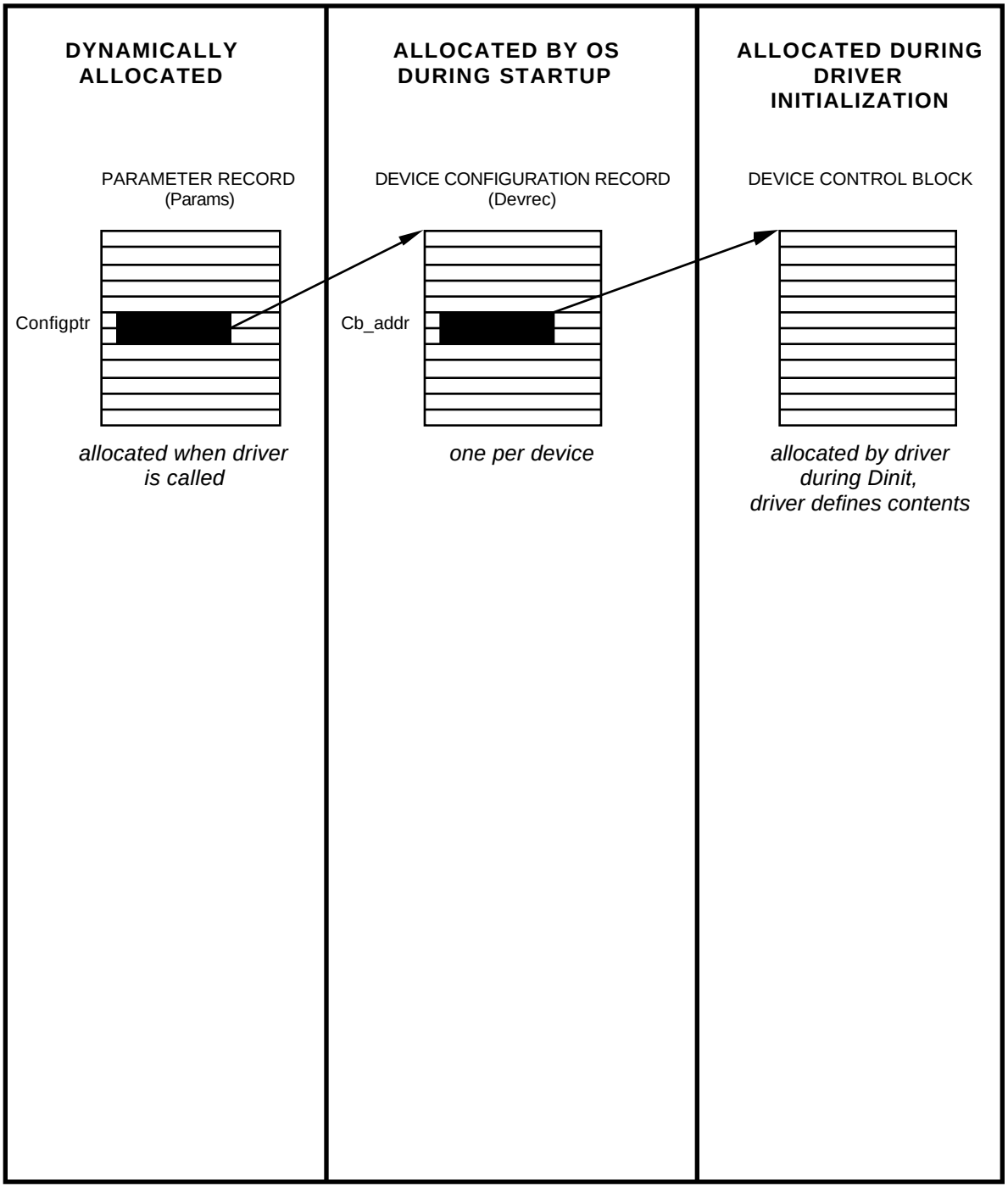


FIGURE 2-3

Figure 2-3. Data Structures for Demultiplexing and non-File System Drivers.

2.2 MEMORY MANAGEMENT

A driver is not linked with the OS, although it must execute as though it were a part of the OS. Therefore a driver cannot contain any statically allocated global variables. Instead the driver must acquire global data space from the OS Memory Manager to hold any variables it needs to keep track of its device. The Device Configuration Record is acquired by the OS during system startup. However, the Device Control Block for the device must be acquired and filled in by the driver when it is invoked with the Dinit function code.

Memory management support routines are provided to ensure that user data buffers will be accessible by the driver when an interrupt occurs.

2.2.1 Global Data Space

Each device may acquire memory space from the OS Memory Manager. One thousand bytes per device is an approximate upper limit on the amount of global data space a driver may acquire.

GETSPACE and **RELSPACE** are for dynamically acquiring and releasing OS global memory space. Drivers typically use **GETSPACE** during driver initialization (**Dinit** function code) and **RELSPACE** during driver resource deallocation (**Ddown** function code).

2.2.1.1 GETSPACE Function

function GETSPACE (Amount:int2; B_area:absptr; var Ordaddr:absptr): boolean

Input Parameters:

Amount: Number of bytes of free space required
B_area: Base address of the free space pool data area.

Output Parameters:

GETSPACE: True if space was allocated;
False if sufficient space was not available.
Ordaddr: Address of the dynamic space allocated.

Dynamic space is allocated from a free space pool. **B_area** must contain a pointer to the free space pool header. The value for **B_area** is provided by the constant **Bsysglob**, defined in **Driverdefs**. To point to the pool, the following lines of code are recommended:

```
var
    ptrsysg: ^absptr;
...
ptrsysg := pointer(Bsysglob);
GETSPACE(Amount, ptrsysg^, Ordaddr);
...
```

Return with *error 610* if **GETSPACE** returns false.

2.2.1.2 RELSPACE Procedure

procedure RELSPACE (Ordaddr: absptr; B_area:absptr)

Input Parameters:

Ordaddr: Address of the dynamic space allocated.
B-area: Base address of the free space pool data area.

Ordaddr contains the address that was provided by the **GETSPACE** function. The dynamic area at **Ordaddr** will be released back into the free space pool maintained in the data area defined by the base address **B_area**. (Refer to the

sample program fragment in the GETSPACE Procedure, Section 2.2.2.1.)

2.2.2 User Data Buffers

User data buffers are located in data segments managed by the OS Memory Manager. *Memory mapping* is an addressing function provided by the MMU (Memory Management Unit) hardware. The mapped address for a data segment never changes during execution of its process; however, the underlying unmapped address (that is, its real location in memory), changes as the OS Memory Manager manipulates the location of the data segment during memory compaction operations.

After a process requests I/O, its data buffers may not be mapped by the hardware MMU at the time of the interrupt. The routines in this section are provided to guarantee that when an interrupt occurs, the driver can locate the memory address for reading or writing data.

Disk drivers do not call these routines because **HDISK** calls them on behalf of the driver.

FREEZE_SEG prevents the data buffer from being moved by the Memory Manager during memory compaction; **UNFREEZE_SEG** releases the data buffer data to normal memory management. **ADJ_IO_CNT** provides a means of keeping track of the number of pending I/O requests for a given data segment.

2.2.2.1 FREEZE_SEG Procedure

procedure FREEZE_SEG (var Errnum:int2; C_sdb:absptr; Offset:int4; Ioreq_addr:absptr; var Buffaddr:absptr);

Input Parameters:

C_sdb: Data segment to be frozen.
Offset: Supplied by a prior call to CVT_BUFF_ADDR.
Ioreq_addr: Pointer to the Request Block for the data segment.

Output Parameters:

Errnum: 0 = successful freeze;
nonzero = data segment in motion, unable to freeze.
Buffaddr: Offset converted to a valid address.

The **FREEZE_SEG** procedure freezes the data segment in memory. The freeze count of data segment **C_sdb** is increased by one each time **FREEZE_SEG** is called. Once frozen, the data segment cannot be moved by the OS Memory Manager until it is unfrozen. When the data segment is frozen, **Offset** is converted to an address that can be used by the driver.

If the Memory Manager is in the process of moving the data segment at the time **FREEZE_SEG** is called, the freeze does not take place. The **Ioreq_addr** pointer is saved by the Memory Manager so that it can be returned to the driver after the data segment has been moved; the Memory Manager calls your driver with the **Reqrestart** function code after it has finished moving the data segment.

C_sdb and **Offset** must be the **Ordsdb** and **Offset** values returned by the **CVT_BUFF_ADDR** procedure. If the driver was invoked with function code **Dskio**, **Seqio**, or **Hdskio** (that is, by **READ_DATA** or **WRITE_DATA**), **CVT_BUFF_ADDR** has already been called and the values it returned were saved for you in the Request Block variables **Buff_rdb_ptr** and **Buff_offset**. If the driver was invoked with function code **Dcontrol** (that is, by a driver-initiated request for I/O from or to a user data buffer), you must call **CVT_BUFF_ADDR** yourself before calling **FREEZE_SEG**.

2.2.2. UNFREEZE_SEG Procedure

procedure UNFREEZE_SEG (c_sdb:absptr)

Input Parameters:
c_sdb: Pointer to the data segment to be unfrozen.

The freeze count of data segment **C_sdb** is decreased by one each time **UNFREEZE_SEG** is called. When the freeze count reaches zero, the data segment is no longer frozen and may be moved by the Memory Manager.

2.2.2.3 ADJ_IO_CNT Procedure
procedure ADJ_IO_CNT (Inc_io_cntF: boolean; C_sdb:absptr)

Input Parameters:
Inc_io_cntF: True if the count is to be incremented.
False if the count is to be decreased.
C_sdb: Data segment to be adjusted.

ADJ_IO_CNT adjusts the count of pending I/O requests for the data segment upward or downward depending on the value of **Inc_io_cntF**. When the I/O count reaches zero, the Memory Manager is permitted to swap the data segment to disk if necessary.

ADJ_IO_CNT is called by driver support routines in the Operating System to increase the I/O count for a device before **Seqio** or **Dskio** is called. To decrease the count, **ADJ_IO_CNT** must be called by your driver (**HDISK** calls it for disk drivers) at the time the I/O request is completed.

2.3 REAL-TIME MANAGEMENT
This section discusses critical timing considerations, interrupt handling and interrupt masking, a timer for timing driver code, and alarms for lengthy interrupt processing.

2.3.1 The Microsecond Timer
To help you determine how much time your driver spends in critical sections of code, a *microsecond timer* is available. The microsecond timer simulates a continuously running 32-bit counter that is incremented every microsecond. The timer changes sign about once every 35 minutes, and rolls over every 70 minutes. It is initialized to zero when the Lisa is booted.

2.3.1.1 MICROTIMER Function
function MICROTIMER: longint

Output Parameters:
MICROTIMER: Current value of the microsecond timer.

The microsecond timer is designed for performance measurements. It has a resolution of 2 microseconds. Calling **MICROTIMER** from Pascal takes about 135 microseconds. Note that interrupt processing will have a major effect on microsecond timings if **MICROTIMER** is called from within a routine that has interrupts enabled.

2.3.2 ALARMS
Twenty independent *alarms* are shared by all drivers. Alarms can be dynamically acquired by drivers and should be returned when no longer needed. For line protocol timeouts, an alarm can be set to "wake up" the driver after a specified interval of time. The alarms have a 10-millisecond resolution; that is, if you set an alarm to go off with a delay of 500 milliseconds, the actual delay will be between 490 and 510 milliseconds.

The routines associated with alarms are

Get an alarm -- **ALARM_ASSIGN**
Set an alarm -- **ALARMRELATIVE**
Turn off an alarm -- **ALARMOFF**
Return an alarm -- **ALARMRETURN**

After obtaining an alarm by calling **ALARM_ASSIGN**, the driver sets the alarm to a specified number of milliseconds in the future. When the millisecond timer reaches the alarm's setting, the driver's alarm handler is called by means of the **Dalarms** function code.

Interrupts at the priority levels specified by **ALARM_ASSIGN** are disabled during execution of the alarm handler.

2.3.2.1 ALARM_ASSIGN Procedure

procedure ALARM_ASSIGN (var Alarm:integer; Pdr:Ptrdevrec; Status:intson_type)

Input Parameters:
Pdr: Pointer to the Device Configuration Record associated with this alarm.
Status: Level of interrupts enabled in alarm handler.

Output Parameters:
Alarm: Number of the assigned alarm; zero if none available.

ALARM_ASSIGN returns an alarm number in **Alarm**. If an alarm is not available, **Alarm** is zero; your driver should then return with error 602.

The driver should associate the alarm with the device specified in **Pdr** by saving the alarm number returned in **Alarm** in the Device Control Block.

Interrupts from your device should be disabled while your alarm handler is executing in order to prevent data in the Device Control Block from being inconsistently modified. Status is the interrupt priority you want to be in effect when the alarm goes off. You should set **Status** to the lowest value that will disable interrupts for your device. (See the **INTSOFF** procedure, Section 2.3.3.1, for more information on possible values of **Status**.) When the alarm goes off, the driver is called using the **Dalarms** function code with the interrupt priority set to **Status**.

2.3.2.2 ALARMRELATIVE Procedure

procedure ALARMRELATIVE (Alarm:integer; Delay:longint)

Input Parameters:
Alarm: Number of the alarm to be set.
Delay: Milliseconds to wait before the alarm goes off.

ALARMRELATIVE sets an alarm to go off **Delay** milliseconds in the future. When **Delay** milliseconds have elapsed, the driver is called with the **Dalarms** function code.

If **Delay** is zero or negative, the alarm goes off immediately (at the next system alarm interrupt).

Each alarm remembers only one setting. To reset an alarm call **ALARMRELATIVE** again; this causes the previous setting to be disregarded.

2.3.2.3 ALARMOFF Procedure

procedure ALARMOFF (Alarm:integer)

Input Parameters:
Alarm: Number of the alarm to be turned off.

ALARMOFF turns off an alarm that was previously set by **ALARMRELATIVE**. That is, **ALARMOFF** can be used to cancel a request for an alarm interrupt.

2.3.2.4 ALARMRETURN Procedure

procedure AlarmReturn (Alarm:integer)

Input Parameters:
Alarm: Number of the alarm to be returned to the pool.

The **ALARMRETURN** procedure returns an alarm acquired by **ALARM_ASSIGN** to the pool of available alarms; another driver can then use it.

Alarms should be returned when they are no longer needed by your driver. This happens when the driver is called with the **Ddown** function code.

2.3.3 Enabling and Disabling Interrupts

Device Control Block variables may be in use by two or more driver functions at a time, potentially leaving the Device Control Block in an inconsistent state. To prevent this, interruptible driver functions must disable interrupts for a short period of time while updating the Device Control Block. The **INTSOFF** procedure allows you to disable some or all interrupts; the **INTSON** procedure re-enables them.

The interrupt priority mask determines which interrupts will be acknowledged by the processor. Any interrupt with a priority higher than the mask is acknowledged; any interrupt with a priority less than or equal to the mask is inhibited. (for a more detailed description of the interrupt priority mask, see the MC68000 user's manual.)

On the Lisa, the interrupt priorities are as shown in Table 2-1.

Table 2-1
Hardware Interrupt Priorities

Priority	Hardware
1	system clock, built-in floppy disk, built-in parallel port
2	keyboard, mouse, time-of-day clock/calendar
3	slot 3
4	slot 2
5	slot 1
6	built-in serial ports A and B
7	memory parity error, programmable NMI (nonmaskable interrupt) key

2.3.3.1 INTSOFF Procedure

procedure INTSOFF (Level:intsoff_type, var Status:intson_type)

Input Parameters:
Level: New interrupt priority

Output Parameters:
Status: Old interrupt priority

INTSOFF allows you to temporarily turn off some or all interrupts by specifying a new value in **Level**. Values for **Level** are

clokints, winints, or twigints (priority = 1)

slotints	(priority = 5)
rsints	(priority = 6)
allints	(priority = 7)

Before **INTSOFF** changes the interrupt priority, it saves the current priority; this is returned to you in **Status** and should be used in the **INTSON** procedure to reset the priority level to its original value.

2.3.1 INTSON Procedure
procedure INTSON (Status: intson_type)

Input Parameters:
Status: Interrupt level to be restored.

The **INTSON** procedure enables interrupts that were disabled by a previous call to **INTSOFF**. Status contains the original interrupt priority level in effect at the time **INTSOFF** was called.

2.3.4 Lengthy Interrupt Processing
An interrupt that takes more than a few milliseconds should process with all other interrupts enabled. The interrupt handler for you device can accomplish this by calling an alarm routine to perform the extended interrupt processing. The alarm mechanism is merely a convenient vehicle for scheduling a lengthy interrupt that can execute with interrupts enabled for all devices.

When your interrupt handler determines that extended processing is required, it call **ALARM_ASSIGN** to associate an alarm with the lengthy interrupt routine, specifying an interrupt level of **clkonints** to enable all interrupts during execution of the routine. The interrupt handler then calls **ALARMRELATIVE** with zero millisecond delay, so that the alarm routine will begin execution as soon as possible. The actual delay before starting will always be less than twenty milliseconds and will average about five milliseconds.

In the 68000 architecture, a device's interrupt handler (its **Dinterrupt** routine) executes with interrupts disabled for all devices of same or lower interrupt priority. The body of the lengthy-interrupt alarm routine (**Dalarms**) will execute with all interrupts enabled. Therefore it is necessary to protect the Device Control Block from simultaneous access when the same device tries to interrupt during the extended processing of a prior interrupt.

Be sending an appropriate signal to the device, the device's interrupt handler can disable (and hold pending) any further interrupts for the interrupting device until the alarm routine has completed its processing. For example, if there is an interrupt from the 6522 parallel port that requires extended processing, the interrupt handler can set a bit in the 6522's Interrupt Enable Register (IER) to zero. This prevents further interrupts from the device. When the alarm routine completes its extended processing alarm routine, the IER must be restored to the required non-zero value so that interrupts may occur again. For the serial ports, Write-Register-1 on the SCC serves the same function as the IER on the 6522. The disadvantage of this technique is that it increases the interrupt response latency on the device requiring extended processing. Depending upon the device, this may or may not be a problem.

Some devices require very short interrupt response latency (less than 70 microseconds) or long interrupt execution times (more than 10 milliseconds). Such devices (for example, laser printers and some serial communications protocols at high baud rates) require all of the processing power of the 68000 for extended periods of time. Such devices are not really suitable for use in the multi-tasking Lisa environment. Unless these devices are provided with intelligent controllers that don't force an interrupt on the transfer of each byte, the Lisa is unavailable for any other tasks while it is supporting these devices.

2.4 I/O REQUEST BLOCK MANAGEMENT

As part of its I/O operations, your driver uses and updates fields within a Request Block. When it is passed to the driver, the Request Block (and, in some cases, its extension) already contains information about the I/O request. The driver is responsible for updating the transfer count and other fields in the Request Block.

2.4.1 Request Block Initialization

When a driver is invoked with function code of **Seqio** or **Dskio**, a pre-initialized Request Block is passed to the driver. All the information needed to initialize the I/O request is passed to the driver in the Request Block. The information in the Request Block is initialized as follows:

pcb_chain: **doubly-linked list to Process Control Block (PCB)**

reqstatus
reqsrvv_f: **active**
reqabt_f: **false**

block_p_f: **false**

blk_in_pcb: **[i_o]**

hard_error: **0**

operatn: **0 for write; 1 for read**

cfigptr: **pointer to device's configuration table entry**

req_extent: **address of Request Block extension**

All Request Block extensions are initialized as follows:

read_flag: **true if read, false if write**

xfer_count: **0**

buff_rdb_ptr and
buff_offset: **result of calling CVT_BUFF_ADDR on data buffer**

For extensions used by Seqio only, the following field is initialized:

num_bytes: **desired length of transfer**

For extensions used by Dskio only, the following fields are initialized:

blkno: block number to begin read or write

soft_headr: initial value for 24-byte file system "pagelabel"

last_fwd_link: fwd_link value in pagelabel of final block

last_data_used: data-used value in pagelabel of final block

num_chunks: number of blocks (512 bytes, plus header) to transfer

io_mode: with_header...write using headers from soft_headr

without_header...doesn't transfer headers

raw_io...memory filled with all the headers, followed by all the data

chained_hdrs...read using headers and verify against contents of soft_headr

2.4.2 Request Block Updating

When the driver starts up an I/O request, the following field should be changed in the Request Block:

reqstatus
reqsrv_f: in_service

When a request for I/O has been completed or an error has occurred, your driver must update the Request Block and then call UNBLK_REQ to notify the OS that your driver is finished with the request. The error field in the Request Block must be set:

hard_error: =0 for no errors, <0 for warnings, >0 for errors
(warnings and errors, pass False to UNBLK_REQ)

The driver must update the transfer count field in the Request Block extension:
xfer_count: number of bytes transferred (not including bytes in disk block headers)

For Dskio only, the driver must update the following field in the Request Block extension:

soft_headr: the last header read when using "chained_hdrs" io_mode

2.4.2.1 UNBLK_REQ

procedure UNBLK_REQ (Reqptr:reqptr_type; Success_f:boolean)

Input Parameters:
Reqptr:Pointer to the request that is completed.
Success_f: True if the request was successfully completed;
False if Hard_error contains non-zero value.

UNBLK_REQ unblocks the process that is waiting for the request specified by **Reqptr**. **Success_f** is a flag that informs the process of the result of the I/O request.

For disk drivers the **HDISK** unit calls **UNBLK_REQ** on behalf of the driver at the end of a read or write. Other drivers must call **UNBLK_REQ** themselves.

2.4.3 Request Block Queues

A queue is a linked list, or chain. Two types of linked lists are used for Request Blocks: the first is a chain of all current Request Blocks for a particular process; the second is a chain of all Request Blocks waiting to perform I/O on a single

peripheral device. Figure 2-1 illustrates the two types of Request Block queues.

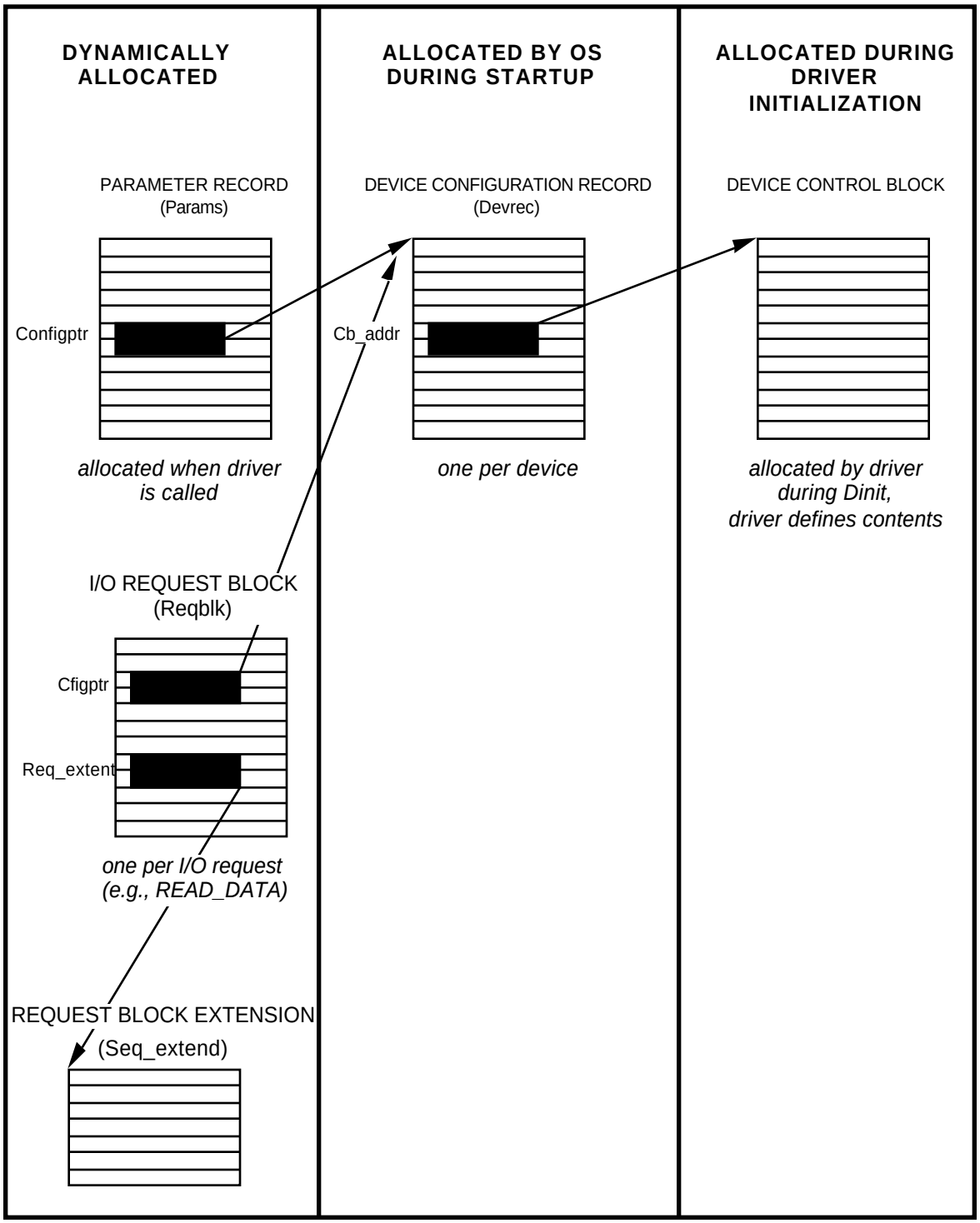


FIGURE 2-1

Figure 2-1

The two types of Request Block queues are circular (the end of the queue points back to the beginning) and doubly-linked (each Request Block points backward to the previous Request Block in the chain and forward to the next one). Figure 2-2 shows how a linkage record is inserted in a circular queue. The forward and backward chaining of Request Blocks is accomplished by means of the linkage data type. Linkages are not full addresses but rather offsets within the global data area.

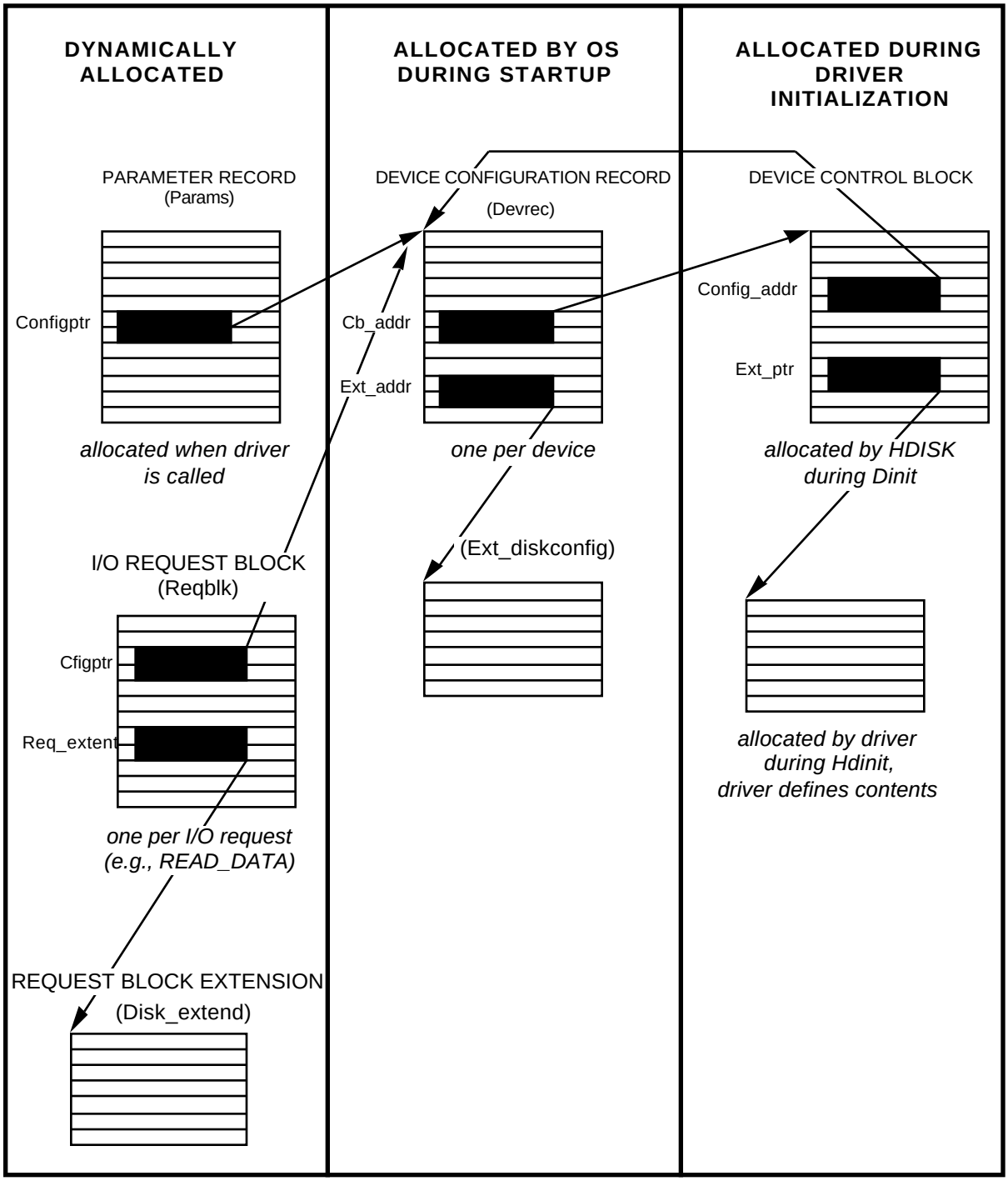


FIGURE 2-2

Figure 2-2

Request Block queues for disk **READ_DATA** and **WRITE_DATA** requests are handled by **HDISK**, a unit in the

Operating System. For all other kinds of I/O, your driver must maintain these queues. Three procedures and functions are available to help you:

- o **ENQUEUE** inserts linkages in a Request Block queue.
- o **DEQUEUE** removes linkages from a queue.
- o **CHAIN_FORWARD** points to the next Request Block in the queue.

2.4.3.1 ENQUEUE Procedure

procedure ENQUEUE (var Newlink, Leftlink:linkage; B_sysarea:absptr)

Input Parameters:

Newlink: Linkage record of the new control block.
Leftlink: Linkage record of previous block in chain.
B_sysarea: Address ordinal of the data area base.

ENQUEUE places **Newlink** next in a chain headed by **Leftlink**. **Newlink** is the linkage record of the Request Block to be added to the chain. **Leftlink** is the linkage record of the Request Block in the chain after which **Newlink** is to be added. The linkage values are relative pointers to the linkage record. **ENQUEUE** assumes that the queue has been initialized as a circular chain and that the caller has exclusive access while the chain is being modified.

B_sysarea must contain the address of the free space pool header; refer to the **GETSPACE** Function, Section 2.2.2.1, for more information.

2.4.3.2 DEQUEUE Procedure

procedure DEQUEUE (var Link:linkage; B_sysarea:absptr)

Input Parameters:

Link: Linkage record to be removed from the queue.
B_sysarea: Address ordinal of the data area base.

DEQUEUE removes a Request Block from a queue. **DEQUEUE** assumes that the queue has been initialized as a circular chain and that the caller has exclusive access while the chain is being modified.

B_sysarea must contain the address of the free space pool header; refer to the **GETSPACE** Function, Section 2.2.2.1, for more information.

2.4.3.3 CHAIN_FORWARD Function

function CHAIN_FORWARD (Current:linkage):reqptr_type

Input Parameters:

Current: Linkage record of old Request Block.

Output Parameters:

CHAIN_FORWARD: Pointer to new Request Block.

CHAIN_FORWARD uses the forward link of the linkage record to point to the next Request Block in the queue so that your driver can initiate the next I/O request for the device.

For non-disk drivers, the following steps should be performed when the current I/O request is complete:

1. Call **UNBLK_REQ** for the request you just completed.

2. Call **CHAIN_FORWARD** to get the next Request Block in the queue linked to the Device Control Block.
3. Start the next I/O request.

For disk drivers, call **IODONE** to perform these steps.

2.5 DISK DRIVER SUPPORT

The Lisa Operating System provides special utility routines for disk drivers: **USE_HDISK**, **CALL_HDISK**, **IODONE**, and **OKXFERNEXT**. These routines make subroutine calls into the **HDISK** unit (listed in Appendix A). Only disk drivers may use these routines.

- **USE_HDISK** notifies the OS that your driver wants to use **CALL_HDISK**. Your disk driver must call **USE_HDISK** once, before calling **CALL_HDISK**, each time the driver is initialized (**Dinit** function code).

- **CALL_HDISK** performs the functions associated with the **Dinit**, **Ddown**, **Dskio**, and **Reqrestart** function codes. Whenever you disk driver gets called with function code, it should call **CALL_HDISK** to perform these functions.

  CALL_HDISK may be used only with a disk device with Lisa-format sectors. These are 512-byte sectors preceded by a sector header in pagelabel format (see the pagelabel format record in **Driverdefs**, listed in Appendix A).

- **IODONE** unblocks the current completed I/O request, takes the next request from the device's Request Block queue, and initiates it.

- **OKXFERNEXT** updates and verifies the sector header for each block transferred to or from the disk. Your driver calls it after transferring each 512-byte block.

2.5.1 USE_HDISK Procedure

procedure USE_HDISK (Configptr:ptrdevrec)

Input Parameters:

Configptr: Points to Device Configuration Record for the hard disk device.

USE_HDISK does initialization for a disk driver. The disk device must use Lisa-format sectors. Your disk driver should call **USE_HDISK** during the driver's **Dinit** routine.

2.5.2 CALL_HDISK Procedure

procedure CALL_HDISK (var Error:integer; Configptr:ptrdevrec; Parameters:param_ptr)

Input Parameters:

Configptr: Points to Device Configuration Record.
Parameters: Parameter list that was passed to the driver.

Output Parameters:

Error: Indicates result of CALL_HDISK operation.

CALL_HDISK calls the **HDISKIO** routine in the **HDISK** unit to support the driver's **Dinit**, **Ddown**, **Dskio**, and **Reqrestart** functions. The driver must take the **Error** value returned by **CALL_HDISK** and assign it to the driver's function result before returning.

2.5.3 IODONE Procedure
procedure IODONE (Port_ptr:hdskcb_ptr; Prev_err:integer)

Input Parameters:
Port_ptr: Points to Device Control Block.
Prev_err: Contains completion error.

Your driver must call **IODONE** after it finishes servicing an I/O request in order to finish processing the request and initiate the next request.

2.5.4 OKXFERNEXT Function
function OKXFERNEXT (Port_ptr:hdiskcb_ptr):integer

Input Parameters:
Port_ptr: Points to Device Control Block.
Output Parameters:
OKXFERNEXT: Zero if successful, else error code.

OKXFERNEXT updates the File System sector headers and data pointers and tests to see whether another sector can be transferred. If any errors are found in the sector just transferred, the transfer is terminated and **OKXFERNEXT** returns a nonzero error code. If a new transfer can be initiated, **OKXFERNEXT** returns zero.

2.6 DRIVER-INITIATED I/O REQUESTS

Your driver may not be able to complete a requested operation immediately because it takes time for the device to perform the operation. This is true when function codes **Dinit**, **Dcontrol**, and **Dskformat** are invoked and call **BLK_REQ** to wait for I/O to complete. **READ_DATA** and **WRITE_DATA** call the OS to do deferred-request management for the driver, calling **Seqio** or **Dskio** in the driver when needed; in other cases the driver has to perform these operations for itself.

To defer an I/O request, follow this procedure:

1. Call **GETSPACE** to create an I/O Request Block.

2. Call **LINK_TO_PCB** to initialize the Request Block and link it to the Process Control Block of the active process.

3. Call **CVT_BUFF_ADDR** to convert the I/O buffer address (if any) to a format the interrupt handler can use.

4. Call **BLK_REQ** to wait until the I/O request has been serviced.

5. When your request is unblocked by the Scheduler, call **CANCEL_REQ** to unlink the Request Block from the Process Control Block and return its space to the free space pool.

When managing your own Request Blocks, you will need to call **INTSOFF**, **INTSON**, **ENQUEUE**, **UNBLK_REQ**, and possibly **ADJ_IO_CNT**, **FREEZE_SEG**, and **UNFREEZE_SEG**, described in this chapter.

2.6.1 LINK_TO_PCB Procedure
procedure LINK_TO_PCB (Req_ptr:reqptr_type)

Input Parameters:
Req_ptr: Pointer to the new Request Block.

LINK_TO_PCB takes a Request Block created using **GETSPACE**, initializes several standard fields, and links the Request Block to the Process Control Block of the currently active process. (The **Reqspec_info** field is not initialized.)

2.6.2 CVT_BUFF_ADDR Procedure

procedure CVT_BUFF_ADDR (var Errnum:int2; Mem_writeF:boolean; Buffaddr:absptr; Byte_cnt:int4; var Ordsdb:absptr; var Offset:int4)

Input Parameters:
Mem_writeF: True if transfer from device to memory.
Buffaddr: Address to be converted.
Byte_cnt: Number of bytes to transfer to or from memory.

Output Parameters:
Errnum: Zero if successful conversion; else nonzero.
Ordsdb: Pointer to the data segment that includes Buffaddr.
Offset: Relative offset within the data segment.

CVT_BUFF_ADDR takes an address in the application program's address space and breaks it into two parts: a pointer to the data segment descriptor and an offset within the data segment. Interrupt handlers need this form of address to access the user's data when it is not currently mapped by the Memory Management Unit hardware. **CVT_BUFF_ADDR** also verifies that the requested I/O operation is valid for the specified data segment; for instance, you can't pass an address within a program as the destination for a read operation.

2.6.3 BLK_REQ Procedure

procedure BLK_REQ (Reqptr:reqptr_type; var First_reqptr:reqptr_type)

Input Parameters:
Reqptr: Pointer to list of requests to wait for.

Output Parameters:
First_reqptr: Pointer to list of completed results.

When **BLK_REQ** is called, the current process is blocked until any of the requests passed to **BLK_REQ** has completed. (Request completion is signaled when the driver calls **UNBLK_REQ**.)

Reqptr points to a linked list of I/O Request Blocks representing events whose completion the current process must await. **First_reqptr** points to a linked list of completed I/O requests, in the order in which they were completed.

2.6.4 CANCEL_REQ Procedure

procedure CANCEL_REQ (Req:reqptr_type)

Input Parameters:
Req: Pointer to Request Block to cancel.

CANCEL_REQ unlinks the specified Request Block from its Process Control Block and releases its space back to

the free space pool.

2.7 MISCELLANEOUS SUBROUTINES

This section contains information on routines that
- report fatal errors -- **SYSTEM_ERROR**.
- detect availability of the System_Log file -- **LOGGING**.
- log data into the System_Log file -- **LOG**.
- test a byte using a bit mask -- **ALLSET**.
- call a device driver from a demultiplexing driver -- **CALLDRIVER**.
- synchronize access to the parallel port -- **DISKSYNC**.
- detect pressing of disk buttons and switches -- **KEYPUSHED**.

2.7.1 SYSTEM_ERROR Procedure
procedure SYSTEM_ERROR (Errnum:integer)

Input Parameters:
Errnum: Error code provided by the driver.

When a driver encounters an error condition so serious that it cannot continue, it should call **SYSTEM_ERROR**, which uses **Errnum** to pass an error code from the driver to the Operating System; **SYSTEM_ERROR** does not return to the driver. Do not use **SYSTEM_ERROR** if an error code can be returned to the caller.

The value of **Errnum** should be 10,000 plus the driver error number.

2.7.2 LOGGING Function
function LOGGING: boolean

Output Parameters:
LOGGING: True if system logging is operational; else false.

Before logging an entry using the **LOG** procedure, use the **LOGGING** function to determine whether system logging is available. If logging is not available, **LOG** should not be called.

2.7.3 LOG Procedure
procedure LOG (var Errnum:integer; Ptr_arr:absptr)

Input Parameters:
Ptr_arr: Pointer to 12 bytes of data to be logged.

Output Parameters:
Errnum: Error code.

The **LOG** procedure allows you to make a 12-byte entry in the **System.Log** file if logging is active. The procedure may be used to log disk errors. The data to be logged may be in any format.

Before calling **LOG**, you should determine whether logging is available by calling the **LOGGING** function.

2.7.4 ALLSET Function
function ALLSET (Source, Target:int1):boolean

Input Parameters:
Source: Mask bits.
Target: Bits to be tested.

Output Parameters:
ALLSET: True if all mask bits are set in target; else false.

ALLSET determines whether all the bits in **Source** are on (set to one) in **Target**. **Target** is a byte in which each bit has separate significance; for instance, the status register for a device. **Source** is a mask byte. If the integer value of **Target** is 7 (00000111) and the integer value of **Source** is 3 (0000011), **ALLSET** returns true. (If the value of **Source** is zero, **ALLSET** returns true.)

2.7.5 CALLDRIVER Procedure
procedure CALLDRIVER (var Errnum:integer; Config_ptr:ptrdevrec; Parameters:param_ptr)

Input Parameters:
Config_ptr: Pointer to the Device Configuration Record for the device.
Parameters: Variant record containing driver parameters.

Output Parameters:
Errnum: Depends upon Parameters.

CALLDRIVER is used by a demultiplexing driver to call a lower-level driver with an interrupt intended for its device. The device driver must previously have been associated with the demultiplexing driver by means of the **Dattach** function code.

2.7.6 DISKSYNC Procedure
procedure DISKSYNC (Busy:boolean)

Input Parameters:
Busy: True if built-in parallel port is busy; False if port is free.

The built-in parallel port (Hard Disk VIA) is shared by the disk driver, printer driver, and video contrast control routines. All device drivers using this port must synchronize their access to it by calling **DISKSYNC** before and after each use of the port. When the port is free, the contrast control routines are able to modify and then restore the registers in the 6522 VIA.

HDISK calls **DISKSYNC** for disk drivers performing reads and writes. However, when a disk driver is formatting or initializing the built-in parallel port, it must call **DISKSYNC** itself.

To gain control of the built-in parallel port, call **DISKSYNC** with **Busy** set to true before doing I/O; when the I/O has completed, call **DISKSYNC** again with **Busy** set to false. (The port is initially free.)

It is not necessary to call **DISKSYNC** when accessing a parallel port in one of the Lisa's three I/O expansion slots. Only the built-in parallel port needs this synchronization.

2.7.7 KEYPUSHED Procedure
procedure KEYPUSHED (Key:0..127)

Input Parameters:

Key: Keycode of the pseudo-key that was pressed.

The disk insertion switches, the disk eject buttons, and the power button -- collectively known as pseudo-keys -- are not detected by the normal keyboard driver. Drivers that detect these switches and buttons must call the **KEYPUSHED** procedure each time a pseudo-key is pressed. You must identify the pseudo-key that was pressed by setting Key to one of the keycode values shown below:

Keycode	Meaning
1	disk was inserted in disk 1
2	disk 1 eject button was pressed
3	disk was inserted in disk 2
4	disk 2 eject button was pressed
11	disk was inserted in built-in Sony drive
12	disk was inserted in slot 1 device
13	disk was inserted in slot 2 device
14	disk was inserted in slot 3 device

Chapter 3
DRIVER FUNCTION CODES

DRIVER FUNCTION CODES

3.1 INVOKING THE DRIVER

A driver is invoked directly by the Operating System or by another driver through the **CALLDRIVER** procedure (described in Section 2.7.5). Every time the driver is invoked, it must perform a specific operation as specified in the *function code* -- the **Fnctn_code** field of the **Params** record that is passed to the driver. This chapter describes the operations that must be performed for each function code.

If your driver receives a function code that it does not recognize, it should return with *error number 656* in its function result.

The **Params** record is passed to the driver no matter which function code is invoked; therefore it is not listed in the documentation for each function code. The parameters that are shown in curly brackets under the function code are those that are passed to the driver in addition to **Params**.

The format of the **Params** record differs depending on the function code; it always contains a pointer to the *Device Configuration Record* for the device as well as the function code itself.

For device-independent applications that call **READ_DATA** and **WRITE_DATA**, standard driver function codes are provided. For nonstandard devices (those that cannot be supported through the File System) or nonstandard applications, a mechanism is provided for the application to communicate all I/O requests directly to the driver using **DEVICE_CONTROL**. In this case the driver must respond to the **Dcontrol** function code and must perform its own Request Block management. Standard drivers may on occasion call **DEVICE_CONTROL** to perform functions such as setting baud rate or acquiring status information.

To improve performance, routines that process driver function codes (for example, **Dinterrupt**, which may require fast execution) may be written in assembler language rather than Pascal.

When Apple integrates networks into the Lisa Operating System, new function codes may be added for handling network-specific functions.

3.2 DRIVER OPERATIONS

There are certain operations that are standard for each driver type: sequential, disk, demultiplexing, and non-File System. Requests for these operations are signaled to the driver by means of the driver function code. Errors the driver encounters while processing a request are reported in the driver function result.

3.2.1 Driver Types and their Function Codes

Table 3-1 is a list of driver function codes and the operations they represent by type.

Table 3-1
Driver Operations

FUNCTION CODES SHARED BY DIFFERENT DRIVER TYPES:

- Dinterrupt.....Handle a device interrupt
- Dinit.....Initialize a driver and its associated device
- Ddown.....Shut down a driver and its associated device
- Dcontrol.....Send or receive device-dependent information
- Reqrestart.....Restart an I/O request

Dalarms.....Handle an alarm interrupt

FUNCTION CODES FOR SEQUENTIAL DEVICE DRIVERS:
Seqio.....Input/output to a sequential device
Ddiscon.....Disconnect modem

FUNCTION CODES FOR DISK DEVICE DRIVERS:
Dskio.....Start I/O to a disk device
Dskunclamp.....Eject a disk from the disk device
Dskformat.....Format a disk
Hdinit.....Initialize a disk device driver
Hddown.....Shut down a disk driver and its associated device
Hdskio.....Transfer data to or from a disk device

FUNCTION CODES FOR DEMULTIPLEXING DRIVERS:
Dattach.....Associate a driver with this demultiplexing driver
Dunattach.....Dissociate a driver from this demultiplexing driver

---

A driver needs to handle only a subset of the standard driver operations. Table 3-2 indicates which driver types must handle each function code. The table also contains information about privilege state, interrupt disabling, and error code requirements for each function code.

Table 3-2
Characteristics of Driver Operations

(1) FUNCTION CODE	(2) PRIVILEGE STATE	(3) DISABLE INTERRUPTS	(4) ERR CODE REQUIRED	SEQ	(5) DRIVER TYPE DISK	DEMX	NON-FS
Dinterrupt	Supervisor	No	No	R	R	R	O
Dinit	User	Yes	Yes	R	R	R	R
Ddown	User	Yes	Yes	R	H	R	R
Dcontrol	User	Yes	Yes	O	O		R
Reqrestart	User	Yes	No	R	H		O
DalarmsSupervisor	No	No	O	O		O	
Seqio	User	Yes	Yes	R			
Ddiscon	User	Yes	No	O			O
Dskio	User	Yes	Yes		H		
Dskunclamp	User	Yes	Yes		O		
Dskformat	User	Yes	Yes		O		
Hdinit	User	No	Yes		R		
Hddown	User	No	Yes		R		
Hdskio	Either	No	Yes		R		
DattachUser	Yes	Yes			R		
Dunattach	User	Yes	No			R	

Column (5): H=HDISK, O=Optional, R=Required

The columns of Table 3-2 contain the following information:

(1) FUNCTION CODE is the identifier assigned to this operation in **Driverdefs** (see Appendix A). The function codes

are summarized in Table 3-1 and documented in this chapter.

(2) PRIVILEGE STATE indicates the setting of the S bit in the *processor's status register*. Certain assembler language instructions are privileged and may be executed only in supervisor state. Your driver should not use privileged instructions to process a function code if the operations will be performed in user state.

(3) DISABLE INTERRUPTS is Yes if the driver must call **INTSOFF** to protect the Device Control Block from being modified by another interrupt while the driver is executing. The call to **INTSOFF** should set the priority just high enough to prevent interrupts from the driver's device. Before returning, your driver must call **INTSON** to restore the interrupt status to its previous priority level.

Note that when the **Dalarms** function is invoked, the interrupt priority has already been set to the value that was established in the driver's prior call to **ALARM_ASSIGN**.

(4) ERR CODE REQUIRED is Yes if the driver must return a value in the integer function-result of the driver function. If an error code is not required, the function result need not be assigned a value.

(5) DRIVER TYPE indicates which function codes a given driver needs to support the types are the following:

SEQ	Sequential device drivers
DISK	Disk device drivers
DEMX	Demultiplexing drivers
NON-FS	Non-File System drivers that use **Dcontrol** for I/O

The letters under each driver type indicate whether the driver must support the function code:

H	The HDISK unit supports this operation
O	Optional for this device type (see detailed description of the function code for more information)
R	Required for this device type

3.2.2 Error Codes

Your driver may return an error number in the driver function result. If your driver receives a function code that it does not recognize, it should return with *error number 656* in its function result. In the case of **Dinterrupt**, **Dalarms**, and **Reqrestart**, the error number should be placed in the **Hard_error** field of the Request Block. In the case of a non-recoverable data inconsistency, the error should be signaled by calling **SYSTEM_ERROR**.

Standard error numbers are indicated throughout this chapter. The following standard error numbers apply to all device types:

660	*Cable disconnected.*
666	*Device timeout.*

You are free to use other error numbers in the range 1840 to 1879 to represent particular conditions your driver expects to encounter. *Warnings* are indicated by a negative error number in the range -1879 to -1840. A warning means that the operation was successfully completed under special circumstances. *System errors* reported by your driver must be in the range 11840 to 11879.

3.3 FUNCTION CODES SHARED BY DIFFERENT DRIVER TYPES

The function codes described in this section are used by drivers of differing type. These function codes are **Dinterrupt**, **Dinit**, **Ddown**, **Dcontrol**, **Reqrestart**, and **Dalarms**.

3.3.1 DINTERRUPT Driver Function Code

***function code* Dinterrupt**
{Intpar: integer}

Parameters:

Intpar: For Serial A & B, contains the interrupt type.

Operation: Handle a device interrupt
Drivers: SEQ (R), DISK (R), DEMX (R), NON-FS (R)
Privilege State: Supervisor
Must Disable Interrupts: No
Error Code: Not required

Your driver is called with the **Dinterrupt** function code when there is an interrupt for it to handle. For most devices, **Intpar** is zero and has no meaning. For Serial A or Serial B interrupts, **Intpar** contains a value between 0 and 3 representing the type of interrupt (refer to read-register 2B in the SCC manual).

For demultiplexing drivers, **Dinterrupt** signals you to find out from the interrupting controller which of its several devices is the source of the interrupt. You must then invoke its device driver to handle the interrupt by passing the same parameter list the demultiplexing driver was called with using the **CALLDRIVER** procedure.

Your interrupt handler for the device must:

- Signal the device to clear the interrupt.
- Perform the desired I/O transfer.
- Call **OKXFERNEXT** (disk devices only).
- Return.

The I/O request may require several I/O transfers. When the request has been completed, or if an error was detected, your interrupt handler should call **IODONE** if your driver supports a disk device; for other device types, it should:

- Call **UNBLK_REQ** to unblock the process that is waiting for I/O.
- Call **ADJ_IO_CNT** to reduce the count of pending I/O requests for the data segment.
- **DEQUEUE** the Request Block from the Device Control Block.
- If another Request Block for this device is on the queue, initiate the I/O.
- Return.

3.3.2 DINIT Driver Function Code

***function code* Dinit**

Operation: Initialize a driver
Drivers: SEQ (R), DISK (R), DEMX (R), NON-FS (R)
Privilege State: User
Must Disable Interrupts: Yes
Error Code: Required

For the boot device or a device that is configured to be pre-loaded, **Dinit** is invoked during startup. For other devices it is called wither at the time the device is mounted or prior to calling **Dcontrol**, **Dskunclamp**, or **Dskformat** for

unmounted devices.

To initialize your device driver, you must:

- Call **GETSPACE** to acquire space for the driver's Device Control Block (its global data).
- Store the address of the Device Control Block into **Parameters^.Configptr^.Cb_addr**.
- Initialize the Device Control Block.
- Call **ALARM_ASSIGN** to reserve any alarms that will be needed while the driver is running. Save the alarm numbers that have been assigned to this device during initialization. (You must reserve space for this purpose in the Device Control Block.)
- Prepare the device hardware for operation. This may involve programming the controller's hardware registers, reading the device's version number, and so on.

If the driver encounters an error during initialization, it should reverse the initialization procedure so that the device and associated control blocks are returned to the state they were in when the driver was invoked with **Dinit** function code. The function result of the driver should be set to reflect the nature of the error:

- *Error 602* means that alarms were not available from **ALARM_ASSIGN**.
- *Error 610* means that memory was not available from **GETSPACE**.

For disk devices that are supported by the Operating System **HDISK** unit (see Section 2.5, Disk Driver Support), your driver should not perform the functions listed above. Instead:

- Call **USE_HDISK**
- Call **CALL_HDISK**

HDISK will then invoke your driver with function code **Hdinit** to request disk driver initialization.

3.3.3 DDOWN Driver Function Code

***function code* Ddown**

Operation: Shut down a device
Drivers: SEQ (R), DISK (H), DEMX (R), NON-FS (R)
Privilege State: User
Must Disable Interrupts: Yes
Error Code: Required

For devices that are configured to be dynamically loaded and unloaded, **Ddown** is invoked during unmounting unless the Characteristics file for the device specifies that the device is to be permanently mounted. (Chapter 5 tells you how to specify characteristics.) **Ddown** tells your driver to undo everything it did in **Dinit**.

Return with Error 60? if the driver is waiting for I/O at the time **Ddown** is invoked. Otherwise do the following:

- Call **RELSPACE** to release the space for the driver's Device Control Block.
- Set the address of the Device Control Block in **Parameters^.Configptr^.Cb_addr** to **ord(nil).**
- Call **ALARMRETURN** to release any alarms that were assigned to the driver.
- Set the device hardware so that it cannot generate an interrupt until **Dinit** is called again.

3.3.4 DCONTROL Driver Function Code

***function code* Dcontrol**
{Parptr: absptr}

Parameters:
Parptr: Address of a Device Control Record

Operation: Send or receive device-dependent information
Drivers: SEQ (O), DISK (O), NON-FS (O)

Privilege State: User
Must Disable Interrupts: Yes
Error Code: Required

For sequential and disk devices, your driver needs to support the **Dcontrol** function code only if application programs will access this device by calling the **DEVICE_CONTROL** procedure. The application program provides **DEVICE_CONTROL** with a pointer to the Device Control Record, and **DEVICE_CONTROL** passes the record to the driver with the **Dcontrol** function code.

Refer to the *Operating System Reference Manual for the Lisa* for information on the **DEVICE_CONTROL** File System call. (Note that the **Dctype** record described in that manual is the same as the **Dc_rec** shown below.) If you want your driver to be compatible with similar existing OS drivers, support all the **DEVICE_CONTROL** operations documented for that device type in the Operating System Reference Manual.

Parptr is the address of the Device Control Record, **Dc_rec**, defined below. The driver and the application program must agree upon the use of the Device Control Record; its contents are not checked by the **DEVICE_CONTROL** procedure.

```
Dc_rec = record
            Dversion: integer;
            Dcode: integer;
            Ar10: array[0..9] of longint;
      end;
```

Dversion is used for application-defined version control between the driver and the application program. **Dcode** tells your driver what status information to get from its device or what control information to send. The **Ar10** array is used to pass additional device information between the application program that calls **DEVICE_CONTROL** and the device driver.

Dcontrol may be used for configuring operational characteristics of the driver, such as the baud rate.

If **Dcode** requests I/O, your driver must perform the following operations (refer to Section 2.6, Driver-initiated I/O Requests, for more information):

- Create its own Request Block
- Define a Request Block Extension containing whatever information is needed.
- Call **CVT_BUFF_ADDR**, **ADJ_IO_CNT**, and **FREEZE_SEG** to freeze the user's data buffer.
- Call **BLK_REQ** to wait until the I/O has completed.

Return *error 623* if the contents of **Dc_rec** are not valid for the device.

3.3.5 REQRESTART Driver Function Code

***function code* Reqrestart**
{Req: reqptr_type}

Parameters:
Req: Pointer to Request Block passed to FREEZE_SEG

Operation: Restart an I/O request
Drivers: SEQ (R), DISK (H), NON-FS (O)
Privilege State: User
Must Disable Interrupts: Yes
Error Code: Not required

Reqrestart is required by all drivers of sequential or disk devices and by non-File System device drivers that call **FREEZE_SEG**. **Reqrestart** is invoked by the OS Memory Manager to restart the driver from the point at which a **FREEZE_SEG** request failed because the data was being relocated in memory. If you call **FREEZE_SEG** from more than one place in your driver, you must set a flag to indicate where **Reqrestart** must restart.

If your driver uses **HDISK**, call **CALL_HDISK** when you are passed a **Reqrestart** function code.

3.3.6 DALARMS Driver Function Code

***function code* Dalarms**
{Intpar: integer}

Parameters:
Intpar: Alarm number

Operation: Handle an alarm interrupt
Drivers: SEQ (O), DISK (O), NON-FS (O)
Privilege State: Supervisor
Must Disable Interrupts: No
Error Code: Not required

Your driver needs to respond to the **Dalarms** function code only if the driver calls **ALARMRELATIVE**. **Dalarms** is invoked when an alarm set by **ALARMRELATIVE** goes off. Intpar contains the alarm number.

Alarms can be used to handle interrupts that require a long time to process. See Lengthy Interrupt Processing, Section 2.3.4.

3.4 FUNCTION CODES FOR SEQUENTIAL DEVICES

The following function-codes are called only for drivers of sequential devices. Sequential devices are accessed as a sequence of bytes; their drivers make no provision for storing and retrieving files at different locations on the device. Applications that require sequential device drivers include data communications and printing.

3.4.1 SEQIO Driver Function Code

***function code* Seqio**
{Req: reqptr_type}

Parameters:
Req: Pointer to the I/O Request Block.

Operation: Input/output to a sequential device
Drivers: SEQ (R)
Privilege State: User
Must Disable Interrupts: Yes
Error Code: Required

Seqio is invoked during I/O to a sequential device when **READ_DATA** or **WRITE_DATA** is called to transfer a sequence of bytes from or to a sequential device. **Req** points to an initialized I/O Request Block as described in Request Block initialization, Section 2.4.1. **Seqio** must call **ENQUEUE** to place the Request Block at the end of the Request Block queue for the device. If there are no other requests currently in progress, the driver can then initiate the I/O transfer for this request. If another I/O request for the device is in progress, the driver will initiate the I/O transfer for this request when it completes all prior requests in the queue.

3.4.2 DDISCON Driver Function Code
***function code* Ddiscon**

Operation: Modem disconnect
Drivers: SEQ (O), NON-FS (O)
Privilege State: User
Must Disable Interrupts: Yes
Error Code: Not required

Ddiscon is invoked when **CLOSE_OBJECT** is called for a sequential device or when a process aborts without closing an open sequential device. If your driver controls a device attached to a modem, disconnect the modem from the line. For other sequential devices, your driver should return immediately after receiving the **Ddiscon** function code.

Note that the driver is not called during **OPEN** for sequential devices. The application program should initialize modem control by calling **DEVICE_CONTROL** from the application program when opening the device.

3.5 FUNCTION CODES FOR DISK DEVICES

The operations described in this section are performed only by drivers of Lisa-format random-access disk devices.

Most disk devices can use the facilities of the **HDISK** unit in the Operating System to perform the standard operations required for function codes **Dinit**, **Ddown**, **Reqrestart**, and **Dskio**. (See Appendix A for a listing of **HDISK**.) These operations include managing the Request Block queue and processing the File System headers that precede each 512-byte block of data on the disk. **HDISK** contains the six routines described below.

Called directly by the disk driver (described in Chapter 2):

- **XFERINIT**
- **IODONE**

Called by the OS when the disk driver calls **CALL_HDISK** (your driver should pass to **CALL_HDISK** the same parameters with which it was called):

- **INIT** for **Dinit** function code
- **DOWN** for **Ddown** function code
- **REQRESTART** for **Reqrestart** function code
- **DSKIO** for **Dskio** function code

Standard error numbers for disk drivers are:

606	*Can't find sector (disk may be unformatted).*
614	*No disk present in drive.*
617	*Checksum error in data -- no data returned.*
616	*Cannot format disk.*
654	*Unspecified hard I/O from disk controller.*
-663	*Read with data returned -- some data may be okay, but a checksum, CRC, or parity error was detected.*
1824	*Write failed.*

3.5.1 DSKIO Driver Function Code

***function code* Dskio**
{Req: reqptr_type}

Parameters:
Req: Pointer to the I/O Request Block.

Operation: Input/output to a disk device
Drivers: DISK (H)
Privilege State: User
Must Disable Interrupts: Yes
Error Code: Required

Dskio is invoked by **READ_DATA** and **WRITE_DATA** to access a disk file. The File System also invokes this function to maintain its internal directory structures while performing file operations such as **OPEN** and **CLOSE_OBJECT**. **Req** points to an initialized I/O Request Block.

When **Dskio** is invoked, your driver should call **CALL_HDISK**, passing it the parameters with which it was invoked. **CALL_HDISK** will then invoke **HDISK** to handle the I/O request.

3.5.2 DSKUNCLAMP Driver Function Code

***function code* Dskunclamp**

Operation: Eject a disk from the disk drive
Drivers: DISK (O)
Privilege State: User
Must Disable Interrupts: Yes
Error Code: Required

If the disk device has a removable disk, eject the disk when **Dskunclamp** is invoked; otherwise return immediately with *error 685*.

If the device will send an interrupt to signal that the eject has completed, it may be necessary for **Dskunclamp** to create a Request Block. Refer to Section 2.6, Driver-initiated I/O Requests.

3.5.3 DSKFORMAT Driver Function Code

***function code* Dskformat**

Operation: Format a disk device
Drivers: DISK (O)
Privilege State: User
Must Disable Interrupts: Yes
Error Code: Required

Your driver needs to handle this function code only if the disk device formats its media; otherwise the driver may return immediately. **Dskformat** may be invoked when the user mounts a disk that cannot be read or when the application receives a request to erase everything on the disk.

Dskformat initiates the format operation by signaling the device's intelligent controller. The driver must create a

Request Block for this operation and call **BLK_REQ** to wait until the format operation has completed. Refer to Section 2.6, Driver-initiated I/O Requests.

3.5.4 HDINIT Driver Function Code

***function code* Hdinit**
{Req: reqptr_type}

Operation: Initialize a disk device-driver
Drivers: DISK (R)
Privilege State: User
Must Disable Interrupts: No
Error Code: Required

Hdinit is invoked by the **INIT** routine within **HDISK**. Your disk driver must perform the following operations when invoked with the **Hdinit** function code:

To initialize your device driver, you must:

- Call **GETSPACE** to acquire space for the driver's Device Control Block extension.
- Store the address of the extension in the **Ext_ptr** field of the Device Control Block.
- Call **ALARM_ASSIGN** to reserve any alarms that will be needed while the driver is running. Save the alarm numbers that have been assigned to this device during initialization. (You must reserve space for this purpose in the Device Control Block.)
- Initialize the disk controller hardware so that read and write commands can be sent.

If the driver encounters an error during initialization, it should reverse the initialization procedure so that the device and associated control blocks are returned to the state they were in when the driver was invoked with **Hdinit** function code. The function result of the driver should be set to reflect the nature of the error:

- *Error 602* means that alarms were not available from **ALARM_ASSIGN**.
- *Error 610* means that memory was not available from **GETSPACE**.

3.5.5 HDDOWN Driver Function Code

***function code* Hddown**
{Req: reqptr_type}

Operation: Shut down a disk device
Drivers: DISK (R)
Privilege State: User
Must Disable Interrupts: No
Error Code: Required

HDISK invokes **Hddown** so that your driver can undo everything it did in **Hdinit**:

- Call **RELSPACE** to release the space for the driver's Device Control Block extension.
- Set the address of the Device Control Block extension in **Ext_ptr** to **ord(nil)**.
- Call **ALARMRETURN** to release any alarms that were assigned to the driver.
- Set the disk device controller so that it cannot generate an interrupt until **Hdinit** is invoked again.

3.5.6 HDSKIO Driver Function Code

***function code* Hdskio**
{C_cmd: integer; C_sector: longint}

Parameters:
C_cmd:
C_sector:

Operation: Perform an I/O transfer
Drivers: DISK (R)
Privilege State: User
Must Disable Interrupts: No
Error Code: Required

HDISK invokes the disk driver with function code **Hdskio** to begin an I/O transfer; that is, **Hdskio** reads or writes a group of contiguous 512-byte blocks. Options in the **Io_mode** field of the Request Block extension allow you to transfer the blocks with or without their 24-byte headers. **Io_mode** options are described in Section 2.4.1, Request Block Initialization. Headers are described in the **Pagelabel** type in **Driverdefs**, listed in Appendix A.

During each interrupt of the I/O transfer, your driver must call **OKXFERNEXT** to update the headers; when the transfer is complete, it must call **IODONE**. (See Disk Driver Support, Section 2.5, for further information.)

3.7 FUNCTION CODES FOR DEMULTIPLEXING DRIVERS

The operations described in this section are performed only by demultiplexing drivers -- those that call lower-level drivers. See Chapter 1 for a description of driver hierarchy and the role of demultiplexing drivers.

3.6.1 DATTACH Driver Function Code

function code **Dattach**
{N_configptr: ptrdevrec}

Parameters:
N_configptr: Pointer to Device Configuration Record of new device.

Operation: Associate a driver with this demultiplexing driver
Drivers: DEMX (R)
Privilege State: User
Must Disable Interrupts: Yes
Error Code: Required

Demultiplexing drivers are drivers that dispatch interrupts for more than one device. (See Figure 1-4, Communication between Drivers.) **Dattach** enables the demultiplexing driver to service the interrupts of the devices whose drivers it attaches. In general, **Dattach** is invoked immediately after the **Dinit** call to the demultiplexing driver. Then, as each of its devices gets initialized, the demultiplexing driver is invoked with **Dattach** to attach the device driver for the new device. That is, **Dattach** is invoked once for each device associated with the demultiplexing driver.

When an interrupt for an attached device is received, the demultiplexing driver calls the lower-level driver using the **CALLDRIVER** procedure described in Chapter 2.

N_configptr points to the Device Configuration Record of a new device being attached to this driver.

3.6.2 DUNATTACH Driver Function Code

function code **Dunattach**
{O_configptr: ptrdevrec; Still_inuse: boolean}

Parameters:
O_configptr: Pointer to Device Configuration Record of new device.
Still_inuse: True if other devices remain attached; False if none.

Operation: Disassociate a driver from this demultiplexing driver
Drivers: DEMX (R)
Privilege State: User
Must Disable Interrupts: Yes
Error Code: Not required

Dunattach is invoked when a device associated with the demultiplexing driver is invoked with **Ddown** function code. **O_configptr** points to the Device Configuration Record for the device being disassociated.

Dunattach must set **Still_inuse** to **true** if other devices remain attached to the demultiplexing driver. If no devices remain attached, **Still_inuse** must be set to **false** so the calling routine can invoke **Ddown**.

3.7 DRIVERS FOR BOOTABLE DEVICES

{ To be documented by Jay Walton.

By the time we release the alpha draft (around 1/11/84), we should have a definite decision on whether configurable drivers for bootable devices will be supported for spring release.

Please advise.}

Chapter 4
HARDWARE INTERFACE

HARDWARE INTERFACE

Chapter 4 supplements the *Lisa Hardware Manual* and *Lisa Theory of Operations* with hardware documentation relevant to device drivers. You need to become familiar only with those sections of the chapter that contain information about the particular device interface your driver uses.

4.1 SYSTEM CLOCK RATES

The 68000 is driven by a 20.375 MHz crystal divided by 4, yielding a 5.09375 MHz clock; the clock rate is therefore one cycle every 196.319 nanoseconds, while it may be impossible to compute exact execution times, you can use this clock to compute an approximate execution time for a given section of code.

Your calculation should round up the published 68000 instruction execution-cycle times to a value n+x (where n is the number of cycles and x is 0, 1, 2, or 3) such that n+x is a multiple of 4. Rounding is necessary because of wait states the Lisa uses to handle the video memory. The pipeline architecture of the 68000, which pre-fetches instructions, makes even this calculation imprecise. In addition, when accessing the address space of a 6522, the 68000 inserts wait states in order to synchronize with the 6522 clock.

4.2 THE SERIAL COMMUNICATIONS CONTROLLER (SCC)

The Serial Communications Controller is described in Zilog's *SCC Technical Manual*. The *Lisa Hardware Manual* (Sections 2.5.2, 3.5.1, and 6.4) also includes information about the operation of the SCC in Lisa.

4.2.1 SCC Timing

Two consecutive accesses to the control register address are necessary to read or write data to internal registers 1 through 15. Therefore you should disable SCC interrupts during consecutive accesses.

The 68000 **CLR** instruction accesses the target address twice during a single instruction. The Lisa's Assembler optimizes the **MOVE #0** instruction to **CLR**. Therefore don't use **CLR** or **MOVE #0** instructions when writing to the SCC. The Lisa's Pascal Compiler generates a **CLR** instruction when you assign zero to a variable. Therefore if you code an SCC interface in Pascal, do not assign zero directly to an SCC control register; first assign zero to a variable, then assign the variable to the SCC address.

A recovery time of 6 CPU cycles is required between consecutive accesses. The Pascal Compiler usually generates code that provides sufficient recovery time. However, if you use WITH statements to optimize SCC accesses, you may need to insert some delay between accesses.

If your driver is written in assembler language, provide recovery time by inserting one or two **NOP** instructions between contiguous instructions that access the SCC.

4.2.2 Other Information Related to the SCC

Asynchronous I/O can occur on either port A or B. The following characteristics are unique to each port:

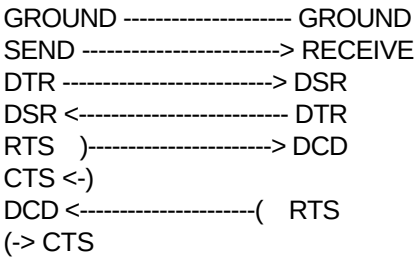
- Port A
  - accomodates synchronous communications.
  - accomodates SDLC/HDLC.
  - provides full RS-232C modem control.
- Port B
  - accomodates Applebus.

4.2.2.1 Cable Connections

The *Lisa Hardware Manual* (Figure 3-7) shows the cable signals for the DB-25 connector. Cable signal names are the same as SCC Pin names except for the following:

CABLE SIGNAL	CONNECTS TO SCC PIN
RxC(A)	RTxCA
TEXT(A) & TxC(A)	TRxCA
DSR(A)	SYNCA (and DCDB on hardware after 1.0)
DSR(B)	CTSB/

Apple's modem eliminator cable connects the following signals (dashed lines indicate wires; arrows show the direction of information flow along the wires):



4.2.2 Register Usage

Registers 2 and 9 are shared between ports A and B. The following restrictions apply:

o Never write to register 2.

o Write to register 9 only when performing a reset operation. To reset SCC Port A or Port B, write calue $8A or $4A to register 9 during **Dinit** and **Ddown**.

The DCD-B bit indicates the state of the signal on the wire it monitors. Never enable interrupts on DCD-B because this signal belongs to Port A although it is read on port B. (Refer to the description of register 15 in the *SCC Manual*). When Port A is in synchronous or SDLC modes, it may read DCD-B to determine the state of signal DSR on Port A's connector (not available on Lisa 1.0).

Set RTS-B to 0 except when using Applebus.

4.3 THE 6522 PARALLEL PORT

You can the cycle times provided in this section to calculate timing for the pulse mode handshake and to compute data throughput rates. On Lisa 1.0, the 5.09375 MHz clock time is therefore 1963.19 nanoseconds. The same 1963.19 nanosecond clock drives the 6522s on the Sony disk-drive card and the internally released four-port card. The cycle time for the 6522s on Lisa 2.0's I/O board, and the two-port card is 785.276 nanoseconds (derived from 68000 cycle time divided by 4). To summarize:

	LISA 1.0	LISA 2.0
I/O board 6522	1963.19 ns	785.276 ns
2-port card	785.276 ns	785.276 ns
4-port card	1963.19 ns	1963.19 ns

Sony 6522	1963.19 ns	1963.19 ns

4.3.1 Using the 6522 Pulse Mode Handshake

When outgoing data is on the data bus, a pulse on a separate handshake line indicates that the data is ready for the receiver to read. The pulse mode is used to strobe outgoing data to a peripheral device or to acknowledge receipt of incoming data from a peripheral device. It is possible, however, for the 68000 to drive the 6522 faster than it can handle the pulse handshake, causing the data to be accessed before completion of the pulse from the previous access. Calculation of the minimum number of 6522 cycles allowed between read or write commands that utilize pulse mode handshake is shown below, using a Profile driver as an example.

For writing to a Profile, the number of 6522 cycles between consecutive writes is n, where n must satisfy the inequality:

n * clock-rate > T(dcw) + 1.5 * clock-rate + T(rs3) + device-latency

where T(dcw) = 300 ns, as stated in the *6522 Data Sheet*;
T(rs3) = 1000 ns, as stated in the *6522 Data Sheet*;
clock-rate = 1963.19 ns or 785.276 ns, depending on which 6522 is being used;
device-latency = 100 ns for Profile.

For reading from a Profile, the number of 6522 cycles between consecutive writes is n, where n must satisfy the inequality:

n * clock-rate > T(pcr) + 1.5 * clock-rate + T(rs1) + device-latency

where T(pcr) = 300 ns, as stated in the *6522 Data Sheet*;
T(rs1) = 1000 ns, as stated in the *6522 Data Sheet*;
clock-rate = 1963.19 ns or 785.276 ns, depending on which 6522 is being used;
device-latency = 600 ns for Profile.

Thus for both reading from and writing to a Profile, n >= 4 on the fast 6522 and n >= 3 on the slow 6522. Four cycles of the fast 6522 can be generated by separating consecutive reads or writes by between 10 and 13 cycles of the 68000 (inclusive of the read or write command). Likewise, three cycles of the slow 6522 can be generated by separating consecutive reads or writes by between 14 and 21 cycles of the 68000. Therefore to code a Profile driver that will accomodate both fast and slow 6522s, insert instructions requiring at least 14 cycles of the 68000 between consecutive reads and writes within the driver.

4.3.2 Parallel Port Differences

The Lisa contains one built-in parallel port. You may also connect a two-port card containing two parallel ports or an internally released four-port card containing three parallel ports plus a fourth non-standard port. Each parallel port is controlled by a 6522. The differences between the ports are highlighted here. The remarks below assume familiarity with the 6522 data sheet and the *Lisa Hardware Manual* (Sections 2.5.3, 3.5.2, and 6.5).

On the built-in port, the CHK signal enters as a keyboard event; on the two-port or four-port card, the CHK signal enters on the CB1 line of each port.

The diagnostic PARITY line is not accessible on the built-in parallel port but may be read on PB6 on either port of the two-port card or the first two ports of the four-port card.

When writing to a register of a 6522 parallel port, it is important to change only the bits you want to set. Therefore, the instructions **ANDI.B**, **ORI.B**, **BSET**, and **BCLR** should be used instead of **MOVE.B** whenever writing to registers **ORB**, **DDRB**, **ACR**, and **PCR**. **IFR** and **IER** are discussed below.

The following bits should never be changed on the built-in parallel port:

Register	Bits
DRB	5, 6, 7
DDRB	5, 6, 7
T1C-L	all
T1C-H	all
T1L-L	all
T1L-H	all
SR	all (never read this register either)
ACR	2, 3, 4, 6, 7
PCR	4
IFR	2, 6
IER	0, 2, 3, 4, 6

Bits 0 through 4 of a parallel port's B register are used differently, depending upon whether a ProFile disk or a Centronics-compatible dot matrix printer (such as the C.Itoh 8510A) is attached. All parallel ports treat bits 0 through 4 similarly. Following is a description of the use of these bits, as well as **CA1**, **CA2**, and **CB2** of the **IFR** register.

Bit #	Direction	ProFile	Parallel Printer
PB0	in	cable detect (1 = disconnect)	paper empty (1 = PE)
PB1	in	BSY state (0 = busy)	acknowledge (0 = ACK)
PB2	out	set = 0 always	set = 0 always
PB3	out	read (= 1)/write(= 0) dir.	set = 1 always
PB4	out	CMD (0 = CMD true)	don't care
CA1*	in	BSY latch (from PB1)	ACK latch (from PB1)
CA2	out	data strobe	data strobe
CB2	in	parity	error latch ignored

* Bit 0 of PCR controls whther CA1 will latch rising ot falling edge of PB1.

The CB2 parity error latch that will be cleared by any read or write to the B register. Therefore check disk parity *before* making any reference to the B register.

Writing a one to a bit that is already one in the 6522 **IFR** and **IER** registers may change the bit to zero. Therefore **MOVE.B** is the only instruction that should be used when writing to **IFR** or **IER**. Do not use the commands **AND**, **ANDI**, **OR**, **ORI**, **BSET**, or **BCLR**.

The 6522 that controls the Lisa's keyboard has some of the signals which belong to the built-in parallel port. Table 4-1 describes the use of the following bits on the keyboard 6522 as they pertain to the parallel port: **PB5** and **PB7** (bits 5 and 7 on the B register) and **DDRB5** and **DDRB7** (bits 5 and 7 of data direction register B). The adress pairs given in parentheses in the table result in references to the same location. This is because address bits 6, 7, and 8 were used in early Lisa prototypes but are currently ignored.

Table 4-1

Bit Settings with Disk Attached

	DDRB7	PB7	DDRB5	PB5
Built-in Parallel:	leave as input (FCD811/FCD911)	ignore (FCD801/FCD901)	leave as input (FCD811/FCD911)	ignore (FCD801/FCD901)
6522 Keyboard:	output (FCDC05/FCDD85)	set to 1 (FCDC01/FCDD81)	output (FCDC05/FCDD85)	normally 1, 0 resets parity (FCDC01/FCDD81)
Two-port/four-port Parallel:	output (FCXX11)	set to 1 (FCXX01)	output (FCXX11)	normally 1, 0 resets parity (FCXX01)

Bit Settings with Printer Attached

	DDRB7	PB7	DDRB5	PB5
Built-in Parallel:	leave as input (FCD811/FCD911)	ignore (FCD801/FCD901)	leave as input (FCD811/FCD911)	ignore (FCD801/FCD901)
6522 Keyboard:	output (FCDC05/FCDD85)	set to 1 (FCDC01/FCDD81)	input (FCDC05/FCDD85)	SELECT(1=online) (FCDC01/FCDD81)
Two-port/four-port Parallel:	output (FCXX11)	set to 1 (FCXX01)	output (FCXX11)	SELECT (1=online) (FCXX01)

To determine the value of XX in addresses FCXX01 and FCXX11 in Table 4-1, consult Table 4-2. Channel 1 refers to the bottom XX; channel 2 refers to the top XX; channel 3 is accessible with the back off on a four-port card only.

Table 4-2

	chan 1	chan 2	chan 3
slot 1 (outer)	20	28	30
slot 2 (middle)	60	68	70
slot 3 (inner)	A0	A8	B0

4.4 PERIPHERAL CARDS

The Lisa's three I/O slots support peripheral cards as well as devices. If a card can support more than one device, a demultiplexing driver is required for the card and one driver is required for each type of device supported by the card. This section describes hardware requirements for Lisa peripheral cards.

Each peripheral device (or its controller) should have a separate *interrupt-enable latch* that operates under software control. When disabled, this latch should hold pending any interrupt, without signaling the interrupt. When the latch is enabled, any previously pending interrupts should be signaled. (The Interrupt Enable Register on the 6522 VIA provides this function.) In addition, a system reset must disable the device controller so that no interrupts are possible until the driver has enabled interrupts on the device.

Any operation that may take the device more than about one millisecond should cause an interrupt upon completion. The driver should poll for this completion condition.

Writing a device driver can be greatly simplified if the device handshake sequence has only one point at which an interrupt is required. For example, it is preferable to have the driver wait for **SEEK** and **WRITE** at the same time on a disk device, rather than waiting for the **SEEK** to complete before being able to start up the **WRITE**.

Disconnecting the cable to a device should cause an interrupt that the driver can respond to. If this is not possible, the controller should provide an interval timer interrupt to allow the driver to poll for disconnection.

Any hardware configuration parameters should operate under software control.

If the device is bootable, its controller must support the conventions adopted in the Lisa Boot ROM.

Each Lisa peripheral card must be self-identifying.

4.5 PRINTERS

Printer programs execute as a shared intrinsic unit within the applications. The printer programs are responsible for creating the appropriate sequence of ASCII bytes to hand off to the OS using **WRITE_DATA**. By calling **MOUNT**, **UNMOUNT**, **OPEN**, **CLOSE_OBJECT**, **WRITE_DATA**, and **DEVICE_CONTROL**, printer programs can communicate with a serial or parallel interface port on the Lisa. *Special printer drivers that control letter quality, dot matrix, laser, and plotter printing are not within the scope of this manual.*

The serial or parallel interface driver within the Operating System is responsible only for making sure that the data is sent to the printer without overrunning the device. Handshake protocols provided by the OS include:

- Delay after CR, LF.
- Hardware handshake.
- XON/XOFF software handshake.

Through **DEVICE_CONTROL**, the handshake protocol, the modem control, parity, byte length, baud rate, and other device characteristics of the printer can be set by the printer program.

4.6 BUILT-IN DEVICES

Drivers for the mouse, speaker, interval timer, alarm, and clock/calendar are built in and are not configurable. Drivers for built-in disk drives, built-in serial ports, keyboard, and screen are configurable, since these devices can be accessed using **READ_DATA** or **WRITE_DATA**.

Chapter 5
SPECIFYING CHARACTERISTICS FOR A NEW DRIVER

SPECIFYING CHARACTERISTICS FOR A NEW DRIVER

5.1 THE INSTALLATION DISK

The installation disk is used by the end user in conjunction with the Preferences tool to install a driver for a new device. The installation disk contains the driver code file, the Characteristics file, and for bootable devices only, a loader file. This software must all be contained on a single installation disk.

The *driver code file* is the configurable driver program you have written to support a new device or group of devices.

The *Characteristics file* contains information about your configurable driver that is needed by the Lisa Operating System. You create the Characteristics file by means of the CDCHAR program described in this chapter.

The *loader file* contains the program for booting the system from the new device; it should have the same name as the driver code file with extension .BT.

Figure 5-1 shows the steps necessary to make a configurable driver available on a new system.

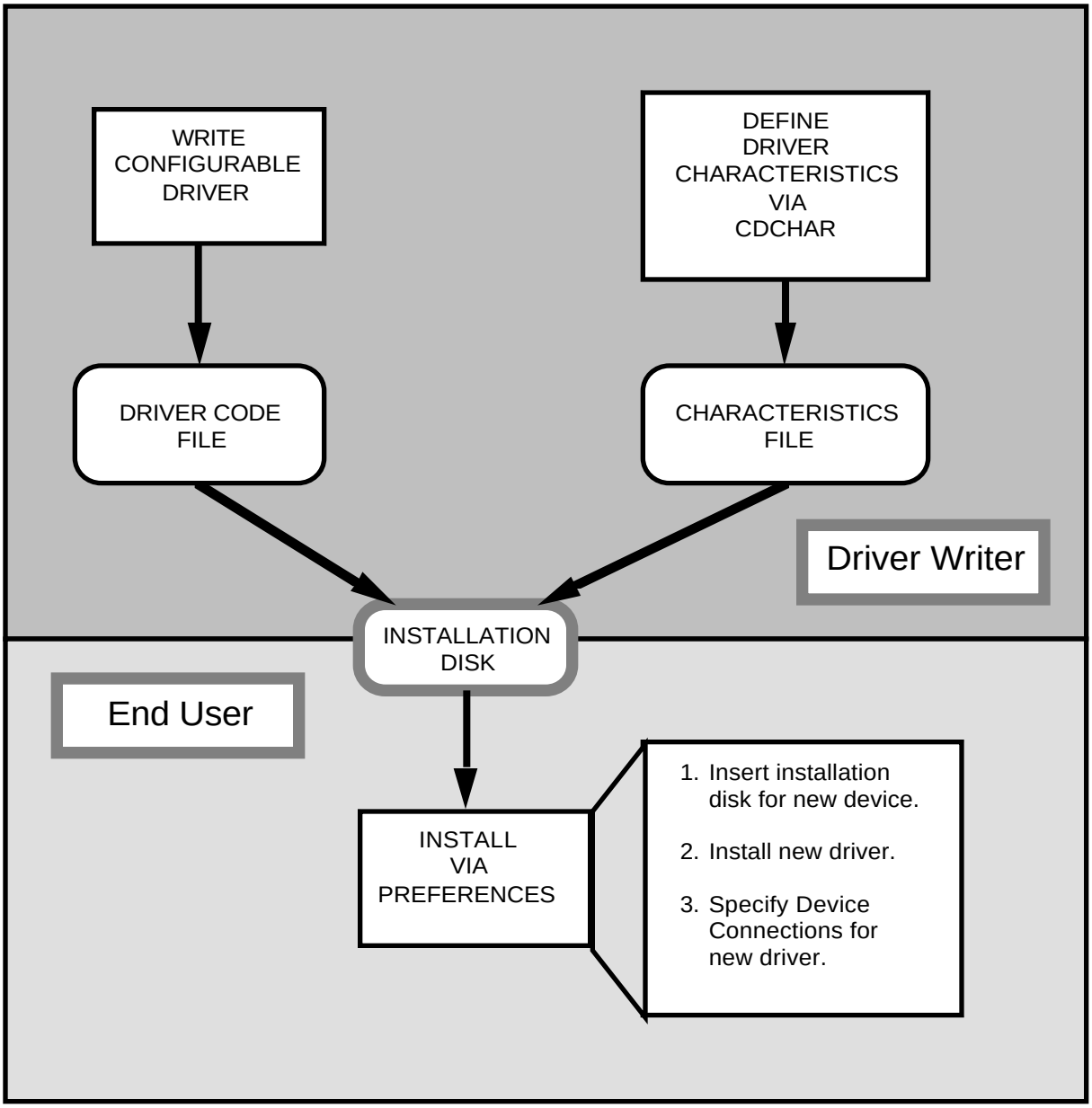


FIGURE 5-1

Figure 5-1. Specifying Driver Characteristics.

5.2 THE CDCHAR PROGRAM

The CDCHAR program asks you questions about your configurable driver and saves the answers in the Characteristics file, which is used by the end user when installing the configurable driver.

The CDCHAR program is supplied with the Lisa Operating System. To run the program, use the Run command in the Workshop. The CDCHAR dialog is as follows:

Driver name?

Respond with the name you want the end user to see in the Device Connections table of the Preferences tool; for example,

Laser Printer

The driver name may contain spaces; it must not be more than 22 characters long. Note that two or more devices with the same driver name may not be configured on the same Lisa.

Unique id?

Supply the permanent identification number for your driver. This number must be assigned to you by Apple Computer. (Numbers 1 through 128 are freely available to you for the purpose of testing your driver.)

Any default info for PM's extension words?

Indicate yes or no. If yes, you are prompted for up to three words of device-specific information that will be stored in parameter memory whenever the configurable driver is loaded. Each word is a number between 0 and 65535. This information is optional and is ignored by the Operating System. The end user can override this information at installation time by means of the Preferences tool.

Device type [1 = sequential, 2 = disk, 3 = demultiplexing]?

Specify whether your device is sequential, disk, or demultiplexing.

Is the device bootable?

Indicate ues or no. If yes, supply a loader file on the installation disk; the loader file contains the program for booting the system from the new device. (This question is asked only for disk devices.)

Removable media?

Indicate yes or no. (This question is asked only for disk devices.)

Ejectable media?

Indicate yes or no. If yes, the driver program must support the Dskunclamp function code. (This question is asked only for disk devices with removable media.)

Address of boot track blocks?

Indicate the block number where the boot tracks should continue. If you specify zero, the boot tracks do not skip any blocks before beginning the first File System block. (This question is asked only for disk

devices.)

Address of the first File System block?

Indicate the absolute block number of the first File System block. (This question is asked only for disk devices.)

Should the driver be preloaded?

Indicate yes or no. If the driver is preloaded -- loaded at boot tiem -- the first acess of the device will be slightly faster. (This question is nor asked for demultiplexing drivers or for disk drivers with removable disks.)

Should the driver stay resident, once loaded?

Indicate yes or no. If yes, the driver remains in memory when the device is unmounted. (This question is not asked for demultiplexing drivers or for disk drivers with removable disks.)

CDCHAR now lists the data you have entered so that you can verify it.

Is all this correct?

If no, you may enter the data again. If yes, the Characteristics file is created.

Characteristics file destination disk (e.g, -MYDISK)?

Specify the name of the installation disk.

5.3 THE PREFERENCES TOOL

The Preferences tool is used by the end user to install a configurable driver, using the installation disk described above. To install a new device, the end user must follow these steps:

1. If the device requires one or more new hardware cards, power down the Lisa and insert the cards.

2. Power up the Workshop or the Office System and open Preferences.

3. Insert the installation disk in the built-in microdisk slot.

4. Let Preferences install the driver on the installation disk.

Chapter 6
HOW TO MAKE A DEVICE DRIVER

HOW TO MAKE A DEVICE DRIVER

6.1 EDITING

Use the Lisa Workshop for writing and editing your driver.

6.2 COMPILING AND ASSEMBLING

This section discusses assembler language and Pascal conventions for drivers.

6.2.1 Assembler Language Considerations

If your driver contains assembler language code, the Workshop Assembler can generate the object files needed for linking. All assembler language programs must conform to the Pascal parameter-passing and register-usage conventions described in the Assembler chapter of the Workshop manual (Sections 6.3 and 6.6). The declaration of an EXTERNAL assembler language routine in your driver must occur within the IMPLEMENTATION portion of the unit, at the outermost level (not within a subroutine). We recommend that you prefix all object-code file names with **OBJECT/**.

6.2.2 Pascal Considerations

A device driver must contain a Pascal unit like the one in Figure 6-1. If you code your driver in assembler language for efficiency, the assembler language driver must perform as though it were this Pascal program.

```
-------------------------------------------------------------------
UNIT <driver unit name>;

INTERFACE
  USES
  (*$U object/driverdefs.obj*)
        driverdefs,
  (*$U object/driversubs.obj*)
        driversubs;

        function DRIVER (parameters: param_ptr): integer;

IMPLEMENTATION
  CONST ...
  TYPE ...
  <declarations of external assembler-language subroutines, if any>
  <internal Pascal subroutines go here>

        function DRIVER;
          CONST ...
          TYPE ...
          VAR ...
          <internal Pascal subroutines may go here>

        begin
                DRIVER := 0;    (*no errors yet*)
                case parameters^.fnctn_code of
                  dinterrupt: ...
                  dinit: ...
                  ddown: ...
                  dskunclamp: ...
```

```
                dskformat: ...
                seqio: ...
                dskio: ...
                dcontrol: ...
                reqrestart: ...
                dattach: ...
                dunattach: ...
                ddsicon: ...
                dalarms: ...
                hdinit: ...
                hddown: ...
                hdskio: ...
                otherwise DRIVER := <error code>
              end (* case *)
        end; (* driver *)
end. (* unit *)
```

Figure 6-1. A Skeleton Driver Program.

For Pepsi release only, every driver must be linked with a dummy main program shown in Figure 6-2. This program is never executed; it is required so that the Linker will mark your configurable driver as an executable program.

```
PROGRAM DUMMYMAIN;
  USES
        (*$Uobject/driverdefs.obj*)
          driverdefs,
        (*$Uobject/driversubs.obj*)
          driversubs,
        (*$Uobject/your-driver-unit's-file.obj*)
          your-unit-name,
  VAR
        i: integer;
        p: param_ptr;
  begin
        i := driver(p);
  end.
```

Figure 6-2. A Dummy Main Program.

To limit the size of the Pascal library (osint.paslib.obj) linked with each driver, limit your use of Pascal functions to the following:

ABS, CHR, DIV, EXIT, MOD, MOVELEFT, ODD, ORD, ORD4, POINTER, PRED, SIZEOF, SUCC

You can reduce the memory requirements of your driver by avoiding sets, console I/O, (**READ**, **READLN**, **WRITE**, and **WRITELN**), string procedures, and procedures dealing with packed array of char.

Pascal dynamic allocation (**NEW**), **real** variables, and general I/O routines provided by the Pascal language are not

supported by the Pascal libraries linked with drivers.

Do not use the **$S** compiler directive. Your driver must reside entirely within the blank segment.

6.3 LINKING

Each driver is linked so that it is contained entirely, within the non-intrinsic blank segment, which is the default segment. The first object file linked with the driver is an assembler language file whose first instruction is a **JMP** to the driver function subroutine. The driver is also linked with a main Pascal program called **drivermain.obj** which, though never executed, tricks the Linker into resolving all references as thought the output of the linkage were a stand-alone, executable program.

Using the Workshop's Linker, link together the following modules: (**driverasm** must be first; the others may be in any order):

object/driverasm.obj
object/*your-driver-unit's-file*.obj
object/*your-other-driver-subroutines*.obj
object/*your-dummymain-file*.obj
object/osintpaslib.obj

6.4 INSTALLING A NEW DRIVER

This section describes the steps you must take in order to install a new configurable driver. Follow these steps in the sequence shown.

6.4.1 Installing the OS

Be sure you have version 8.1 or above of the Operating System. Then install this version on a test disk, *not on your Workshop boot disk*.

6.4.2 Copying Files

Move the following files from the test drive to your Workshop prefix volume.

build/assemb.text
build/comp.text
build/link.text
object/driverasm.obj
object/driverdefs.obj
object/driversubs.obj
object/osintpaslib.obj

Edit, rename, and copy the following files to your Workshop prefix volume.

build/rsgen.text
(example of compile/link macro for driver)
build/rslinklist.text
(example of Linker input list)
source/rs232main.text
(example of dummy main program)

6.4.3 Running the Build Macro

Run your new build macro (based on **build/rsgen.text**). A macro is a file containing commands to be executed by

the Workshop.

6.4.4 Installing the Driver

Copy your driver to the test disk and boot from the test disk. Then follow these steps to replace an existing driver. An RS232 driver is given as an example. ***(Only RS232 drivers are configurable for Pepsi release.)***

If you are replacing an existing driver,

* Issue an **UNMOUNT** command for the device, RS232A.
* Rename the existing driver on the test disk, **system.cd_rs232a** to some temporary file name.
* Rename your driver to **system.cd_rs232a**, the name the existing driver had.
* Issue a **MOUNT** command for RS232A, the device your driver supports. Your new driver will be invoked.

Table 6-1 contains a list of devices supported under version 8.1 of the Lisa Operating System together with their driver name.

Table 6-1
Currently Supported Drivers (OS Version 8.1)

Device	Driver Name
Serial Port A	**system.cd_rs232a**
Serial Port B	**system.cd_rs232b**

6.5 TESTING AND DEBUGGING

You can debug your device driver by writing messages to the screen with **WRITELN**, by using LisaBug, or by a combination of these techniques. You can call **WRITELN** to display the contents of variables at various states in the execution of a driver.

You may want to turn these debugging features on and off. **WRITELN** commands can be *selectively executed* using the **TRACE** function described below or they can be *selectively compiled* by using compile-time conditional compilation directives described in the Workshop manual.

Other procedures and functions that are useful during program development are **MICROTIMER**, **LOG**, and **LOGGING**, discussed in Chapter 2.

6.5.1 TRACE Function

function* TRACE (Part: osportion; Level:integer):*boolean

Input Parameters:

Part: Always 'DD'.
Level: 0..99, value to compare to the trace fence.

Output Parameters:

TRACE: True if the trace fence is <= Level; else false.

Your driver can call the **TRACE** function to test a *trace fence* in the OS indicating whether **WRITELN** commands should be executed. The trace fence for device drivers is an integer located at location $220 from domain 0. (All

drivers execute in domain 0.) You can change the trace fence at execution time by using the LisaBug commands Set Memory or Set Word.

The **TRACE** function returns **true** when the trace fence is set to a value less than or equal to **Level**. In debug statements, you want to appear in all or most instances, set **Level** to a high value. For device drivers, **Part** must contain the letters 'DD'.

The TRACE function is used in a Pascal statement like the following:

```
if TRACE('dd', 60) then WRITELN('debug message')
```

In the example above, if the trace fence is 60 or less, the debug message will be printed.

6.5.2 Getting Control During OS Startup

Some driver function codes are invoked during startup. To test them, you will need:

* OS version 5.4 (or later).
* CPU boot PROM $117 (or later).
* A copy of LisaBug (file **system.debug**) on your boot volume.

The OS loader chooses whether to bring the OS up in 'debug mode', based on the value of location $10000. The PROM normally initializes all of memory to -1, so a -1 in that cell means run without debug mode. In order to select debug mode, you must modify location $10000 to a value other than -1 before the loader gets control. Here's how:

1. Reboot from the PROM.

2. Press the Apple and numeric-key-pad Enter keys simultaneously at the point where you would normally request a boot device selection.

3. When the PROM presents a menu for 'restart', 'continue', or 'startup from', press the Apple and S keys simultaneously: this selects *service mode*.

4. Use the Set Memory command (by typing **2**) to store 0 in memory location $10000. The Display Memory command lets you see whether the debug-mode cell really has been changed.

5. Quit service mode by typing **7** or **9**, returning control to the menu described in step 3 above. Choose the 'startup from' option to boot from your boot volume.

6. The loader executes next. Just before it loads the main part of the OS, the loader emulates a level-7 interrupt to give you a chance to debug the loader. Type g to continue.

7. When it reaches the **DB_INIT** section of startup -- when OS symbols and labels are available -- the OS emulates another level-7 interrupt.

Now you can set breakpoints, examine system global variables, set trace fences, and perform any other LisaBug commands. Don't forget to set all breakpoints in terms of domain 0; for example:

```
>BR 0:my_label
```

Cross Reference Listing:

Beginning of file: DRIVERSUBS.TEXT

```
1:      1.
1:      2.  UNIT DRIVERSUBS;         { UNIT NEEDED BY CONFIGURABLE DRIVERS }
1:      3.
1:      4.  INTERFACE
1:      5.    USES
1:      6.         {$U object/driverdefs.obj}
1:      7.           DRIVERDEFS;
1:      8.
1:      9.
1:     10.    procedure CANCEL_REQ(req: reqptr_type);
1:     11.
1:     12.    procedure ENQUEUE(var newlink, leftlink: linkage; b_sysarea: absptr);
1:     13.
1:     14.    procedure DEQUEUE(var link: linkage; b_sysarea: absptr);
1:     15.
1:     16.    procedure RELSPACE(ordaddr: absptr; b_area: absptr);
1:     17.
1:     18.    function GETSPACE(amount: int2; b_area: absptr;
1:     19.                                                          var ordaddr: absptr): boolean;
1:     20.
1:     21.    procedure SYSTEM_ERROR(errnum: int2);
1:     22.
1:     23.    function TRACE(part: osportion; level: integer) : boolean;
1:     24.
1:     25.    procedure status_req(reqptr: reqptr_type;
1:     26.                                                 var status: reqsts_type);
1:     27.
1:     28.    procedure unblk_req(reqptr: reqptr_type;
1:     29.                                                 success_f: boolean);
1:     30.
1:     31.    procedure blk_req(reqptr_list: reqptr_type;
1:     32.                                                 var first_reqptr: reqptr_type);
1:     33.
1:     34.    procedure IODONE(port_ptr: hdiskcb_ptr; prev_err: integer);
1:     35.
1:     36.    procedure FREEZE_SEG(var errnum: int2; c_sdb: absptr; offset: int4;
1:     37.                                                 ioreq_addr: absptr; var buffaddr: absptr);
1:     38.
1:     39.    procedure UNFREEZE_SEG(c_sdb: absptr);
1:     40.
1:     41.    procedure ADJ_IO_CNT(inc_io_count: boolean; c_sdb: absptr);
1:     42.
1:     43.    procedure CVT_BUFF_ADDR(var errnum: int2; mem_writeF: boolean; buffaddr: absptr;
1:     44.                                          byte_cnt: int4; var ordsdb: absptr; var offset: int4);
1:     45.
1:     46.    procedure CALLDRIVER(var errnum: integer; config_ptr: ptrdevrec;
1:     47.                                          parameters: param_ptr);
1:     48.
1:     49.    procedure INTSOFF(level: intsoff_type; var status: intson_type);
1:     50.
1:     51.    procedure INTSON(status: intson_type);
1:     52.
1:     53.    function ALLSET(b1, b2: int1): boolean;
1:     54.
1:     55.    function LOGGING: boolean;
1:     56.
1:     57.    procedure LOG(var errnum: integer; ptr_arr: longint);
1:     58.
1:     59.    function CHAIN_FORWARD(current: linkage): reqptr_type;
1:     60.
1:     61.    procedure DISKSYNC(busy: boolean);
1:     62.
1:     63.    function MICROTIMER: longint;
1:     64.
1:     65.    procedure ALARM_ASSIGN(var alarm: integer; pdr: ptrdevrec; status: intson_type);
1:     66.
1:     67.    procedure ALARMRETURN(alarm: integer);
1:     68.
1:     69.    procedure ALARMRELATIVE(alarm: integer; delay: longint);
1:     70.
1:     71.    procedure ALARMOFF(alarm: integer);
```

```
1:     72.
1:     73.    procedure LINK_TO_PCB(req_ptr: reqptr_type);
1:     74.
1:     75.    procedure Use_Hdisk(configptr: ptrdevrec);
1:     76.
1:     77.    procedure Call_Hdisk(var error: int2; configptr: ptrdevrec; parameters: param_ptr);
1:     78.
1:     79.    function OKXFERNEXT(port_ptr: hdiskcb_ptr): integer;
1:     80.
1:     81.
1:     82.  IMPLEMENTATION
1:     83.
1:     84.    procedure CANCEL_REQ;
1:     85.    external;
1:     86.
1:     87.    procedure ENQUEUE;
1:     88.    external;
1:     89.
1:     90.    procedure DEQUEUE;
1:     91.    external;
1:     92.
1:     93.    procedure RELSPACE;
1:     94.    external;
1:     95.
1:     96.    function GETSPACE;
1:     97.    external;
1:     98.
1:     99.    procedure SYSTEM_ERROR;
1: 100.    external;
1: 101.
1: 102.    function TRACE;
1: 103.    external;
1: 104.
1: 105.    procedure status_req;
1: 106.    external;
1: 107.
1: 108.    procedure unblk_req;
1: 109.    external;
1: 110.
1: 111.    procedure blk_req;
1: 112.    external;
1: 113.
1: 114.    procedure IODONE;
1: 115.    external;
1: 116.
1: 117.    procedure FREEZE_SEG;
1: 118.    external;
1: 119.
1: 120.    procedure UNFREEZE_SEG;
1: 121.    external;
1: 122.
1: 123.    procedure ADJ_IO_CNT;
1: 124.    external;
1: 125.
1: 126.    procedure CVT_BUFF_ADDR;
1: 127.    external;
1: 128.
1: 129.    procedure CALLDRIVER;
1: 130.    external;
1: 131.
1: 132.    procedure INTSOFF;
1: 133.    external;
1: 134.
1: 135.    procedure INTSON;
1: 136.    external;
1: 137.
1: 138.    function ALLSET;
1: 139.    external;
1: 140.
1: 141.    function LOGGING;
1: 142.    external;
1: 143.
1: 144.    procedure LOG;
1: 145.    external;
1: 146.
1: 147.    function CHAIN_FORWARD;
```

```
 1: 148.    external;
 1: 149.
 1: 150.    procedure DISKSYNC;
 1: 151.    external;
 1: 152.
 1: 153.    function MICROTIMER;
 1: 154.    external;
 1: 155.
 1: 156.    procedure ALARM_ASSIGN;
 1: 157.    external;
 1: 158.
 1: 159.    procedure ALARMRETURN;
 1: 160.    external;
 1: 161.
 1: 162.    procedure ALARMRELATIVE;
 1: 163.    external;
 1: 164.
 1: 165.    procedure ALARMOFF;
 1: 166.    external;
 1: 167.
 1: 168.    procedure LINK_TO_PCB;
 1: 169.    external;
 1: 170.
 1: 171.    procedure Use_Hdisk;
 1: 172.    external;
 1: 173.
 1: 174.    procedure Call_Hdisk;
 1: 175.    external;
 1: 176.
 1: 177.    function OKXFERNEXT;
 1: 178.    external;
 1: 179.
 1: 180.  end.
 1: 181.

End of File: DRIVERSUBS.TEXT


Directory of files in Cross Reference:

 1: DRIVERSUBS.TEXT
level =  1.

absptr  1:        12,   1:  14,     1:       16,   1:  16,     1:       18,   1:  19,     1:       36,   1:  37,
          1:      37,   1:  39,     1:       41,   1:  43,     1:       44.
alarm   1:        65*,  1:  67*,    1:       69*,  1:  71*.
amount  1:        18*.
b1        1:      53*.
b2        1:      53*.
b_area  1:        16*,  1:  18*.
b_sysare 1:       12*,  1:  14*.
buffaddr 1:       37*,  1:  43*.
busy    1:        61*.
byte_cnt 1:       44*.
c_sdb   1:        36*,  1:  39*,    1:       41*.
config_p 1:       46*.
configpt 1:       75*,  1:  77*.
current 1:        59*.
delay   1:        69*.
driverde 1:        7.
driversu 1:        2.
errnum  1:        21*,  1:  36*,    1:       43*,  1:  46*,    1:       57*.
error   1:        77*.
external 1:       85,   1:  88,     1:       91,   1:  94,     1:       97,   1: 100,     1: 103,   1: 106,
         1: 109,   1: 112,          1: 115,   1: 118,          1: 121,   1: 124,          1: 127,   1: 130,
         1: 133,   1: 136,          1: 139,   1: 142,          1: 145,   1: 148,          1: 151,   1: 154,
         1: 157,   1: 160,          1: 163,   1: 166,          1: 169,   1: 172,          1: 175,   1: 178.
first_re 1:       32*.
hdiskcb_ 1:       34,   1:  79.
implemen 1:       82.
inc_io_c 1:       41*.
int1    1:        53.
int2    1:        18,   1:  21,     1:       36,   1:  43,     1:       77.
int4    1:        36,   1:  44,     1:       44.
interfac 1:        4.
intsoff_ 1:       49.
```

```
intson_t 1:       49,   1:  51,     1:       65.
ioreq_ad 1:       37*.
leftlink 1:       12*.
level    1:       23*,  1:  49*.
link     1:       14*.
linkage  1:       12,   1:  14,     1:       59.
mem_writ 1:       43*.
newlink  1:       12*.
offset   1:       36*,  1:  44*.
ordaddr  1:       16*,  1:  19*.
ordsdb   1:       44*.
osportio 1:       23.
param_pt 1:       47,   1:  77.
paramete 1:       47*,  1:  77*.
part     1:       23*.
pdr      1:       65*.
port_ptr 1:       34*,  1:  79*.
prev_err 1:       34*.
ptr_arr  1:       57*.
ptrdevre 1:       46,   1:  65,     1:       75,   1:  77.
req      1:       10*.
req_ptr  1:       73*.
reqptr   1:       25*,  1:  28*.
reqptr_l 1:       31*.
reqptr_t 1:       10,   1:  25,     1:       28,   1:  31,     1:       32,   1:  59,     1:       73.
reqsts_t 1:       26.
status   1:       26*,  1:  49*,    1:       51*,  1:  65*.
success_ 1:       29*.
unit     1:        2.

Procedures

adj_io_c 1:       41*,  1: 123*.
alarm_as 1:       65*,  1: 156*.
alarmoff 1:       71*,  1: 165*.
alarmrel 1:       69*,  1: 162*.
alarmret 1:       67*,  1: 159*.
blk_req  1:       31*,  1: 111*.
call_hdi 1:       77*,  1: 174*.
calldriv 1:       46*,  1: 129*.
cancel_r 1:       10*,  1:  84*.
cvt_buff 1:       43*,  1: 126*.
dequeue  1:       14*,  1:  90*.
disksync 1:       61*,  1: 150*.
enqueue  1:       12*,  1:  87*.
freeze_s 1:       36*,  1: 117*.
intsoff  1:       49*,  1: 132*.
intson   1:       51*,  1: 135*.
iodone   1:       34*,  1: 114*.
link_to_ 1:       73*,  1: 168*.
log      1:       57*,  1: 144*.
relspace 1:       16*,  1:  93*.
status_r 1:       25*,  1: 105*.
system_e 1:       21*,  1:  99*.
unblk_re 1:       28*,  1: 108*.
unfreeze 1:       39*,  1: 120*.
use_hdis 1:       75*,  1: 171*.

Functions

allset   1:       53*,  1: 138*.
chain_fo 1:       59*,  1: 147*.
getspace 1:       18*,  1:  96*.
logging  1:       55*,  1: 141*.
microtim 1:       63*,  1: 153*.
okxferne 1:       79*,  1: 177*.
trace    1:       23*,  1: 102*.

Declaration Character : '*'
Assignment Character :  '='
```

Cross Reference Listing:

Beginning of file: GENIO.TEXT

```
 1:      1.  UNIT genio;
 1:      2.                                                              { general I/O driver }
 1:      3.
 1:      4.
 1:      5.                  { By Dave Offen }
 1:      6.                  { Copyright 1983, Apple Computer Inc. }
 1:      7.                  { Modified 4/2/83 by Wendell Henry         }
 1:      8.                  {                Add CALLDRIVER interface          }
 1:      9.
 1:     10.  INTERFACE
 1:     11.
 1:     12.    USES
 1:     13.         {$Uobject/driverdefs.obj}
 1:     14.           driverdefs,
 1:     15.         {$Uobject/hwint.obj}
 1:     16.           hwint,
 1:     17.         {$Uobject/sysglobal.obj}
 1:     18.           globalda,
 1:     19.         {$Uobject/procprims.obj}
 1:     20.           proc_prims,
 1:     21.         {$Uobject/mmprim.obj}
 1:     22.           mmprimitives,
 1:     23.         {$Uobject/asynctr.obj}
 1:     24.           asynctr;
 1:     25.
 1:     26.    TYPE
 1:     27.         drvrec = record
 1:     28.                                  driver_id: longint;         { as a debugging aid }
 1:     29.                                  version: integer;                  { driver's version number }
 1:     30.                                  sem: semaphore;             { locks access to drvrec }
 1:     31.                                  addrdrivrname: absptr;      { for loading driver (ptr to pathname) }
 1:     32.                                  nusers: 0..32767;                  { use count for driver - 0 if unloaded }
 1:     33.                                  e_pt: absptr;                      { driver address - undefined if nusers=0 }
 1:     34.                                  kres_addr: absptr;                 { driver address if kernel-resident. nil
 1:     35.                                                                                        if requires loading off disk
}
 1:     36.                             end;
 1:     37.
 1:     38.    procedure LINK_TO_PCB(req_ptr: reqptr_type);
 1:     39.
 1:     40.    procedure CALLDRIVER(var errnum: integer; config_ptr: ptrdevrec;
 1:     41.                                                    parameters: param_ptr);
 1:     42.
 1:     43.    procedure UP(var errnum: integer; config_ptr: ptrdevrec;
 1:     44.                                  callers_config_ptr: ptrdevrec);
 1:     45.
 1:     46.    procedure DOWN(var errnum: integer; config_ptr: ptrdevrec;
 1:     47.                                  callers_config_ptr: ptrdevrec; enter_at: integer);
 1:     48.
 1:     49.    procedure ALARM_ASSIGN(var alarm: integer; pdr: ptrdevrec;
 1:     50.                                  status: intson_type);
 1:     51.
 1:     52.    procedure ALARM_FIRES(alarm: integer);
 1:     53.
 1:     54.    procedure CANCEL_REQ(req: reqptr_type);
 1:     55.
 1:     56.    procedure DRIVERCALL(var errnum: integer; dev_index: integer;
 1:     57.                                  parameters: param_ptr);
 1:     58.
 1:     59.    procedure SEQENTIO(var errnum: integer; dev_index: integer; io_buff_addr: absptr;
 1:     60.                                            length: longint; read_f: boolean; pcb_ptr: ptr_pcb;
 1:     61.                                            var req_ptr: reqptr_type);
 1:     62.
 1:     63.    procedure DISKIO(var errnum: integer; dev_index: integer; var header: pagelabel;
 1:     64.                                            extra_link: int4; extra_data_used: integer; io_buff_addr: absptr;
 1:     65.                                            length: integer; block_no: int4; read_f: boolean;
 1:     66.                                            mode: disk_io_type; pcb_ptr: ptr_pcb; var req_ptr: reqptr_type);
 1:     67.
 1:     68.    function CHAIN_FORWARD(current: linkage): reqptr_type;
 1:     69.
 1:     70.    function BADCALL(parameters: param_ptr): integer;
```

```
 1:     71.
 1:     72.    function SCC(parameters: param_ptr): integer;
 1:     73.
 1:     74.  IMPLEMENTATION
 1:     75.
 1:     76.    {$S krgenio}
 1:     77.
 1:     78.    {$IFC not debug2}
 1:     79.    {$R-} { rangecheck off unless debug mode }
 1:     80.    {$ENDC}
 1:     81.
 1:     82.
 1:     83.
 1:     84.    CONST
 1:     85.         errbase = 600;
 1:     86.         syserrbase = 10600;
 1:     87.
 1:     88.    TYPE
 1:     89.         ptrpathname = ^pathname;  { s/b USES }
 1:     90.
 1:     91.    VAR { Yes, it's global }
 1:     92.         alarm_table: array[0..20] { keyed to # of alarms in HWINT } of record
 1:     93.                                          cfg_ptr: ptrdevrec;
 1:     94.                                          intstat: intson_type;
 1:     95.                                          filler: integer; { makes record a power of 2 - fast subscripts }
 1:     96.                                    end;
 1:     97.
 1:     98.    procedure LOAD_DRIVER(var err: integer; var add: absptr; name: ptrpathname);
 1:     99.                                                  external; { s/b USES }
 1: 100.
 1: 101.    procedure UNLOAD_DRIVER(var err: integer; add: absptr); external; { s/b USES }
 1: 102.
 1: 103.    procedure CALLDRIVER; external;
 1: 104.
 1: 105.    procedure ALRM; external;
 1: 106.
 1: 107.    procedure TWIGIO(var er: integer; var req: reqblk; addr: int4); external;
 1: 108.
 1: 109.    procedure CANCEL_REQ(* req: reqptr_type *);
 1: 110.     {************************************************************}
 1: 111.     {*
*}
 1: 112.     {* Description: Dispose of request block & unlink from PCB *}
 1: 113.     {*
*}
 1: 114.     {* Input Parameters: Pointer to request block                          *}
 1: 115.     {*
*}
 1: 116.     {* Output Parameters: none                                                                  *}
 1: 117.     {*
*}
 1: 118.     {* Side Effects: none                                                                       *}
 1: 119.     {*
*}
 1: 120.     {* Special Conditions of Use: none                                                                  *}
 1: 121.     {*                                                                                  *}
 1: 122.     {* Error Conditions:                                                                          *}
 1: 123.     {*
*}
 1: 124.     {************************************************************}
 1: 125.
 1: 126.
 1: 127.   VAR
 1: 128.     prevints: intson_type;
 1: 129.     pblk_in_pcb: ptrblk_type;
 1: 130.
 1: 131.   begin
 1: 132.     INTSOFF(allints, prevints);
 1: 133.
 1: 134.     with req^ do
 1: 135.     begin
 1: 136.           DEQUEUE(pcb_chain.header, b_sysglobal_ptr);
 1: 137.           INTSON(prevints);
 1: 138.           pblk_in_pcb := @blk_in_pcb;
 1: 139.           if pblk_in_pcb^ = [i_o] then
 1: 140.             with cfigptr^ do
```

```
1: 141.                   if permreq_ptr = req then
1: 142.                   begin
1: 143.                     preq_avail := true;
1: 144.                     EXIT(cancel_req) { permanent request doesn't get deleted }
1: 145.                   end
1: 146.     end;
1: 147.     RELSPACE(ord(req), b_sysglobal_ptr)
1: 148.   end; { cancel_req }
1: 149.
1: 150.
1: 151.   procedure DISKIO;
1: 152.           {************************************************************}
1: 153.           {*
          *}
1: 154.           {* Description:  Start up or queue a disk read or write        *}
1: 155.           {*
          *}
1: 156.           {* Input Parameters: You wouldn't believe it if I told you. *}
1: 157.           {*
          *}
1: 158.           {*
          *}
1: 159.           {*
          *}
1: 160.           {* Output Parameters: Req_ptr is the address of the newly      *}
1: 161.           {*        created request block.                                                    *}
1: 162.           {*
          *}
1: 163.           {* Side Effects: Device dependent driver is invoked.           *}
1: 164.           {*
          *}
1: 165.           {* Special Conditions of Use: none                                          *}
1: 166.           {*
          *}
1: 167.           {* Error Conditions:                                                                *}
1: 168.           {*
          *}
1: 169.           {************************************************************}
1: 170.
1: 171.   VAR
1: 172.     p: params;
1: 173.     d_ptr: absptr;
1: 174.     ext_ptr: extdptr_type;
1: 175.     pblk_in_pcb: ptrblk_type;
1: 176.     ext_config: ^ext_diskconfig;
1: 177.     leftlink: link_ptr;
1: 178.     bytes: longint;
1: 179.     prevints: intson_type;
1: 180.
1: 181.   begin
1: 182.     errnum := 0;
1: 183.     ext_config := pointer(configinfo[dev_index]^.ext_addr);
1: 184.     if (block_no < 0) or (length <= 0) or
1: 185.             ((block_no + length) > ext_config^.num_bloks) or
1: 186.             ((mode = chained_hdrs) and not read_f) or
1: 187.             (read_f and (mode = with_header)) then
1: 188.           errnum := errbase + 8 { illegal mode, start addr or transfer length }
1: 189.     else
1: 190.     if not GETSPACE(sizeof(reqblk)+sizeof(disk_extend), b_sysglobal_ptr, d_ptr) then
1: 191.           errnum := errbase + 10;
1: 192.
1: 193.     if errnum = 0 then
1: 194.     begin
1: 195.           req_ptr := pointer(d_ptr); { point to the request }
1: 196.           ext_ptr := pointer(d_ptr + sizeof(reqblk)); { point to the extension area }
1: 197.
1: 198.           { initialize the request block }
1: 199.
1: 200.           with req_ptr^ do
1: 201.           begin
1: 202.             pcb_chain.kind := reqblk_type;
1: 203.             reqstatus.reqsrv_f := active;
1: 204.             reqstatus.reqabt_f := false;
1: 205.             pblk_in_pcb := @blk_in_pcb;
1: 206.             pblk_in_pcb^ := [i_o];
1: 207.             block_p_f := false;
```

```
1: 208.             hard_error := 0;
1: 209.             operatn := ord(read_f);
1: 210.             cfigptr := configinfo[dev_index];
1: 211.             req_extent := ord(ext_ptr); { point the request block to the extension }
1: 212.           end;
1: 213.
1: 214.           { transfer parameters to disk extension data block }
1: 215.
1: 216.           with ext_ptr^ do
1: 217.           begin
1: 218.             if (mode = without_header) and not read_f then
1: 219.                    with soft_hdr do
1: 220.                    begin { zero out the header when writing w/o headers }
1: 221.                      version := 0;
1: 222.                      datastat := dataok;
1: 223.                      filler := 0;
1: 224.                      volume := 0;
1: 225.                      fileid := 0;
1: 226.                      dataused := 0;
1: 227.                      abspage := 0;
1: 228.                      relpage := 0;
1: 229.                      fwdlink := 0;
1: 230.                      bkwdlink := 0
1: 231.                    end
1: 232.             else
1: 233.                    soft_hdr := header;
1: 234.             last_fwd_link := extra_link;
1: 235.             last_data_used := extra_data_used;
1: 236.             read_flag := read_f;
1: 237.             blkno := block_no + ext_config^.strt_blok;
1: 238.             io_mode := mode;
1: 239.             num_chunks := length;
1: 240.             xfer_count := 0;
1: 241.             if io_mode = raw_io then
1: 242.                    bytes := 536
1: 243.             else
1: 244.                    bytes := 512;
1: 245.             CVT_BUFF_ADDR(errnum, read_f, io_buff_addr, bytes*num_chunks,
1: 246.                           buff_rdb_ptr, buff_offset);
1: 247.           end;
1: 248.
1: 249.           if errnum > 0 then
1: 250.             RELSPACE(ord(req_ptr), b_sysglobal_ptr)
1: 251.           else
1: 252.           begin   { continue if no errors yet }
1: 253.             ADJ_IO_CNT(true, ext_ptr^.buff_rdb_ptr); { prevent swapout }
1: 254.
1: 255.             { add request to pcb chain }
1: 256.
1: 257.             INTSOFF(allints, prevints);
1: 258.             leftlink := pointer(pcb_ptr^.req_chain.header.bkwd_link + b_sysglobal_ptr);
1: 259.             ENQUEUE(req_ptr^.pcb_chain.header, leftlink^, b_sysglobal_ptr);
1: 260.             INTSON(prevints);
1: 261.
1: 262.             { call appropriate disk driver }
1: 263.
1: 264.             p.fnctn_code := dskio;
1: 265.             p.req := req_ptr;
1: 266.             CALLDRIVER(errnum, req_ptr^.cfigptr, @p); { call dskio }
1: 267.             if errnum > 0 then
1: 268.             begin
1: 269.                    ADJ_IO_CNT(false, ext_ptr^.buff_rdb_ptr); { allow swapout on errors }
1: 270.                    CANCEL_REQ(req_ptr);
1: 271.             end
1: 272.           end
1: 273.     end
1: 274.   end; { diskio }
1: 275.
1: 276.
1: 277.
1: 278.   procedure DRIVERCALL(* var errnum: integer; dev_index: integer;
1: 279.                          parameters: param_ptr *);
1: 280.
1: 281.           {************************************************************}
1: 282.           {*
          *}
```

```
1: 283.          {* Description:  Device-independent driver call                        *}
1: 284.          {*
         *}
1: 285.          {* Input Parameters: Index into configinfo, and call-                  *}
1: 286.          {*                 specific parameter block                                                  *}
1: 287.          {*
         *}
1: 288.          {* Output Parameters: Only through pointers in parameter blk *}
1: 289.          {*
         *}
1: 290.          {* Side Effects: Driver routine dependent.                                        *}
1: 291.          {*
         *}
1: 292.          {* Special Conditions of Use: none                                                      *}
1: 293.          {*
         *}
1: 294.          {* Error Conditions:                                                                                 *}
1: 295.          {*
         *}
1: 296.          {************************************************************}
1: 297.
1: 298.   begin
1: 299.     CALLDRIVER(errnum, configinfo[dev_index], parameters)
1: 300.   end; { drivercall }
1: 301.
1: 302.
1: 303.   procedure LINK_TO_PCB(* req_ptr: reqptr_type *);
1: 304.
1: 305.          {************************************************************}
1: 306.          {*
         *}
1: 307.          {* Description:  link passed request to current process'        *}
1: 308.          {*                ctrl blk and initialize all but respec_info fields     *}
1: 309.          {*
         *}
1: 310.          {* Input Parameters: Pointer to request block                                     *}
1: 311.          {*
         *}
1: 312.          {* Output Parameters: none                                                                    *}
1: 313.          {*
         *}
1: 314.          {* Side Effects:                                                                                      *}
1: 315.          {*
         *}
1: 316.          {* Special Conditions of Use: none                                                      *}
1: 317.          {*
         *}
1: 318.          {* Error Conditions:                                                                                 *}
1: 319.          {*
         *}
1: 320.          {************************************************************}
1: 321.   VAR
1: 322.     prevints: intson_type;
1: 323.     cur_pcb_ptr: ptr_pcb;
1: 324.     leftlink: reqptr_type;
1: 325.     pblk_in_pcb: ptrblk_type;
1: 326.
1: 327.   begin
1: 328.     with req_ptr^ do
1: 329.     begin
1: 330.           pcb_chain.kind := reqblk_type;
1: 331.           reqstatus.reqsrv_f := active;
1: 332.           reqstatus.reqabt_f := false;
1: 333.           pblk_in_pcb := @blk_in_pcb;
1: 334.           pblk_in_pcb^ := [i_o];
1: 335.           block_p_f := false;
1: 336.           hard_error := 0;
1: 337.           INTSOFF(allints, prevints);
1: 338.           cur_pcb_ptr := pointer(c_pcb_ptr);
1: 339.           leftlink := pointer(cur_pcb_ptr^.req_chain.header.bkwd_link +
1: 340.                           b_sysglobal_ptr);
1: 341.           ENQUEUE(pcb_chain.header, leftlink^.pcb_chain.header,
1: 342.                           b_sysglobal_ptr);
1: 343.           INTSON(prevints)
1: 344.     end
1: 345.   end; { link_to_pcb }
```

```
1: 346.
1: 347.
1: 348.    {$S fs3}
1: 349.
1: 350.    procedure DOWN(* var errnum: integer; config_ptr: ptrdevrec;
1: 351.                             callers_config_ptr: ptrdevrec; enter_at: integer *);
1: 352.
1: 353.           {************************************************************}
1: 354.           {*
          *}
1: 355.           {* Description:  Undo what UP did                                          *}
1: 356.           {*
          *}
1: 357.           {* Input Parameters: Pointer to configinfo record of device  *}
1: 358.           {*       to be DOWNed, pointer to hierarchically lower configinfo *}
1: 359.           {*       (or nil if none lower); enter_at allows for several entry*}
1: 360.           {*       points (0 is standard value except when called from UP          *}
1: 361.           {*
          *}
1: 362.           {* Output Parameters: none                                                                  *}
1: 363.           {*
          *}
1: 364.           {* Side Effects:  UNLOAD_DRIVER, dunattach and ddown may be  *}
1: 365.           {*        called. Semaphore is cleared when enter_at > 1.                *}
1: 366.           {*
          *}
1: 367.           {* Special Conditions of Use: none                                         *}
1: 368.           {*
          *}
1: 369.           {* Error Conditions:  only error is: I/O still in progress      *}
1: 370.           {*
          *}
1: 371.           {************************************************************}
1: 372.
1: 373.    LABEL
1: 374.   1, 2, 3;
1: 375.
1: 376.    VAR
1: 377.   err2: integer;
1: 378.   p: params;
1: 379.   pdrvrec: ^drvrec;
1: 380.
1: 381.    begin
1: 382.   with config_ptr^ do
1: 383.   begin
1: 384.     errnum := 0;
1: 385.     pdrvrec := pointer(drvrec_ptr);
1: 386.     if pdrvrec = nil then
1: 387.           exit(DOWN); { only until all configinfos are configurable }
1: 388.
1: 389.     { test for branch to internal entry points for backing out of UP on errors }
1: 390.
1: 391.     if enter_at > 0 then
1: 392.     begin
1: 393.           if enter_at = 1 then goto 1;
1: 394.           if enter_at = 2 then goto 2;
1: 395.           if enter_at = 3 then goto 3;
1: 396.     end;
1: 397.
1: 398.     if entry_pt = ord(nil) then
1: 399.           exit(DOWN); { device not currently "upped" }
1: 400.
1: 401.     p.still_inuse := false;
1: 402.     if callers_config_ptr <> nil then
1: 403.     begin
1: 404.           p.fnctn_code := dunattach;
1: 405.           p.o_configptr := callers_config_ptr;
1: 406.           CALLDRIVER(err2, config_ptr, @p)     { unattach the sub_driver }
1: 407.     end;
1: 408.     if permanent or p.still_inuse then
1: 409.           exit(DOWN); { keep device up }
1: 410.
1: 411.    1:  WAIT_SEM(pdrvrec^.sem, []);
1: 412.     p.fnctn_code := ddown;
1: 413.     CALLDRIVER(err2, config_ptr, @p); { down the device }
1: 414.     if enter_at = 0 then
```

```
 1: 415.            if err2 > 0 then
 1: 416.            begin
 1: 417.              errnum := err2;
 1: 418.              SIGNAL_SEM(pdrvrec^.sem);
 1: 419.              exit(DOWN);      { I/O still in progress }
 1: 420.            end;
 1: 421.
 1: 422.     2:  entry_pt := ord(nil);
 1: 423.      with pdrvrec^ do
 1: 424.      begin
 1: 425.            nusers := nusers - 1;
 1: 426.            if nusers = 0 then { driver no longer in use }
 1: 427.              if kres_addr = ord(nil) then
 1: 428.                    UNLOAD_DRIVER(err2, e_pt);
 1: 429.      end;
 1: 430.
 1: 431.     3:  SIGNAL_SEM(pdrvrec^.sem);
 1: 432.      if required_drvr <> nil then
 1: 433.            DOWN(err2, required_drvr, config_ptr, 0);
 1: 434.   end
 1: 435.     end; { down }
 1: 436.
 1: 437.
 1: 438.
 1: 439.     {$S krgenio }
 1: 440.
 1: 441.     procedure UP(* var errnum: integer; config_ptr: ptrdevrec;
 1: 442.                            callers_config_ptr: ptrdevrec *);
 1: 443.
 1: 444.   {************************************************************}
 1: 445.   {*
*}
 1: 446.   {* Description:  Up drivers "above" this, load driver.          *}
 1: 447.   {*   initialize driver, and attach "lower" drivers.             *}
 1: 448.   {*
*}
 1: 449.   {* Input Parameters: Pointer to configinfo record of device  *}
 1: 450.   {*  to be UPed, pointer to hierarchically lower configinfo   *}
 1: 451.   {*  (or nil if none lower)                                                    *}
 1: 452.   {*
*}
 1: 453.   {* Output Parameters: none                                                    *}
 1: 454.   {*
*}
 1: 455.   {* Side Effects:  may call LOAD_DRIVER, dinit and dattach       *}
 1: 456.   {*
*}
 1: 457.   {* Special Conditions of Use: none                                          *}
 1: 458.   {*
*}
 1: 459.   {* Error Conditions:                                                                 *}
 1: 460.   {*
*}
 1: 461.   {************************************************************}
 1: 462.
 1: 463.
 1: 464.     VAR
 1: 465.   err2: integer;
 1: 466.   p: params;
 1: 467.   pdrvrec: ^drvrec;
 1: 468.
 1: 469.
 1: 470.     begin
 1: 471.           with config_ptr^ do
 1: 472.           begin
 1: 473.             errnum := 0;
 1: 474.
 1: 475.             pdrvrec := pointer(drvrec_ptr);
 1: 476.             if pdrvrec = nil then
 1: 477.                   exit(UP);           { only until all configinfos are configurable }
 1: 478.
 1: 479.             if entry_pt <> ord(nil) then
 1: 480.             begin  { already UPed }
 1: 481.                   if callers_config_ptr <> nil then
 1: 482.                   begin
 1: 483.                     p.fnctn_code := dattach;
```

```
 1: 484.                     p.n_configptr := callers_config_ptr;
 1: 485.                     CALLDRIVER(errnum, config_ptr, @p)   { attach the sub-driver }
 1: 486.                   end;
 1: 487.                   exit(UP)
 1: 488.             end;
 1: 489.
 1: 490.             if required_drvr <> nil then
 1: 491.             begin { bring up the required driver before this one }
 1: 492.                   UP(errnum, required_drvr, config_ptr);
 1: 493.                   if errnum > 0 then exit(UP)
 1: 494.             end;
 1: 495.
 1: 496.             pdrvrec := pointer(drvrec_ptr);
 1: 497.             with pdrvrec^ do
 1: 498.             begin
 1: 499.                   WAIT_SEM(sem, []);
 1: 500.                   if nusers = 0 then
 1: 501.                     if kres_addr <> ord(nil) then
 1: 502.                           e_pt := kres_addr  { kernel resident driver, no need to load }
 1: 503.                     else
 1: 504.                     begin
 1: 505.                           LOAD_DRIVER(errnum, e_pt, pointer(addrdrivrname));
 1: 506.                           if errnum > 0 then
 1: 507.                           begin
 1: 508.                             DOWN(err2, config_ptr, callers_config_ptr, 3); { back out & clear sem }
 1: 509.                             exit(UP)
 1: 510.                           end
 1: 511.                     end;
 1: 512.
 1: 513.                     entry_pt := e_pt;
 1: 514.                     nusers := nusers + 1;
 1: 515.
 1: 516.                     p.fnctn_code := dinit;
 1: 517.                     CALLDRIVER(errnum, config_ptr, @p); { initialize the driver }
 1: 518.                     if errnum > 0 then
 1: 519.                     begin
 1: 520.                           DOWN(err2, config_ptr, callers_config_ptr, 2); { back out & clear sem }
 1: 521.                           exit(UP)
 1: 522.                     end;
 1: 523.                     SIGNAL_SEM(sem);
 1: 524.                   end;
 1: 525.
 1: 526.                   if callers_config_ptr <> nil then
 1: 527.                   begin
 1: 528.                     p.fnctn_code := dattach;
 1: 529.                     p.n_configptr := callers_config_ptr;
 1: 530.                     CALLDRIVER(errnum, config_ptr, @p);  { attach the sub-driver }
 1: 531.                     if errnum > 0 then
 1: 532.                           DOWN(err2, config_ptr, callers_config_ptr, 1); { back out }
 1: 533.                   end
 1: 534.             end
 1: 535.
 1: 536.    end; { up }
 1: 537.
 1: 538.
 1: 539.    procedure ALARM_FIRES(* alarm: integer *);
 1: 540.   {************************************************************}
 1: 541.   {*
*}
 1: 542.   {* Description:  driver alarms call ALRM, which calls this   *}
 1: 543.   {*
*}
 1: 544.   {* Input Parameters: alarm number is in alarm on entry          *}
 1: 545.   {*
*}
 1: 546.   {* Output Parameters: none                                                    *}
 1: 547.   {*
*}
 1: 548.   {* Side Effects:                                                                                  *}
 1: 549.   {*
*}
 1: 550.   {* Special Conditions of Use: none                                          *}
 1: 551.   {*
*}
 1: 552.   {* Error Conditions:                                                                      *}
 1: 553.   {*
```

```
*}
 1: 554.   {************************************************************}
 1: 555.    VAR
 1: 556.   errnum: integer;
 1: 557.   p: params;
 1: 558.
 1: 559.    begin
 1: 560.   with alarm_table[alarm] do
 1: 561.   begin
 1: 562.     INTSON(intstat);
 1: 563.     p.fnctn_code := dalarms;
 1: 564.     p.intpar := alarm;
 1: 565.     CALLDRIVER(errnum, cfg_ptr, @p)
 1: 566.   end
 1: 567.    end; {alarm_fires }
 1: 568.
 1: 569.    procedure ALARM_ASSIGN(* var alarm: integer; pdr: ptrdevrec;
 1: 570.                             status: intson_type *);
 1: 571.
 1: 572.   {************************************************************}
 1: 573.   {*
*}
 1: 574.   {* Description: Call ALARMASSIGN while providing for correct *}
 1: 575.   {*                         driver to be called when alarm fires.                  *}
 1: 576.   {*
*}
 1: 577.   {* Input Parameters: pointer to this devices configinfo rec. *}
 1: 578.   {*      and desired interrupt priority during alarm execution *}
 1: 579.   {*
*}
 1: 580.   {* Output Parameters: alarm number (=0 when none available)  *}
 1: 581.   {*
*}
 1: 582.   {* Side Effects:                                                                  *}
 1: 583.   {*
*}
 1: 584.   {* Special Conditions of Use: none                                                *}
 1: 585.   {*
*}
 1: 586.   {* Error Conditions:  non available when alarm = 0              *}
 1: 587.   {*
*}
 1: 588.   {************************************************************}
 1: 589.
 1: 590.    begin
 1: 591.   ALARMASSIGN(alarm, ord(@ALRM));
 1: 592.   with alarm_table[alarm] do
 1: 593.   begin
 1: 594.     cfg_ptr := pdr;
 1: 595.     intstat := status
 1: 596.   end
 1: 597.    end; { alarm_assign }
 1: 598.
 1: 599.    function BADCALL(* parameters: param_ptr ): integer *);
 1: 600.   {************************************************************}
 1: 601.   {*
*}
 1: 602.   {* Description:  Illegal driver.                                                 *}
 1: 603.   {*
*}
 1: 604.   {* Input Parameters: standard parameter record                          *}
 1: 605.   {*
*}
 1: 606.   {* Output Parameters: errnum returned as function result        *}
 1: 607.   {*
*}
 1: 608.   {* Side Effects:                                                                  *}
 1: 609.   {*
*}
 1: 610.   {* Special Conditions of Use: none                                                *}
 1: 611.   {*
*}
 1: 612.   {* Error Conditions:  Always causes an error                                      *}
 1: 613.   {*
*}
 1: 614.   {************************************************************}
```

```
 1: 615.
 1: 616.    begin
 1: 617.   if parameters^.fnctn_code = dinterrupt then
 1: 618.     SYSTEM_ERROR(syserrbase+9); { interrupts would ignore returned errnum }
 1: 619.   badcall := errbase+9;
 1: 620.    end; { badcall }
 1: 621.
 1: 622.
 1: 623.    function CHAIN_FORWARD(* current: linkage ): reqptr_type *);
 1: 624.   {************************************************************}
 1: 625.   {*
*}
 1: 626.   {* Description: Use forward link of relptr to point to next  *}
 1: 627.   {*                          request block                                  *}
 1: 628.   {*
*}
 1: 629.   {* Input Parameters: linkage pointing to next request block  *}
 1: 630.   {*
*}
 1: 631.   {* Output Parameters: pointer to the request block              *}
 1: 632.   {*
*}
 1: 633.   {* Side Effects:                                                                *}
 1: 634.   {*
*}
 1: 635.   {* Special Conditions of Use: callable from drivers             *}
 1: 636.   {*
*}
 1: 637.   {* Error Conditions:                                                            *}
 1: 638.   {*
*}
 1: 639.   {************************************************************}
 1: 640.
 1: 641.    begin
 1: 642.   CHAIN_FORWARD := pointer(current.fwd_link+b_sysglobal_ptr-sizeof(Rb_headT))
 1: 643.    end; { chain_forward }
 1: 644.
 1: 645.
 1: 646.    function SCC(* parameters: param_ptr ): integer *);
 1: 647.   {************************************************************}
 1: 648.   {*
*}
 1: 649.   {* Description:  driver for 2-port SCC                                  *}
 1: 650.   {*
*}
 1: 651.   {* Input Parameters: standard parameter record                       *}
 1: 652.   {*
*}
 1: 653.   {* Output Parameters: errnum returned as function result        *}
 1: 654.   {*
*}
 1: 655.   {* Side Effects:                                                                *}
 1: 656.   {*
*}
 1: 657.   {* Special Conditions of Use: only for initialization of SCC *}
 1: 658.   {*
*}
 1: 659.   {* Error Conditions:                                                            *}
 1: 660.   {*
*}
 1: 661.   {************************************************************}
 1: 662.
 1: 663.    CONST
 1: 664.   ebase = 680;
 1: 665.
 1: 666.    TYPE
 1: 667.   rscb = record { this is the control block for the SCC. 1 pointer for
 1: 668.                                  each channel's control block }
 1: 669.                     a, b: ptrdevrec;
 1: 670.                   end;
 1: 671.
 1: 672.
 1: 673.   {$S init}
 1: 674.
 1: 675.   procedure SCINIT;
 1: 676.   {*****************************************}
```

```
1: 677.   {* internal subroutine to initialize SCC chip *}
1: 678.   {*******************************************}
1: 679.
1: 680.   VAR
1: 681.     cb_rec: ^rscb;
1: 682.     prevints: intson_type;
1: 683.     temp: longint;
1: 684.
1: 685.   begin
1: 686.     INTSOFF(rsints, prevints);
1: 687.
1: 688.     if not GETSPACE(sizeof(rscb), b_sysglobal_ptr, temp) then
1: 689.           SCC := ebase+3
1: 690.     else
1: 691.     begin
1: 692.           parameters^.configptr^.cb_addr := temp;
1: 693.           cb_rec := pointer(temp);
1: 694.           cb_rec^.a := nil;
1: 695.           cb_rec^.b := nil;
1: 696.           SCC := 0; { no errors }
1: 697.     end;
1: 698.     INTSON(prevints)
1: 699.   end; { scinit }
1: 700.
1: 701.
1: 702.   procedure SCATTACH;
1: 703.   {*****************************************************}
1: 704.   {* internal subroutine to attach a or b driver to SCC *}
1: 705.   {*****************************************************}
1: 706.
1: 707.   VAR
1: 708.     cb_rec: ^rscb;
1: 709.
1: 710.   begin
1: 711.     with parameters^ do
1: 712.     begin
1: 713.           cb_rec := pointer(configptr^.cb_addr);
1: 714.           if n_configptr^.iochannel = 0 then
1: 715.             cb_rec^.a := n_configptr
1: 716.           else
1: 717.             cb_rec^.b := n_configptr;
1: 718.           SCC := 0;
1: 719.     end
1: 720.   end; { scattach }
1: 721.
1: 722.   {$S fs3}
1: 723.
1: 724.   procedure SCDOWN;
1: 725.   {****************************************}
1: 726.   {* internal subroutine to down scc devices *}
1: 727.   {****************************************}
1: 728.
1: 729.   VAR
1: 730.     prevints: intson_type;
1: 731.
1: 732.   begin
1: 733.     INTSOFF(rsints, prevints);
1: 734.     with parameters^.configptr^ do
1: 735.     begin
1: 736.           RELSPACE(cb_addr, b_sysglobal_ptr);
1: 737.           cb_addr := ord(nil)
1: 738.     end;
1: 739.     INTSON(prevints);
1: 740.     SCC := 0 { no errors }
1: 741.   end; { scdown }
1: 742.
1: 743.   procedure SCUNATTACH;
1: 744.   {*******************************************************}
1: 745.   {* internal subroutine to unattach a or b driver from SCC *}
1: 746.   {*******************************************************}
1: 747.
1: 748.   VAR
1: 749.     cb_rec: ^rscb;
1: 750.
1: 751.   begin
1: 752.     with parameters^ do
```

```
 1: 753.     begin
 1: 754.           cb_rec := pointer(configptr^.cb_addr);
 1: 755.           with cb_rec^ do
 1: 756.           begin
 1: 757.             if o_configptr^.iochannel = 0 then
 1: 758.                   a := nil
 1: 759.             else
 1: 760.                   b := nil;
 1: 761.
 1: 762.             still_inuse := ((a <> nil) or (b <> nil));
 1: 763.           end
 1: 764.     end
 1: 765.   end; { scunattach }
 1: 766.
 1: 767.    {************************************************************************}
 1: 768.
 1: 769.    {$S krgenio}
 1: 770.
 1: 771.    begin { scc }
 1: 772.   case parameters^.fnctn_code of
 1: 773.
 1: 774.   dinit: SCINIT;  { call internal subroutine in init segment }
 1: 775.
 1: 776.   ddown: SCDOWN;  { call internal subroutine in non-res segment }
 1: 777.
 1: 778.   dattach: SCATTACH;  { call internal subroutine in non-res segment }
 1: 779.
 1: 780.   dunattach: SCUNATTACH;  { call internal subroutine in non-res segment }
 1: 781.
 1: 782.   otherwise
 1: 783.     SCC := ebase + 2;  { no other functions allowed for 2-line SCC }
 1: 784.
 1: 785.   end { case }
 1: 786.    end; { SCC }
 1: 787.
 1: 788.
 1: 789.    {$S nigenio}
 1: 790.
 1: 791.    procedure SEQENTIO(* var errnum: integer; dev_index: integer; io_buff_addr: absptr;
 1: 792.                                    length: longint; read_f: boolean; pcb_ptr: ptr_pcb;
 1: 793.                                    var req_ptr: reqptr_type *);
 1: 794.
 1: 795.   {************************************************************}
 1: 796.   {*
*}
 1: 797.   {* Description: Start up or queue a sequential read/write.   *}
 1: 798.   {*
*}
 1: 799.   {* Input Parameters: configinfo index, memory address,          *}
 1: 800.   {*   length in bytes, read/write flag, process' pcb_ptr.        *}
 1: 801.   {*
*}
 1: 802.   {* Output Parameters: Req_ptr is the address of the newly       *}
 1: 803.   {*    created request block.                                                    *}
 1: 804.   {*
*}
 1: 805.   {* Side Effects: Device dependent driver is invoked.                    *}
 1: 806.   {*
*}
 1: 807.   {* Special Conditions of Use: none                                              *}
 1: 808.   {*
*}
 1: 809.   {* Error Conditions:                                                                  *}
 1: 810.   {*
*}
 1: 811.   {************************************************************}
 1: 812.
 1: 813.
 1: 814.    VAR
 1: 815.   d_ptr: absptr;
 1: 816.   ext_ptr: seqextptr_type;
 1: 817.   prevints: intson_type;
 1: 818.   leftlink: link_ptr;
 1: 819.   p: params;
 1: 820.   pblk_in_pcb: ptrblk_type;
 1: 821.
```

```
 1: 822.    begin
 1: 823.   errnum := 0;
 1: 824.   with configinfo[dev_index]^ do
 1: 825.     if preq_avail then
 1: 826.     begin { use pre-allocated request block }
 1: 827.           preq_avail := false;
 1: 828.           d_ptr := ord(permreq_ptr)
 1: 829.     end
 1: 830.     else
 1: 831.           if not GETSPACE(sizeof(reqblk)+sizeof(seq_extend), b_sysglobal_ptr, d_ptr) then
 1: 832.             errnum := errbase + 10;
 1: 833.     if errnum = 0 then
 1: 834.     begin
 1: 835.           req_ptr := pointer(d_ptr);  { return pointer to the request }
 1: 836.           ext_ptr := pointer(d_ptr + sizeof(reqblk));  { point to extension area }
 1: 837.
 1: 838.           { initialize the request block }
 1: 839.
 1: 840.           with req_ptr^ do
 1: 841.           begin
 1: 842.             pcb_chain.kind := reqblk_type;
 1: 843.             reqstatus.reqsrv_f := active;
 1: 844.             reqstatus.reqabt_f := false;
 1: 845.             pblk_in_pcb := @blk_in_pcb;
 1: 846.             pblk_in_pcb^ := [i_o];
 1: 847.             block_p_f := false;
 1: 848.             hard_error := 0;
 1: 849.             operatn := ord(read_f);
 1: 850.             cfigptr := configinfo[dev_index];
 1: 851.             req_extent := ord(ext_ptr);      { point the request block to the extension }
 1: 852.           end;
 1: 853.
 1: 854.           { initialize the extension data block }
 1: 855.
 1: 856.           with ext_ptr^ do
 1: 857.           begin
 1: 858.             read_flag := read_f;
 1: 859.             num_bytes := length;
 1: 860.             xfer_count := 0;
 1: 861.             CVT_BUFF_ADDR(errnum, read_f, io_buff_addr, length, buff_rdb_ptr,
 1: 862.                                   buff_offset);
 1: 863.           end;
 1: 864.           if errnum > 0 then
 1: 865.             with configinfo[dev_index]^ do
 1: 866.             if permreq_ptr = req_ptr then
 1: 867.                   preq_avail := true
 1: 868.             else
 1: 869.                   RELSPACE(ord(req_ptr), b_sysglobal_ptr)
 1: 870.           else
 1: 871.           begin   { continue if no errors yet }
 1: 872.             ADJ_IO_CNT(true, ext_ptr^.buff_rdb_ptr);  { prevent swapout }
 1: 873.
 1: 874.             { add request to pcb chain }
 1: 875.
 1: 876.             INTSOFF(allints, prevints);
 1: 877.             leftlink := pointer(pcb_ptr^.req_chain.header.bkwd_link + b_sysglobal_ptr);
 1: 878.             ENQUEUE(req_ptr^.pcb_chain.header, leftlink^, b_sysglobal_ptr);
 1: 879.             INTSON(prevints);
 1: 880.
 1: 881.             { call driver to start the request }
 1: 882.
 1: 883.             p.fnctn_code := seqio;
 1: 884.             p.req := req_ptr;
 1: 885.             CALLDRIVER(errnum, req_ptr^.cfigptr, @p);  { call seqio }
 1: 886.             if errnum > 0 then
 1: 887.             begin
 1: 888.                   ADJ_IO_CNT(false, ext_ptr^.buff_rdb_ptr);   { allow swapout on errors }
 1: 889.                   CANCEL_REQ(req_ptr);
 1: 890.             end
 1: 891.           end
 1: 892.    end
 1: 893.   end; { seqentio }
 1: 894.
 1: 895.  end.

End of File: GENIO.TEXT
```

```
Directory of files in Cross Reference:

 1: GENIO.TEXT
level =  1.

a         1: 669*,  1: 694=,         1: 715=,  1: 758=,         1: 762.
abspage 1: 227=.
absptr  1:         31,   1:  33,     1:       34,   1:  59,     1:       64,   1:  98,     1: 101,   1: 173,
          1: 815.
active  1: 203,   1: 331,   1: 843.
add       1:       98*,  1: 101*.
addr    1: 107*.
addrdriv 1:        31*,  1: 505.
adj_io_c 1: 253,   1: 269,  1: 872,   1: 888.
alarm   1:         49*,  1:  52*,    1: 560,   1: 564,          1: 591,   1: 592.
alarm_ta 1:        92*,  1: 560,     1: 592.
alarmass 1: 591.
allints 1: 132,   1: 257,   1: 337,   1: 876.
asynctr 1:         24.
b         1: 669*,  1: 695=,         1: 717=,  1: 760=,         1: 762.
b_sysglo 1: 136,   1: 147,  1: 190,   1: 250,          1: 258,   1: 259,          1: 340,   1: 342,
          1: 642,   1: 688,          1: 736,   1: 831,          1: 869,   1: 877,          1: 878.
bkwd_lin 1: 258,   1: 339,  1: 877.
bkwdlink 1: 230=.
blk_in_p 1: 138,   1: 205,  1: 333,   1: 845.
blkno   1: 237=.
block_no 1:        65*,  1: 184,     1: 185,   1: 237.
block_p_ 1: 207=,  1: 335=,          1: 847=.
buff_off 1: 246,   1: 862.
buff_rdb 1: 246,   1: 253,  1: 269,   1: 861,          1: 872,   1: 888.
bytes   1: 178*,  1: 242=,  1: 244=,  1: 245.
c_pcb_pt 1: 338.
callers_ 1:        44*,  1:  47*,    1: 402,   1: 405,          1: 481,   1: 484,          1: 508,   1: 520,
          1: 526,   1: 529,          1: 532.
cb_addr 1: 692=,  1: 713,   1: 736,   1: 737=,         1: 754.
cb_rec  1: 681*,  1: 693=,  1: 694,   1: 695,          1: 708*,  1: 713=,         1: 715,   1: 717,
          1: 749*,  1: 754=,         1: 755.
cfg_ptr 1:         93*,  1: 565,     1: 594=.
cfigptr 1: 140,   1: 210=,  1: 266,   1: 850=,         1: 885.
chained_ 1: 186.
config_p 1:        40*,  1:  43*,    1:       46*,  1: 382,     1: 406,   1: 413,          1: 433,   1: 471,
          1: 485,   1: 492,          1: 508,   1: 517,          1: 520,   1: 530,          1: 532.
configin 1: 183,   1: 210,  1: 299,   1: 824,          1: 850,   1: 865.
configpt 1: 692,   1: 713,  1: 734,   1: 754.
cur_pcb_ 1: 323*,  1: 338=,          1: 339.
current 1:         68*,  1: 642.
cvt_buff 1: 245,   1: 861.
d_ptr   1: 173*,  1: 190,   1: 195,   1: 196,          1: 815*,  1: 828=,         1: 831,   1: 835,
          1: 836.
dalarms 1: 563.
dataok  1: 222.
datastat 1: 222=.
dataused 1: 226=.
dattach 1: 483,   1: 528,   1: 778.
ddown   1: 412,   1: 776.
dequeue 1: 136.
dev_inde 1:        56*,  1:  59*,    1:       63*,  1: 183,     1: 210,   1: 299,          1: 824,   1: 850,
          1: 865.
dinit   1: 516,   1: 774.
dinterru 1: 617.
disk_ext 1: 190.
disk_io_ 1:        66.
driver_i 1:        28*.
driverde 1:        14.
drvrec  1:         27*,  1: 379,     1: 467.
drvrec_p 1: 385,   1: 475,  1: 496.
dskio   1: 264.
dunattac 1: 404,   1: 780.
e_pt    1:         33*,  1: 428,     1: 502=,  1: 505,          1: 513.
ebase   1: 664*,  1: 689,   1: 783.
enqueue 1: 259,   1: 341,   1: 878.
enter_at 1:        47*,  1: 391,     1: 393,   1: 394,          1: 395,   1: 414.
entry_pt 1: 398,   1: 422=,          1: 479,   1: 513=.
er        1: 107*.
```

```
link_to_ 1:       38*,  1: 303*.
load_dri 1:       98*,  1: 505.
scattach 1: 702*,  1: 778.
scdown  1: 724*,  1: 776.
scinit  1: 675*,  1: 774.
scunatta 1: 743*,  1: 780.
seqentio 1:       59*,  1: 791*.
twigio  1: 107*.
unload_d 1: 101*,  1: 428.
up       1:       43*,  1: 441*,    1: 477,   1: 487,         1: 492,   1: 493,         1: 509,   1: 521.

Functions

badcall 1:        70*,  1: 599*,    1: 619=.
chain_fo 1:       68*,  1: 623*,    1: 642=.
scc      1:       72*,  1: 646*,    1: 689=,  1: 696=,        1: 718=,  1: 740=,        1: 783=.

Declaration Character : '*'
Assignment Character :  '='
```

Cross Reference Listing:

Beginning of file: HDISK.TEXT

```
 1:      1.  UNIT HDISK;
 1:      2.
 1:      3.
 1:      4.          { By Dave Offen }
 1:      5.          { Copyright 1983, Apple Computer Inc. }
 1:      6.
 1:      7.                { For Profile compatible drives up to 32MB. For larger drives, make
 1:      8.                  the following changes:
 1:      9.                   - change assembly language routine in PROFASM
 1:     10.                         so that checksum is not embedded in header.
 1:     11.                }
 1:     12.
 1:     13.          { By Wendell Henry  1/17/83 }
 1:     14.          { o Convert this driver from a configurable Profile driver to a resident }
 1:     15.          {   generic driver of hard disks of Apple format.  A device specific     }
 1:     16.          {   configurable driver is called by HDISK.                                                                  }
 1:     17.
 1:     18.  INTERFACE
 1:     19.
 1:     20.    USES
 1:     21.           {$U object/driverdefs.obj}
 1:     22.                 driverdefs,
 1:     23.           {$U object/hwint.obj}
 1:     24.                 hwint,
 1:     25.           {$U object/sysglobal.obj}
 1:     26.                 globalda,
 1:     27.           {$U object/procprims.obj}
 1:     28.                 proc_prims,
 1:     29.           {$U object/mmprim.obj}
 1:     30.                 mmprimitives,
 1:     31.           {$U object/asynctr.obj}
 1:     32.                 asynctr,
 1:     33.           {$U object/genio.obj}
 1:     34.                 genio;
 1:     35.
 1:     36.
 1:     37.    {$IFC OS15}
 1:     38.
 1:     39.    procedure USE_HDISK( configptr: ptrdevrec);
 1:     40.
 1:     41.    procedure CALL_HDISK( var error: int2; configptr: ptrdevrec;
 1:     42.                          parameters: param_ptr);
 1:     43.
 1:     44.    procedure IODONE( drivecb_ptr: hdiskcb_ptr; prev_err: integer);
 1:     45.
 1:     46.    function OKXFERNEXT( drivecb_ptr: hdiskcb_ptr): integer;
 1:     47.
 1:     48.    {$ENDC}
 1:     49.
 1:     50.    function HDISKIO( parameters: param_ptr): integer;
 1:     51.
 1:     52.
 1:     53.  IMPLEMENTATION
 1:     54.
 1:     55.    {$S krgenio}
 1:     56.
 1:     57.    {$IFC not debug2}
 1:     58.    {$R-}  { rangecheck off unless debug mode }
 1:     59.    {$ENDC}
 1:     60.
 1:     61.    CONST
 1:     62.         syserrbase = 10650;
 1:     63.         errbase = 650;     { error number base }
 1:     64.         premenderr = 658;           { premature end of file in chained header record }
 1:     65.         vfyerr = 659;               { header read verify error }
 1:     66.         ignorerr = 661;   { error code when get timer interrupt requiring no action }
 1:     67.         cserr = -663;               { data is returned but contains checksum or crc error }
 1:     68.         hd_err = 654;               { hard error from profile itself }
 1:     69.         wait_int = 1;               { wait-for-next-interrupt error code }
 1:     70.         readcmd = 0;                { read command }
 1:     71.         writecmd = 1;               { write command }
```

```
 1:     72.
 1:     73.
 1:     74.    function LOGGING: boolean; external;
 1:     75.
 1:     76.    procedure LOG(var errnum: integer; ptr_arr: longint); external;
 1:     77.
 1:     78.    procedure START_DISK(drivecb_ptr: hdiskcb_ptr); forward;
 1:     79.
 1:     80.    procedure START_NEW_REQUEST(drivecb_ptr: hdiskcb_ptr); forward;
 1:     81.
 1:     82.    procedure CALL_HDISK; external;
 1:     83.
 1:     84.    procedure USE_HDISK(* configptr: ptrdevrec *);
 1:     85.           {************************************************************}
 1:     86.           {*
          *}
 1:     87.           {* Description: The device is a hard disk with Apple format *}
 1:     88.           {*                          sectors and will be using HDISK to handle   *}
 1:     89.           {*                          sector headers.
*}
 1:     90.           {*
          *}
 1:     91.           {* Input Parameters: configptr points to configinfo entry.  *}
 1:     92.           {*
          *}
 1:     93.           {* Output Parameters: none                                                  *}
 1:     94.           {*
          *}
 1:     95.           {************************************************************}
 1:     96.         var
 1:     97.           ext_ptr: ^ext_diskconfig;
 1:     98.
 1:     99.         begin
 1: 100.     ext_ptr := pointer(configptr^.ext_addr);
 1: 101.     ext_ptr^.hentry_pt := ord(@HDISKIO);  { HDISKIO can now be called }
 1: 102.   end; { USE_HDISK }
 1: 103.
 1: 104.
 1: 105.
 1: 106.    procedure FINISH_REQ( drivecb_ptr: hdiskcb_ptr; success_flag: boolean;
 1: 107.                          error: integer);
 1: 108.     {************************************************************}
 1: 109.     {*
*}
 1: 110.     {* Description: Finish current request.                                    *}
 1: 111.     {*
*}
 1: 112.     {* Input Parameters: drivecb_ptr points to control block.   *}
 1: 113.     {*
*}
 1: 114.     {* Output Parameters: none                                                  *}
 1: 115.     {*
*}
 1: 116.     {* Side Effects: none                                                       *}
 1: 117.     {*
*}
 1: 118.     {* Special Conditions of use: appropriate interrupts must   *}
 1: 119.     {*                              be off
*}
 1: 120.     {*
*}
 1: 121.     {* Error Conditions: none                                                   *}
 1: 122.     {*
*}
 1: 123.     {************************************************************}
 1: 124.
 1: 125.
 1: 126.   VAR
 1: 127.     errnum: integer;
 1: 128.     succ_f: boolean;
 1: 129.
 1: 130.   begin
 1: 131.     with drivecb_ptr^, cur_info_ptr^ do
 1: 132.     begin
 1: 133.           succ_f := success_flag;
 1: 134.
```

```
 1: 135.           with req_hd_ptr^ do
 1: 136.             if operatn <= 1 then
 1: 137.             begin  { read or write request }
 1: 138.                    if succ_f then
 1: 139.                    begin
 1: 140.                      if io_mode = chained_hdrs then
 1: 141.                             xfer_count := xfer_count + soft_hdr.dataused
 1: 142.                      else
 1: 143.                             xfer_count := xfer_count + 512;
 1: 144.                      if worstwarning <> 0 then
 1: 145.                      begin
 1: 146.                             hard_error := worstwarning;
 1: 147.                             succ_f := false
 1: 148.                      end
 1: 149.                    end
 1: 150.                    else
 1: 151.                    begin { unsuccessful }
 1: 152.                      hard_error := error;
 1: 153.
 1: 154.                      {$IFC debug2}
 1: 155.                      if TRACE(DD, 1) then
 1: 156.                             writeln('*** PROFILE ERROR #: ', hard_error);
 1: 157.                      {$ENDC}
 1: 158.
 1: 159.                    end;
 1: 160.
 1: 161.             UNFREEZE_SEG(buff_rdb_ptr);                { allow data to move }
 1: 162.             ADJ_IO_CNT(false, buff_rdb_ptr);   { allow data to be swapped }
 1: 163.           end
 1: 164.           else
 1: 165.             hard_error := error;
 1: 166.
 1: 167.           UNBLK_REQ(req_hd_ptr, succ_f);
 1: 168.
 1: 169.           { remove from the top of the queue }
 1: 170.
 1: 171.           DEQUEUE(req_hd_ptr^.dev_chain, b_sysglobal_ptr);
 1: 172.           req_hd_ptr := CHAIN_FORWARD(req_hd_ptr^.dev_chain); { new head of request list }
 1: 173.           cur_num_requests := cur_num_requests - 1;
 1: 174.           if cur_num_requests > 0 then
 1: 175.           begin
 1: 176.             if req_hd_ptr = dummy_req_ptr then
 1: 177.             begin { if dummy at head, skip over it and reverse its cylinder number }
 1: 178.                    dummy_req_ptr^.reqspec_info := dummy_req_ptr^.reqspec_info +
 1: 179.                                   $80000000;
 1: 180.                    req_hd_ptr := CHAIN_FORWARD(dummy_req_ptr^.dev_chain);
 1: 181.             end;
 1: 182.             START_NEW_REQUEST(drivecb_ptr);
 1: 183.           end
 1: 184.           else
 1: 185.             if int_prio = winints then
 1: 186.                    DISKSYNC(false);  { allow contrast changes after I/O }
 1: 187.     end
 1: 188.   end; { FINISH_REQ }
 1: 189.
 1: 190.    procedure NEXT_HDR( var info: disk_extend; last: boolean);
 1: 191.     {***********************************************************}
 1: 192.     {*
*}
 1: 193.     {* Description: Update the next software header.               *}
 1: 194.     {*
*}
 1: 195.     {* Input Parameters: Info is the request block extension    *}
 1: 196.     {*    containing the software header to be updated. Last is  *}
 1: 197.     {*    true if this is the last header to be updated for this *}
 1: 198.     {*    request. Used by WRITEs only.                                                  *}
 1: 199.     {*
*}
 1: 200.     {* Output Parameters: The updated soft_hdr in the request   *}
 1: 201.     {*
*}
 1: 202.     {* Side Effects: none                                                                 *}
 1: 203.     {*
*}
 1: 204.     {* Special Conditions of use: none                                                *}
 1: 205.     {*
```

```
*}
 1: 206.     {* Error Conditions: none                                                    *}
 1: 207.     {*
*}
 1: 208.     {************************************************************}
 1: 209.
 1: 210.   begin
 1: 211.     with info.soft_hdr do
 1: 212.     begin
 1: 213.           bkwdlink := abspage;
 1: 214.           abspage := abspage + 1;
 1: 215.           relpage := relpage + 1;
 1: 216.           if last then
 1: 217.           begin
 1: 218.             fwdlink := info.last_fwd_link;
 1: 219.             dataused := info.last_data_used
 1: 220.           end
 1: 221.           else
 1: 222.             fwdlink := abspage + 1
 1: 223.     end
 1: 224.   end; { NEXT_HDR }
 1: 225.
 1: 226.
 1: 227.    function OKXFERNEXT(* drivecb_ptr: hdiskcb_ptr): integer *);
 1: 228.   { Called by device specific driver to prepare for transfer of next sector }
 1: 229.   { from the disk. Headers and data pointers will be updated as needed.     }
 1: 230.   { Any errors found in sector just transferred will be returned as a non-  }
 1: 231.   { zero return which should terminate the transfer.                        }
 1: 232.
 1: 233.   var
 1: 234.     error: integer;
 1: 235.
 1: 236.   begin
 1: 237.     with drivecb_ptr^, cur_info_ptr^ do
 1: 238.     begin
 1: 239.           error := 0;
 1: 240.           sect_left := sect_left - 1;
 1: 241.
 1: 242.           if io_mode = chained_hdrs then
 1: 243.           begin   { reading using chained headers }
 1: 244.             with soft_hdr do
 1: 245.                   if (version <> soft_hdr.version) or
 1: 246.                           (fileid <> soft_header.fileid) or
 1: 247.                           (relpage <> soft_header.relpage) then
 1: 248.                   begin { verify error }
 1: 249.                     error := vfyerr;
 1: 250.                   end
 1: 251.                   else
 1: 252.                   begin { verifies ok }
 1: 253.                     if (fwdlink = -1) then
 1: 254.                           if sect_left > 1 then
 1: 255.                           begin { premature end of chain }
 1: 256.                             error := premenderr;
 1: 257.                             xfer_count := xfer_count + dataused
 1: 258.                           end
 1: 259.                   end
 1: 260.           end; { chained headers }
 1: 261.
 1: 262.           if error = 0 then
 1: 263.           begin
 1: 264.             if read_flag then
 1: 265.                   case raw_header_ptr^.datastat of
 1: 266.                     datamaybe:  if worstwarning = 0 then
 1: 267.                                                 worstwarning := -626;
 1: 268.                     databad:    if worstwarning <> cserr then
 1: 269.                                                 worstwarning := -625;
 1: 270.                   end; { case raw_header }
 1: 271.
 1: 272.            if (sect_left > 0) then
 1: 273.            begin { setup for next sector }
 1: 274.                   blkno := blkno + 1;
 1: 275.                   xfer_count := xfer_count + 512;
 1: 276.                   x_leng := xfer_count;
 1: 277.                   case io_mode of
 1: 278.                     with_header:
 1: 279.                           NEXT_HDR(cur_info_ptr^, sect_left = 1);
```

```
 1: 280.                       raw_io:
 1: 281.                             raw_header_ptr := pointer(ord(raw_header_ptr) + 24);
 1: 282.                       chained_hdrs:
 1: 283.                             soft_header.relpage := soft_header.relpage + 1
 1: 284.                     end { case io_mode }
 1: 285.               end
 1: 286.             end;
 1: 287.
 1: 288.             okxfernext := error;
 1: 289.
 1: 290.     end { with drivecb_ptr, cur_info_ptr }
 1: 291.   end; {OKXFERNEXT}
 1: 292.
 1: 293.    procedure IODONE (* drivecb_ptr: hdiskcb_ptr; prev_err: integer *);
 1: 294.     {************************************************************}
 1: 295.     {*
*}
 1: 296.     {* Description: Continue I/O request after call to driver.  *}
 1: 297.     {*
*}
 1: 298.     {* Input Parameters: drive_cb_ptr points to control block   *}
 1: 299.     {*
*}
 1: 300.     {* Output Parameters: none                                                                  *}
 1: 301.     {*
*}
 1: 302.     {* Side Effects:                                                                                    *}
 1: 303.     {*
*}
 1: 304.     {* Special Conditions of use: appropriate interrupts must   *}
 1: 305.     {*                       be off                                                                     *}
 1: 306.     {*
*}
 1: 307.     {* Error Conditions:                                                                        *}
 1: 308.     {*
*}
 1: 309.     {************************************************************}
 1: 310.   LABEL
 1: 311.     1;
 1: 312.
 1: 313.   VAR
 1: 314.     parm: params;
 1: 315.     success_flag: boolean;
 1: 316.     err: integer;
 1: 317.     disk_log_rec: record
 1: 318.                                       code: int1;
 1: 319.                                       rd_flag: boolean;
 1: 320.                                       slot: int1;
 1: 321.                                       chan: int1;
 1: 322.                                       er: integer;
 1: 323.                                       estat: longint;
 1: 324.                                       dev: integer;
 1: 325.                                     end;
 1: 326.
 1: 327.   begin
 1: 328.     with drivecb_ptr^, cur_info_ptr^ do
 1: 329.     begin
 1: 330.             if req_hd_ptr^.operatn > 1
 1: 331.               then FINISH_REQ(drivecb_ptr, prev_err = 0, prev_err) { non I/O operation }
 1: 332.               else  { I/O operation }
 1: 333.    1:               case prev_err of
 1: 334.                     0: begin { successful operation }
 1: 335.                               if sect_left = 0 then
 1: 336.                               begin { all sectors have been transferred }
 1: 337.                                     FINISH_REQ(drivecb_ptr, true, 0);
 1: 338.                               end
 1: 339.                               else
 1: 340.                               begin { sectors still to be transferred }
 1: 341.                                     success_flag := true;
 1: 342.                                     if read_flag then
 1: 343.                                     begin
 1: 344.                                       if io_mode = chained_hdrs then
 1: 345.                                       begin
 1: 346.                                             with soft_hdr do
 1: 347.                                               if (version <> soft_header.version) or
 1: 348.                                                       (fileid <> soft_header.fileid) or
```

```
1: 349.                                          (relpage <> soft_header.relpage) then
1: 350.                                  begin { verify error }
1: 351.                                        prev_err := vfyerr;
1: 352.                                        success_flag := false
1: 353.                                  end
1: 354.                                  else { verifies ok }
1: 355.                                        if (fwdlink = -1) then
1: 356.                                          if sect_left > 1 then
1: 357.                                          begin  { premature end of chain }
1: 358.                                                 prev_err := premenderr;
1: 359.                                                 success_flag := false;
1: 360.                                                 xfer_count := xfer_count + dataused
1: 361.                                          end;
1: 362.                            end; { chained headers }
1: 363.
1: 364.                            if success_flag then
1: 365.                            case raw_header_ptr^.datastat of
1: 366.                                   datamaybe: if worstwarning = 0 then
1: 367.                                                              worstwarning := -625;
1: 368.                                   databad:   if worstwarning <> cserr then
1: 369.                                                              worstwarning := -625;
1: 370.                            end;
1: 371.                          end; { read_mode }
1: 372.
1: 373.                          { next complete previous request if it's done. }
1: 374.                          { and start up next disk I/O, if any }
1: 375.
1: 376.                          if (sect_left > 1) and success_flag then
1: 377.                          begin { update current request }
1: 378.                            sect_left := sect_left - 1;
1: 379.                            blkno := blkno + 1;
1: 380.                            xfer_count := xfer_count + 512;
1: 381.                            x_leng := xfer_count;
1: 382.                            case io_mode of
1: 383.                                   with_header:
1: 384.                                     NEXT_HDR(cur_info_ptr^, sect_left = 1);
1: 385.                                   raw_io:
1: 386.                                     raw_header_ptr := pointer(ord(raw_header_ptr) + 24);
1: 387.                                   chained_hdrs:
1: 388.                                     soft_header.relpage := soft_header.relpage + 1;
1: 389.                            end;
1: 390.                            START_DISK(drivecb_ptr);
1: 391.                          end
1: 392.                          else
1: 393.                            FINISH_REQ(drivecb_ptr, success_flag, prev_err);
1: 394.
1: 395.                    end; { sectors still to be transferred }
1: 396.                  end;
1: 397.
1: 398.                  wait_int: { nothing to do but wait for next interrupt }
1: 399.                    begin
1: 400.                    end;
1: 401.
1: 402.                  otherwise
1: 403.                    begin { got error }
1: 404.                          restrt_count := restrt_limit; { hard error - prevent retries }
1: 405.                          if LOGGING then
1: 406.                            with disk_log_rec do
1: 407.                            begin
1: 408.                                   code := -3;
1: 409.                                   rd_flag := read_flag;
1: 410.                                   with config_addr^ do
1: 411.                                   begin
1: 412.                                     slot := slot_no;
1: 413.                                     chan := iochannel;
1: 414.                                     dev := device_no;
1: 415.                                   end;
1: 416.                                   er := prev_err;
1: 417.                                   estat := 0;
1: 418.                                   LOG(err, ord(@disk_log_rec)) { write error to log file }
1: 419.                            end;
1: 420.
1: 421.                          restrt_count := restrt_count + 1;
1: 422.                          if restrt_count <= restrt_limit then
1: 423.                          begin
1: 424.                            total_restarts := total_restarts + 1;
```

```
 1: 425.                                    START_DISK(drivecb_ptr);          { do it again from the top }
 1: 426.                                  end
 1: 427.                                  else { exhausted the retries available }
 1: 428.                                    if prev_error = cserr then
 1: 429.                                    begin
 1: 430.                                          prev_error := 0;
 1: 431.                                          worstwarning := cserr;
 1: 432.                                          goto 1
 1: 433.                                    end
 1: 434.                                    else
 1: 435.                                          FINISH_REQ(drivecb_ptr, false, prev_err); { quit if too }
 1: 436.
          { many errors }
 1: 437.
 1: 438.                              end;
 1: 439.                            end; { case }
 1: 440.     end;
 1: 441.   end; { IODONE }
 1: 442.
 1: 443.
 1: 444.    procedure START_DISK(* drivecb_ptr, hdiskcb_ptr *);
 1: 445.     {************************************************************}
 1: 446.     {*
*}
 1: 447.     {* Description: Start a read or write of one sector on a    *}
 1: 448.     {*                                  hard disk.                                                  *}
 1: 449.     {*
*}
 1: 450.     {* Input Parameters: drivecb_ptr points to control block    *}
 1: 451.     {*
*}
 1: 452.     {* Output Parameters: none                                                                      *}
 1: 453.     {*
*}
 1: 454.     {* Side Effects:                                                                               *}
 1: 455.     {*
*}
 1: 456.     {* Special Conditions of use: appropriate interrupts must   *}
 1: 457.     {*                       be off                                                              *}
 1: 458.     {*
*}
 1: 459.     {* Error Conditions:                                                                           *}
 1: 460.     {*
*}
 1: 461.     {************************************************************}
 1: 462.   VAR
 1: 463.     parm: params;
 1: 464.     errnum: integer;
 1: 465.
 1: 466.   begin
 1: 467.     with drivecb_ptr^, cur_info_ptr^ do
 1: 468.     begin
 1: 469.           with parm do
 1: 470.           begin
 1: 471.             fnctn_code := hdskio;
 1: 472.             if req_hd_ptr^.operatn <= 1 then
 1: 473.             begin { I/O operation }
 1: 474.                   if read_flag
 1: 475.                     then c_cmd := readcmd
 1: 476.                     else c_cmd := writecmd;
 1: 477.                   c_sector := blkno;
 1: 478.             end;
 1: 479.             CALLDRIVER(errnum, config_addr, @parm); { call driver to start I/O }
 1: 480.           end; { with }
 1: 481.     end;
 1: 482.   end; { start_disk }
 1: 483.
 1: 484.
 1: 485.    procedure START_NEW_REQUEST(* drivecb_ptr, hdiskcb_ptr *);
 1: 486.     {************************************************************}
 1: 487.     {*
*}
 1: 488.     {* Description: Initialize control block and make first I/O *}
 1: 489.     {*    call for the request at the head of the drive's queue  *}
 1: 490.     {*
*}
```

```
 1: 491.     {* Input Parameters: drivecb_ptr points to this drive's variables   *}
 1: 492.     {*
*}
 1: 493.     {* Output Parameters: none                                                                    *}
 1: 494.     {*
*}
 1: 495.     {* Side Effects:  Unit globals: num_sectors_left,              *}
 1: 496.     {*                                          cur_info_ptr, req_hd_ptr                      *}
 1: 497.     {*             Request changed from active to in_service                *}
 1: 498.     {*
*}
 1: 499.     {* Special Conditions of use: required ints must be off.    *}
 1: 500.     {*
*}
 1: 501.     {* Error Conditions:                                                                          *}
 1: 502.     {*
*}
 1: 503.     {************************************************************}
 1: 504.
 1: 505.   VAR
 1: 506.     er: integer;
 1: 507.
 1: 508.   begin
 1: 509.     with drivecb_ptr^ do
 1: 510.     begin
 1: 511.
 1: 512.           { set up global vars for this request }
 1: 513.
 1: 514.           req_hd_ptr^.reqstatus.reqsrv_f := in_service; { from active to in_service }
 1: 515.           cur_info_ptr := pointer(req_hd_ptr^.req_extent);
 1: 516.           if req_hd_ptr^.operatn > 1 then
 1: 517.           begin   { non I/O operation }
 1: 518.             START_DISK(drivecb_ptr);
 1: 519.           end
 1: 520.           else
 1: 521.           begin { I/O operation }
 1: 522.             with cur_info_ptr^ do
 1: 523.             begin
 1: 524.
 1: 525.                   { next try to lock the data buffer in memory }
 1: 526.
 1: 527.                   FREEZE_SEG(er, buff_rdb_ptr, buff_offset, ord(req_hd_ptr),
 1: 528.                          resolved_addr);
 1: 529.                   if er = 0 then { lock worked }
 1: 530.                   begin
 1: 531.                     sect_left := num_chunks;
 1: 532.                     restrt_count := 0;
 1: 533.                     worstwarning := 0;
 1: 534.                     raw_header_ptr := @soft_hdr;
 1: 535.                     raw_data_ptr := resolved_addr;
 1: 536.                     x_leng := xfer_count;
 1: 537.                     case io_mode of
 1: 538.                           raw_io:
 1: 539.                             begin
 1: 540.                                   raw_header_ptr := pointer(resolved_addr);
 1: 541.                                   if num_chunks = 1 then
 1: 542.                                     raw_data_ptr := resolved_addr + 24  { speed optimization }
 1: 543.                                   else
 1: 544.                                     raw_data_ptr := resolved_addr + ord4(24)*num_chunks
 1: 545.                             end;
 1: 546.
 1: 547.                           chained_hdrs: soft_header := soft_hdr; { remember which header to match }
 1: 548.                     end; { case }
 1: 549.
 1: 550.                     START_DISK(drivecb_ptr);
 1: 551.                   end
 1: 552.             end { with cur_info_ptr }
 1: 553.           end;
 1: 554.     end { with drivecb_ptr }
 1: 555.   end;  { start_new_request }
 1: 556.
 1: 557.
 1: 558.    function HDISKIO(* parameters: param_ptr): integer *);
 1: 559.     {************************************************************}
 1: 560.     {*
*}
```

```
 1: 561.     {* Description: HDISKIO is the generic device driver for all*}
 1: 562.     {*                          hard disks with the Apple format. Device    *}
 1: 563.     {*                          specific activities will be passed on to the*}
 1: 564.     {*                          appropriate device driver.                                  *}
 1: 565.     {*
*}
 1: 566.     {* Input Parameters: Each function code request has its own *}
 1: 567.     {*                                      input parameters.                                          *}
 1: 568.     {*
*}
 1: 569.     {* Output Parameters: none                                                                 *}
 1: 570.     {*
*}
 1: 571.     {* Side Effects: The request is added to the queue for the  *}
 1: 572.     {*    appropriate drive. If no request is busy, the request  *}
 1: 573.     {*    is started up immediately.                                                          *}
 1: 574.     {*
*}
 1: 575.     {* Special Conditions of use: none                                                         *}
 1: 576.     {*
*}
 1: 577.     {* Error Conditions:                                                                       *}
 1: 578.     {*
*}
 1: 579.     {************************************************************}
 1: 580.
 1: 581.   VAR
 1: 582.     parm: params;
 1: 583.     p_extdevrec: ^ext_diskconfig;
 1: 584.     drivecb_ptr: hdiskcb_ptr;
 1: 585.     ext_ptr: extdptr_type;
 1: 586.     ints: intson_type;
 1: 587.     left_sort_ptr, right_sort_ptr, head_sort_ptr: reqptr_type;
 1: 588.     new_sec, left_sec, right_sec: longint;
 1: 589.     errnum: integer;
 1: 590.
 1: 591.     {$S init}
 1: 592.
 1: 593.   procedure HINITIT;
 1: 594.   {************************************************************}
 1: 595.   {* Internal subroutine in initialize segment for profile init *}
 1: 596.   {************************************************************}
 1: 597.   TYPE
 1: 598.     initrec = record
 1: 599.                               index: integer;
 1: 600.                             end;
 1: 601.
 1: 602.   VAR
 1: 603.     chan: integer;
 1: 604.     initrec_ptr: ^initrec;
 1: 605.     newdrivecb, newreqblk: absptr;
 1: 606.
 1: 607.   begin
 1: 608.     errnum := 0;
 1: 609.     chan := parameters^.configptr^.iochannel;
 1: 610.     if chan >= 0 then INTSOFF(slotints, ints)
 1: 611.                                  else INTSOFF(winints, ints);
 1: 612.     drivecb_ptr := pointer(parameters^.configptr^.cb_addr);
 1: 613.     if drivecb_ptr = nil then
 1: 614.     begin  { not initialized yet }
 1: 615.            errnum := errbase + 3;  { insufficeint sysglobal space }
 1: 616.            if GETSPACE(sizeof(hdisk_cb), b_sysglobal_ptr, newdrivecb) then
 1: 617.              if GETSPACE(sizeof(reqblk), b_sysglobal_ptr, newreqblk) then
 1: 618.              begin
 1: 619.                     errnum := 0;
 1: 620.                     parameters^.configptr^.cb_addr := newdrivecb;
 1: 621.                     drivecb_ptr := pointer(newdrivecb);
 1: 622.                     with drivecb_ptr^ do
 1: 623.                     begin
 1: 624.                       config_addr := parameters^.configptr;
 1: 625.                       ext_ptr := ord(nil);
 1: 626.                       v_flag := false;  { default is don't re-read all writes }
 1: 627.                       total_restarts := 0;
 1: 628.                       cur_num_requests := 0;
 1: 629.                       restrt_limit := 4;  { max times an I/O is retried }
 1: 630.                       if chan >= 0 then int_prio := slotints
```

```
1: 631.                                                 else int_prio := winints;
1: 632.                       dummy_req_ptr := pointer(newreqblk);
1: 633.                       req_hd_ptr := dummy_req_ptr;
1: 634.                       with dummy_req_ptr^ do
1: 635.                       begin
1: 636.                             dev_chain.fwd_link := newreqblk - b_sysglobal_ptr +
1: 637.                                                   sizeof(rb_headT);
1: 638.                             dev_chain.bkwd_link := dev_chain.fwd_link;
1: 639.                             reqspec_info := -1;  { initialize dummy cylinder # }
1: 640.                             reqstatus.reqsrv_f := active;
1: 641.                             reqstatus.reqabt_f := false;
1: 642.                             block_p_f := false
1: 643.                       end;
1: 644.                     end;
1: 645.
1: 646.                     { initialize device specific driver }
1: 647.                     with parameters^ do
1: 648.                     begin   { reuse parameter block }
1: 649.                       fnctn_code := hdinit;
1: 650.                       CALLDRIVER(errnum, configptr, parameters);
1: 651.                     end; { with }
1: 652.               end;
1: 653.             end;
1: 654.             INTSON(ints);
1: 655.
1: 656.             {$IFC debug1}
1: 657.             if TRACE(DD,1) then
1: 658.               if errnum <> 0 then
1: 659.               begin
1: 660.                     with parameters^.configptr^ do
1: 661.                     begin
1: 662.                       writeln('Error ', errnum, ' initializing hard disk: ');
1: 663.                       writeln('Slot=', slot_no, ' chan=', iochannel, ' dev=', device_no);
1: 664.                     end;
1: 665.               end;
1: 666.             {$ENDC}
1: 667.
1: 668.             HDISKIO := errnum;
1: 669.       end; { hinitit }
1: 670.
1: 671.       {*********************************************************************}
1: 672.
1: 673.       {$S krgenio}
1: 674.
1: 675.       begin {hdiskio }
1: 676.             drivecb_ptr := pointer(parameters^.configptr^.cb_addr);
1: 677.
1: 678.             case parameters^.fnctn_code of
1: 679.
1: 680.             dskio: begin
1: 681.               HDISKIO := 0;  { no errors }
1: 682.               with parameters^.req^ do
1: 683.               begin
1: 684.                     ext_ptr := pointer(req_extent);
1: 685.                     new_sec := ext_ptr^.blkno;
1: 686.                     reqspec_info := new_sec;
1: 687.               end;
1: 688.
1: 689.               {$IFC debug1}
1: 690.               if TRACE(DD, 10) then
1: 691.                     if ext_ptr^.read_flag then
1: 692.                       writeln('Reading Profile block# = ', new_sec)
1: 693.                     else
1: 694.                       writeln('Writing Profile block# = ', new_sec);
1: 695.               {$ENDC}
1: 696.
1: 697.
1: 698.               {* Add the new request to the device queue, sorted by sector
1: 699.                     * Every queue contains a 'dummy' request whose sector number is either
1: 700.                     * smaller than any valid sector (-1) or larger than any valid sector
1: 701.                     * ($7FFFFFFF). The sector numbers of the requests along the device queue
1: 702.                     * monotonically increase or decrease until the dummy is reached. The sector
1: 703.                     * numbers then reverse directions and monotonically decrease or increase
1: 704.                     * until they reach the same sector number as the request at the head of
1: 705.                     * the queue. This ordering of the queue allows the interrupt handler to
1: 706.                     * pull the next request from the top of the queue, maintaining optimal seek
```

```
1: 707.                  * ordering. The drive will pick up all requests which are in the same
1: 708.                  * direction of head travel, before it services requests in the other direction.
1: 709.                  * The dummy request is at the head of the queue only when the queue is empty. *}
1: 710.
1: 711.            with drivecb_ptr^ do
1: 712.            begin
1: 713.                   INTSOFF(int_prio, ints);
1: 714.                   left_sort_ptr := req_hd_ptr;
1: 715.                   head_sort_ptr := left_sort_ptr;  { remember where we started }
1: 716.                   if cur_num_requests = 0 then  { point head to new req }
1: 717.                     req_hd_ptr := parameters^.req
1: 718.                   else
1: 719.                   begin
1: 720.                     left_sec := left_sort_ptr^.reqspec_info;
1: 721.                     if left_sec = new_sec then  { past dummy if same as current sector }
1: 722.                            left_sort_ptr := dummy_req_ptr;
1: 723.                     right_sort_ptr := CHAIN_FORWARD(left_sort_ptr^.dev_chain);
1: 724.                     right_sec := right_sort_ptr^.reqspec_info;
1: 725.
1: 726.                     { look for insertion point }
1: 727.
1: 728.                     while (right_sort_ptr <> head_sort_ptr) and
1: 729.                            ((right_sec = new_sec) or ((new_sec > right_sec) and (new_sec > left_sec))
1: 730.                              or ((new_sec < right_sec) and (new_sec < left_sec))) do
1: 731.                     begin  { chain forward one link if not at insertion point }
1: 732.                            left_sort_ptr := right_sort_ptr;
1: 733.                            left_sec := right_sec;
1: 734.                            right_sort_ptr := CHAIN_FORWARD(left_sort_ptr^.dev_chain);
1: 735.                            right_sec := right_sort_ptr^.reqspec_info;
1: 736.                     end
1: 737.                   end;
1: 738.
1: 739.                   { insert it }
1: 740.
1: 741.                   cur_num_requests := cur_num_requests + 1;
1: 742.                   ENQUEUE(parameters^.req^.dev_chain, left_sort_pointer^.dev_chain,
1: 743.                          b_sysglobal_ptr);
1: 744.                   if cur_num_requests = 1 then
1: 745.                   begin
1: 746.                     if int_prio = winints then { built-in parallel port }
1: 747.                            DISKSYNC(true);  { prevent contrast changes during I/O }
1: 748.                     START_NEW_REQUEST(drivecb_ptr)
1: 749.                   end;
1: 750.                   INTSON(ints)
1: 751.            end { with }
1: 752.          end; { dskio }
1: 753.
1: 754.          dinit: HINITIT;  { call internal subroutine in initialize segment }
1: 755.
1: 756.          ddown:  { down a device }
1: 757.            begin
1: 758.                   if parameters^.configptr^.slot_no >= 0 then INTSOFF(slotints, ints)
1: 759.                                 else INTSOFF(winints, ints);
1: 760.                   if drivecb_ptr^.cur_num_requests <> 0
1: 761.                   then hdiskio:= errbase { + ???? }
1: 762.                   else
1: 763.                   begin { no requests outstanding - shut the device down }
1: 764.                     with parameters^ do
1: 765.                     begin { reuse parameter block }
1: 766.                            fnctn_code := hddown;
1: 767.                            CALLDRIVER(errnum, configptr, parameters);
1: 768.                     end;
1: 769.                     RELSPACE(ord(drivecb_ptr^.req_hd_ptr), b_sysglobal_ptr);
1: 770.                     RELSPACE(ord(drivecb_ptr), b_sysglobal_ptr);
1: 771.                     parameters^.configptr^.cb_addr := ord(nil);
1: 772.                     hdiskio := 0;
1: 773.                   end;
1: 774.                   INTSON(ints)
1: 775.            end; { ddown }
1: 776.
1: 777.          reqrestart: begin
1: 778.            HDISKIO := 0;
1: 779.
1: 780.            {$IFC debug2}
1: 781.            if TRACE(DD, 5) then
1: 782.                   writeln('        Profile restarting request.');
```

```
 1: 783.              {$ENDC}
 1: 784.
 1: 785.              INTSOFF(drivecb_ptr^.int_prio, ints);
 1: 786.              START_NEW_REQUEST(drivecb_ptr);  { no other requests present }
 1: 787.              INTSON(ints);
 1: 788.              end; {reqrestart}
 1: 789.
 1: 790.            otherwise
 1: 791.              HDISKIO := errbase +6; { illegal function code or not implemented yet }
 1: 792.
 1: 793.     end; { case }
 1: 794.   end; {HDISKIO}
 1: 795.
 1: 796.
 1: 797.  end.
 1: 798.
 1: 799.

End of File: HDISK.TEXT


Directory of files in Cross Reference:

 1: HDISK.TEXT
level =  1.

abspage 1: 213,   1: 214=,  1: 214,   1: 222.
absptr  1: 605.
active  1: 640.
adj_io_c 1: 162.
asynctr 1:         32.
b_sysglo 1: 171,   1: 616,  1: 617,   1: 636,          1: 743,   1: 769,          1: 770.
bkwd_lin 1: 638=.
bkwdlink 1: 213=.
blkno   1: 274=,  1: 274,   1: 379=,  1: 379,          1: 477,   1: 685.
block_p_ 1: 642=.
buff_off 1: 527.
buff_rdb 1: 161,   1: 162,  1: 527.
c_cmd   1: 475=,  1: 476=.
c_sector 1: 477=.
calldriv 1: 479,   1: 650,  1: 767.
cb_addr 1: 612,   1: 620=,  1: 676,   1: 771=.
chain_fo 1: 172,   1: 180,  1: 723,   1: 734.
chained_ 1: 140,   1: 242,  1: 282,   1: 344,          1: 387,   1: 547.
chan    1: 321*,  1: 413=,  1: 603*,  1: 609=,         1: 610,   1: 630.
code    1: 318*,  1: 408=.
config_a 1: 410,   1: 479,  1: 624=.
configpt 1:        39*,  1:  41*,    1: 100,   1: 609,          1: 612,   1: 620,          1: 624,   1: 650,
         1: 660,   1: 676,          1: 758,   1: 767,          1: 771.
cserr   1:         67*,  1: 268,     1: 368,   1: 428,          1: 431.
cur_info 1: 131,   1: 237,  1: 279,   1: 328,          1: 384,   1: 467,          1: 515=,  1: 522.
cur_num_ 1: 173=,  1: 173,  1: 174,   1: 628=,         1: 716,   1: 741=,         1: 741,   1: 744,
         1: 760.
databad 1: 268,   1: 368.
datamayb 1: 266,   1: 366.
datastat 1: 265,   1: 365.
dataused 1: 141,   1: 219=,          1: 257,   1: 360.
dd        1: 155,   1: 657,          1: 690,   1: 781.
ddown   1: 756.
dequeue 1: 171.
dev       1: 324*,  1: 414=.
dev_chai 1: 171,   1: 172,  1: 180,   1: 636,          1: 638,   1: 638,          1: 723,   1: 734,
         1: 742,   1: 742.
device_n 1: 414,   1: 663.
dinit   1: 754.
disk_ext 1: 190.
disk_log 1: 317*,  1: 406,  1: 418.
disksync 1: 186,   1: 747.
drivecb_ 1:        44*,  1:  46*,    1:       78*,  1:  80*,    1: 106*,  1: 131,          1: 182,   1: 237,
         1: 328,   1: 331,          1: 337,   1: 390,          1: 393,   1: 425,          1: 435,   1: 467,
         1: 509,   1: 518,          1: 550,   1: 584*,         1: 612=,  1: 613,          1: 621=,  1: 622,
         1: 676=,  1: 711,          1: 748,   1: 760,          1: 769,   1: 770,          1: 785,   1: 786.
driverde 1:        22.
dskio   1: 680.
dummy_re 1: 176,   1: 178,  1: 178,   1: 180,          1: 632=,  1: 633,          1: 634,   1: 722.
enqueue 1: 742.
```

Procedures

Functions

```
hdiskio 1:        50*,  1: 101,     1: 558*,  1: 668=,        1: 681=,  1: 761=,        1: 772=,  1: 778=,
         1: 791=.
logging 1:        74*,  1: 405.
okxferne 1:       46*,  1: 227*,    1: 288=.

Declaration Character : '*'
Assignment Character :  '='
```

Cross Reference Listing:

Beginning of file: PROFILE.TEXT

```
 1:      1.  UNIT PROFDRVR;
 1:      2.
 1:      3.
 1:      4.                  { By Wendell Henry 1/17/83 }
 1:      5.                  { A device specific driver for the Profile. It uses the generic hard }
 1:      6.                  { disk driver HDISK }
 1:      7.
 1:      8.                  { By Wendell Henry 8/23/83 }
 1:      9.                  { The driver has been extended to support the Widget device }
 1:     10.
 1:     11.
 1:     12.    INTERFACE
 1:     13.
 1:     14.         USES
 1:     15.           {$U object/driverdefs.obj}
 1:     16.                 driverdefs,
 1:     17.           {$U object/hwint.obj}
 1:     18.                 hwint,
 1:     19.           {$U object/sysglobal.obj}
 1:     20.                 globaldata,
 1:     21.           {$U object/procprims.obj}
 1:     22.                 proc_prims,
 1:     23.           {$U object/mmprim.obj}
 1:     24.                 mmprimitives,
 1:     25.           {$U object/synctr.obj}
 1:     26.                 asynctr,
 1:     27.           {$U object/genio.obj}
 1:     28.                 genio,
 1:     29.           {$U object/hdisk.obj}
 1:     30.                 hdisk;
 1:     31.
 1:     32.
 1:     33.           function PROFILE(parameters: param_ptr): integer;
 1:     34.
 1:     35.    IMPLEMENTATION
 1:     36.
 1:     37.    CONST
 1:     38.         errbase = 650;
 1:     39.         countlimit = 100;         { 100 = 5 secs before timeout on 2-port }
 1:     40.                                                 {         = 12 secs on parallel }
 1:     41.         wait_int = 1;             { "wait for next interrupt" error code }
 1:     42.         ignorerr = 661;  { error code when get timer error requiring no action }
 1:     43.         hd_err = 654;
 1:     44.         cserr = -663;
 1:     45.
 1:     46.         { Drive types }
 1:     47.         T_Profile = 0;
 1:     48.         T_Seagate = 1;
 1:     49.         T_Widget = 2;
 1:     50.
 1:     51.         { Driver commands }
 1:     52.         Readcmd = 0;
 1:     53.         Writecmd = 1;
 1:     54.         Formatcmd = 2;
 1:     55.
 1:     56.    TYPE
 1:     57.         { Extension to Drive_cb for Profile }
 1:     58.
 1:     59.         (* NOTE: definitions marked with {**} have corresponding definitions in PROFASM *)
 1:     60.
 1:     61.         ext_drive_cb = record     { driver info for Profile hard disk }
 1:     62.                   {**} hwbase: longint;             { base address of 6522 }
 1:     63.                   {**} hwstatus: longint;                   { addr of 6522 status register B }
 1:     64.                   {**} remap_interleave: boolean; { true if interleave remapped }
 1:     65.                   {**} command_buffer: record       { command buffer for profile }
 1:     66.                   {**}   case integer of
 1:     67.                   {**}  0: (cmd: 0..2);    { set value after storing "sector" }
 1:     68.                   {**}  1: (sector: longint; retry_cnt: int1; sparing_thresh:int1);
 1:     69.                   {**}   end;
 1:     70.                   {**} checksum: integer;                   { temp storage during write }
 1:     71.                   {**} expect_hs: int1;             { internal storage for profasm }
```

```
1:     72.                  {**} drivetype: int1;            { type of disk drive }
1:     73.                  {**} asm_state: integer;         { state internal to assemb lang }
1:     74.                  {**} errstat: longint;           { error returned from profile }
1:     75.                  {**} asmerr: integer;            { err returned from assmbly routine }
1:     76.                  {**} last_hard_error: longint; { last error from errstat }
1:     77.                  {**} counter: integer;           { used for timeout }
1:     78.                  {**} discsize: longint;                  { device size from controller }
1:     79.                  {**} cxfercnt: int1;             { current transfer count }
1:     80.                  {**} csum_valid: int1;           { true if precomputed checksum valid }
1:     81.                  {**} nested_bdr: int1;           { true if bad response being handled }
1:     82.                  {**} extstat: array[0..15] of int1; { 16 bytes for extended status }
1:     83.                  {**} cmd_buf: array[0..7] of int1;  { 8 bytes for sending widget cmds }
1:     84.          end;
1:     85.
1:     86.    ptr_ext_drive_cb = ^ext_drive_cb;
1:     87.
1:     88.    function GP_STATUS(via_addr: longint; var counter:integer): integer; external;
1:     89.
1:     90.    procedure PROFASM(control_block: hdiskcb_ptr); external;
1:     91.
1:     92.    function PROF_INIT(ext_cb: ptr_ext_drive_cb; via_addr: longint;
1:     93.                   hwst_irb: longint; hwst_ddrb: longint): integer; external;
1:     94.
1:     95.    procedure START_NONIO(var errnum: integer; drivecb_ptr: hdiskcb_ptr);
1:     96.         {*******************************************}
1:     97.         {* Issue non-I/O request to low level driver *}
1:     98.         {*******************************************}
1:     99.         var
1: 100.     extdrivecb_ptr: ptr_ext_drive_cb;
1: 101.     reqptr: reqptr_type;
1: 102.     bufaddr: longint;
1: 103.
1: 104.   begin
1: 105.     extdrivecb_ptr := pointer(drivecb_ptr^.ext_ptr);
1: 106.     with drivecb_ptr^, extdrivecb_ptr^, req_hd_ptr^ do
1: 107.     begin
1: 108.           reqstatus.reqsrv_f := in_service;
1: 109.           cur_info_ptr := pointer(req_extent);
1: 110.           { process format request }
1: 111.           command_buffer.sector := ord(req_hd_ptr)+sizeof(reqblk); { data address }
1: 112.           command_buffer.cmd := operatn;
1: 113.           asm_state := 0;
1: 114.           PROFASM(drivecb_ptr);
1: 115.           errnum := asmerr;
1: 116.           if errnum <> 0 then
1: 117.             if errnum = wait_int then
1: 118.                   counter := countlimit;
1: 119.     end; { with }
1: 120.   end; { START_NONIO }
1: 121.
1: 122.
1: 123.    procedure NONIO_REQ(var errnum: integer; cmd: integer; devconfig: ptrdevrec);
1: 124.   {*****************************}
1: 125.   {* Build a request for Format *}
1: 126.   {*****************************}
1: 127.   var
1: 128.     drivecb_ptr: hdiskcb_ptr;
1: 129.     reqaddr: absptr;
1: 130.     reqptr: reqptr_type;
1: 131.     ints: intson_type;
1: 132.     leftlink: reqptr_type;
1: 133.     space: integer;
1: 134.
1: 135.   begin
1: 136.     drivecb_ptr := pointer(devconfig^.cb_addr);
1: 137.     errnum := errbase + 3;  { insufficient sysglobal space }
1: 138.     space := sizeof(reqblk);
1: 139.     if cmd = formatcmd then
1: 140.           space := space + 512; { extra space for spare table map }
1: 141.     if GETSPACE(space, b_sysglobal_ptr, reqaddr) then
1: 142.     begin
1: 143.           errnum := 0;
1: 144.           reqptr := pointer(reqaddr);
1: 145.           with reqptr^, drivecb_ptr^ do
1: 146.           begin
1: 147.             cfigptr := devconfig;
```

```
 1: 148.             operatn := cmd;
 1: 149.             req_extent := ord(nil);
 1: 150.             with list_chain do
 1: 151.             begin
 1: 152.                    fwd_link := ord(@fwd_link) - b_sysglobal_ptr;
 1: 153.                    bkwd_link := fwd_link;
 1: 154.             end;
 1: 155.             LINK_TO_PCB(reqptr);
 1: 156.
 1: 157.             { add to device queue }
 1: 158.             INTSOFF(int_prio, ints);
 1: 159.             reqspec_info := req_hd_ptr^.reqspec_info;
 1: 160.             leftlink := pointer(req_hd_ptr^.dev_chain.bkwd_link -
 1: 161.                           sizeof(rb_headT) + b_sysglobal_ptr);
 1: 162.             if req_hd_ptr = dummy_req_ptr then
 1: 163.                    req_hd_ptr := reqptr;     { place at head }
 1: 164.             cur_num_requests := cur_num_requests + 1;
 1: 165.             ENQUEUE(dev_chain, leftlink^.dev_chain, b_sysglobal_ptr);
 1: 166.             if cur_num_requests = 1 then
 1: 167.                    START_NONIO(errnum, drivecb_ptr);
 1: 168.             INTSON(ints);
 1: 169.
 1: 170.             if errnum = wait_int then
 1: 171.             begin
 1: 172.                    BLK_REQ(reqptr, reqptr);
 1: 173.                    { process will block until the operation is done }
 1: 174.
 1: 175.                    if reqptr^.reqstatus.reqsuccess_f
 1: 176.                      then errnum := 0
 1: 177.                      else errnum := hd_err;
 1: 178.             end;
 1: 179.             CANCEL_REQ(reqptr);     { this will also release data space }
 1: 180.           end; { with }
 1: 181.     end;
 1: 182.   end; {NONIO_REQ}
 1: 183.
 1: 184.
 1: 185.    function PROFILE (*parameteres: param_ptr): integer *);
 1: 186.   {*********************************************************}
 1: 187.   {*
*}
 1: 188.   {* Description: PROFILE device-dependent user I/O request     *}
 1: 189.   {*
*}
 1: 190.   {* Input Parameters: Rqst is the partially initialized        *}
 1: 191.   {*   request block. Blno is the s-file block number.                  *}
 1: 192.   {*   Rast^.reqspec_info[2] will hold the cylinder number.     *}
 1: 193.   {*   Rqst^.reqspec_info[1] points to disk-extend record       *}
 1: 194.   {*      which holds info necessary to do I/O to the disk.     *}
 1: 195.   {*
*}
 1: 196.   {* Output Parameters: none                                                    *}
 1: 197.   {*
*}
 1: 198.   {* Side Effects: The request is added to the queue for the *}
 1: 199.   {*   appropriate drive. If no request is busy, the request *}
 1: 200.   {*   is started up immediately.                                        *}
 1: 201.   {*
*}
 1: 202.   {* Special Conditions of Use: none                                     *}
 1: 203.   {*
*}
 1: 204.   {* Error Conditions:                                                          *}
 1: 205.   {*
*}
 1: 206.   {*********************************************************}
 1: 207.
 1: 208.    label 20;
 1: 209.
 1: 210.    var
 1: 211.   hwddrb: longint;
 1: 212.   p_extdevrec: ^ext_diskconfig;
 1: 213.   port_ptr: hdiskcb_ptr;
 1: 214.   newextdrivecb: absptr;
 1: 215.   p_extdrivecb: ptr_ext_drive_cb;
 1: 216.   p_dc_rec: ^dc_rec;
```

```
1: 217.   ints: intson_type;
1: 218.   error: int2;
1: 219.   parm: params;
1: 220.   response: integer;
1: 221.   i: integer;
1: 222.   operation: integer;
1: 223.
1: 224.    begin { PROFILE DRIVER }
1: 225.   with parameters^ do
1: 226.   begin
1: 227.
1: 228.     port_ptr := pointer(configptr^.cb_addr);
1: 229.
1: 230.     { Preliminaries are done - now process function request }
1: 231.     case fnctn_code of
1: 232.
1: 233.     dinit: { first level of device initialization }
1: 234.            begin
1: 235.              USE_HDISK(configptr);  { Use HDISK for handling sector headers }
1: 236.              CALL_HDISK(error, configptr, parameters);
1: 237.              profile := error;
1: 238.            end; { dinit }
1: 239.
1: 240.     hdinit:        { now initialize device }
1: 241.            begin
1: 242.              profile := errbase + 3;          { insufficient sysglobal space }
1: 243.              if GETSPACE(sizeof(ext_drive_cb), b_sysglobal_ptr, newextdrivecb) then
1: 244.              begin
1: 245.                     p_extdrivecb := pointer(newextdrivecb);
1: 246.                     with p_extdrivecb^, configptr^ do
1: 247.                     begin
1: 248.                       port_ptr^.ext_ptr := newextdrivecb;
1: 249.                       p_extdevrec := pointer(ext_addr);
1: 250.                       if iochannel >= 0 then
1: 251.                       begin  { attached to multi-port card }
1: 252.                              hwbase := iospacemmu * $20000 + ord4($4000)*slot_no +
1: 253.                                       $2001 + $800 * iochannel;
1: 254.                              hwstatus := hwbase;
1: 255.                              hwddrb := hwstatus + $10;
1: 256.                       end
1: 257.                       else
1: 258.                       begin  { attached to built-in port }
1: 259.                              hwbase := iospacemmu*$20000 + $0D801;
1: 260.                              hwstatus := hwbase + $400;
1: 261.                              hwddrb := hwstatus + 4;
1: 262.                       end;
1: 263.                       nested_bdr := 0;
1: 264.                       for i := 0 to 15 do extstat[i] := 0;
1: 265.                       discsize := 0;
1: 266.                       { assume the device is a profile }
1: 267.                       with p_extdevrec^ do num_bloks := 9720;
1: 268.                       last_hard_err := 0;
1: 269.                       command_buffer.retry_cnt := 10;
1: 270.                       command_buffer.sparing_thresh := 3;
1: 271.                       remap_interleave := true;
1: 272.                       if iochannel < 0 then DISKSYNC(true);
1: 273.                       response := -PROF_INIT(p_extdrivecb, hwbase, hwstatus, hwddrb);
1: 274.                       if iochannel < 0 then DISKSYNC(false);
1: 275.                       profile := response;
1: 276.                       if (response <> 0) or (discsize <= 0) or (discsize > 30000)
1: 277.                              then drivetype := T_Profile { set dirvetype to profile }
1: 278.                              else
1: 279.                              begin
1: 280.                                with p_extdevrec^ do num_bloks := discsize-strt_blok;
1: 281.                                if drivetype <> 0 then
1: 282.                                begin  { widget }
1: 283.                                       remap_interleave := false;
1: 284.                                       drivetype := T_Widget;
1: 285.                                       {****************** TEMPORARY
1: 286.                                       remap_interleave := true;
1: 287.                                       drivetype := 1;
1: 288.                                       ****************** TEMPORARY}
1: 289.                                end
1: 290.                                else
1: 291.                                begin  { Profile or Seagate }
1: 292.                                       if discsize > 9728 then drivetype := T_Seagate;  { 10MB Seagate }
```

```
1: 293.                             end;
1: 294.                           end;
1: 295.                   end; { with }
1: 296.             end;
1: 297.           end; {hdinit}
1: 298.
1: 299.     hdskio:       { start I/O to disk }
1: 300.           begin
1: 301.             operation := port_ptr^.req_hd_ptr^.operatn;
1: 302.             if operation = formatcmd then
1: 303.             begin
1: 304.                   START_NONIO(error, port_ptr);
1: 305.                   profile := error;
1: 306.             end
1: 307.             else
1: 308.             begin
1: 309.                   p_extdrivecb := pointer(port_ptr^.ext_ptr);
1: 310.                   with p_extdrivecb^ do
1: 311.                   begin
1: 312.                     command_buffer.sector := c_sector;
1: 313.                     command_buffer.cmd := c_cmd;  { set after sector is stored! }
1: 314.                     asm_state := 0;         { initialize state machine }
1: 315.                     PROFASM(port_ptr);  { call state machine }
1: 316.                     profile := asmerr;  { return driver status }
1: 317.                     if asmerr <> 0 then
1: 318.                           if asmerr = wait_int
1: 319.                             then counter := countlimit { timeout limit }
1: 320.                             else IODONE(port_ptr, asmerr);  { unexpected error }
1: 321.                   end;
1: 322.             end;
1: 323.           end; {hdskio}
1: 324.
1: 325.     dinterrupt:    { Interrupt from device }
1: 326.           begin
1: 327.             with port_ptr^ do
1: 328.             begin
1: 329.                   p_extdrivecb := pointer(ext_ptr);
1: 330.                   with p_extdrivecb^ do
1: 331.                   begin
1: 332.                     asmerr := GP_STATUS(hwbase, counter);  { get profile status }
1: 333.                     profile := asmerr;  { return status }
1: 334.                     if (cur_num_requests <> 0) then
1: 335.                     begin
1: 336.                           if asmerr > 0 then goto 20;  { handle error }
1: 337.                           PROFASM(port_ptr);
1: 338.                           profile := asmerr;  { return status }
1: 339.                           if asmerr = wait_int
1: 340.                             then counter := countlimit  { timeout limit }
1: 341.                             else
1: 342.                             begin  { unexpected error }
1: 343.     20:                           if asmerr <> ignorerr then
1: 344.                                   begin
1: 345.                                     if errstat <> 0 then
1: 346.                                           if (asmerr = hd_err) or (asmerr = cserr)
1: 347.                                             then last_hard_error := errstat;
1: 348.                                     IODONE(port_ptr, asmerr);
1: 349.                                   end;
1: 350.                           end;
1: 351.                     end;
1: 352.                   end; { with p_extdrivecb }
1: 353.             end; { with port_ptr }
1: 354.           end; {dinterrupt}
1: 355.
1: 356.     dskunclamp:    { Unclamp request }
1: 357.           begin
1: 358.             profile := 685;         { not an ejectable media }
1: 359.           end; {dskunclamp}
1: 360.
1: 361.     dcontrol:  { Device control }
1: 362.           begin
1: 363.             begin
1: 364.                   p_dc_rec := pointer(parptr);
1: 365.                   p_extdrivecb := pointer(port_ptr^, ext_ptr);
1: 366.                   with port_ptr^, p_dc_rec^, p_extdrivecb^ do
1: 367.                   begin
1: 368.                     if dversion <> 2
```

```
 1: 369.                            then profile := errbase + 7  { wrong application version }
 1: 370.                            else
 1: 371.                            begin
 1: 372.                              profile := 0;
 1: 373.                              INTSOFF(int_prio, ints);
 1: 374.                              case dcode of
 1: 375.                              15: begin  { return }
 1: 376.                                                    { o last error from profile controller }
 1: 377.                                                    { o state-machine restart count             }
 1: 378.                                        ar10[0] := last_hard_error;
 1: 379.                                        ar10[1] := total_restarts;
 1: 380.                                      end;
 1: 381.                              20: begin  { get disk status }
 1: 382.                                        ar10[0] := 4;    { good disk }
 1: 383.                                        ar10[1] := 0;
 1: 384.                                        ar10[2] := 999;  { don't know how many blocks }
 1: 385.                                                                    {  are available
}
 1: 386.                                        ar10[3] := 0;
 1: 387.                                        ar10[4] := 1;    { sparing always enabled }
 1: 388.                                        ar10[5] := ord(command_buffer.sparing_thresh
 1: 389.                                                                    <= 10);
 1: 390.                                        ar10[6] := ord(v_flag);
 1: 391.                                      end;
 1: 392.                              21: begin  { enable/disable sparing }
 1: 393.                                        if ar10[1] = 0 then  { don't rewrite sort errs }
 1: 394.                                          command_buffer.sparing_thresh := 11
 1: 395.                                        else  { rewrite if > 30% err rate }
 1: 396.                                          command_buffer.sparing_thresh := 3;
 1: 397.                                        v_flag := (ar10[2] <> 0);          { verify all writes }
 1: 398.                                      end;
 1: 399.                              otherwise  profile := errbase + 7;
 1: 400.                            end; { case }
 1: 401.                            INTSON(ints);
 1: 402.                        end;
 1: 403.                      end; { with port_ptr, p_dc_rec, p_extdrivecb }
 1: 404.              end;
 1: 405.            end;  {dcontrol}
 1: 406.
 1: 407.     dskformat:  { format disk }
 1: 408.            begin
 1: 409.              p_extdrivecb := pointer(port_ptr^, ext_ptr);
 1: 410.              if p_extdrivecb^.drivetype < T_Widget
 1: 411.                     then error := 0  { disk already formatted }
 1: 412.                     else NONIO_REQ(error, Formatcmd, configptr);  { Widget can be formatted }
 1: 413.              profile := error
 1: 414.            end; {dskformat}
 1: 415.
 1: 416.     otherwise { pass the function on to HDISK }
 1: 417.            begin
 1: 418.              CALL_HDISK(error, configptr, parameters);
 1: 419.              profile := error;
 1: 420.            end;
 1: 421.
 1: 422.     end; { case }
 1: 423.   end; { with }
 1: 424.    end; { PROFILE DRIVER }
 1: 425.  end.
 1: 426.
 1: 427.

End of File: PROFILE.TEXT


Directory of files in Cross Reference:

 1: PROFILE.TEXT                                                                                                    level =  1.

absptr  1: 129,   1: 214.
ar10    1: 378=,  1: 379=,  1: 382=,  1: 383=,         1: 384=,  1: 386=,         1: 387=,  1: 388=,
          1: 390=,  1: 393,          1: 397.
asm_stat 1:        73*,  1: 113=,    1: 314=.
asmerr  1:         75*,  1: 115,     1: 316,   1: 317,          1: 318,   1: 320,          1: 332=,  1: 333,
          1: 336,   1: 338,          1: 339,   1: 343,          1: 346,   1: 346,          1: 348.
asynctr 1:         26.
b_sysglo 1: 141,   1: 152,  1: 161,   1: 165,          1: 243.
```

Procedures

```
nonio_re 1: 123*,  1: 412.
profasm 1:         90*,  1: 114,     1: 315,   1: 337.
start_no 1:        95*,  1: 167,     1: 304.

Functions

gp_statu 1:        88*,  1: 332.
prof_ini 1:        92*,  1: 273.
profile 1:         33*,  1: 185*,    1: 237=,  1: 242=,         1: 275=,  1: 305=,         1: 316=,  1: 333=,
          1: 338=,  1: 358=,         1: 369=,  1: 372=,         1: 399=,  1: 413=,         1: 419=.

Declaration Character : '*'
Assignment Character :  '='
```

Cross Reference Listing:

Beginning of file: RS232.TEXT

```
1:      1.
1:      2.  UNIT RS232;                          { built-in RS232 driver }
1:      3.
1:      4.          { By Dave Offen }
1:      5.          { Copyright 1983, Apple Computer Inc. }
1:      6.
1:      7.  INTERFACE
1:      8.
1:      9.    USES
1:     10.         {$U object/driverdefs.obj}
1:     11.           driverdefs,
1:     12.         {$U object/driversubs.obj}
1:     13.           driversubs;
1:     14.
1:     15.         function DRIVER( parameters: param_ptr): integer;
1:     16.
1:     17.  IMPLEMENTATION
1:     18.
1:     19.
1:     20.    {$IFC not debug2}
1:     21.    {$R-}  { rangecheck off unless debug mode }
1:     22.    {$ENDC}
1:     23.
1:     24.    CONST
1:     25.         errbase = 640;
1:     26.         axmit = 0;
1:     27.         amodem = 1;
1:     28.         arecv = 2;
1:     29.         aerr = 3;
1:     30.
1:     31.    TYPE
1:     32.         send_hs = (hw_hs, xmit_xon, xmit_delay);  { transmit handshake type }
1:     33.         recv_hs = (hwhs, rec_xon);  { receive handshake type }
1:     34.
1:     35.         int1ptr = ^int1;
1:     36.
1:     37.         int1recptr = ^int1rec;
1:     38.         int1rec = record
1:     39.                                    controlreg: int1;
1:     40.                             end;
1:     41.
1:     42.         portrec_ptr = ^portrec;
1:     43.         portrec = record
1:     44.                                    cur_req_ptr: reqptr_type;
1:     45.                                    cur_info_ptr: seqextptr_type;
1:     46.                                    cur_read_flag: boolean;
1:     47.                                    hwdata: int1ptr;  { actual I/O address }
1:     48.                                    hwcontrol: int1recptr;  { actual I/O address }
1:     49.                                    nulllink: linkage;  { points to head of queue }
1:     50.                                    openwr5: int1;
1:     51.                                    openwrl4: int1;
1:     52.                                    discon_detect: boolean;  { detect disconnect on framing error }
1:     53.                                    restrt_out, restrt_in: boolean;
1:     54.                                    prev_char: int1;
1:     55.                                    autolf: boolean;
1:     56.
1:     57.                                    { RS232 output variables }
1:     58.
1:     59.                                    xmt_hs: send_hs;  { handshake for transmitting }
1:     60.                                    in_break: boolean;  { when T, openwr5 has bit#4 set & alarm pending }
1:     61.                                    xmit_wait: boolean; { waiting for XON, modem signal or timeout }
1:     62.                                    xmit_ref: integer;  { timer refnum }
1:     63.
1:     64.                                    {xmt_hs = hw_hs or xmt_xon; (modem or xoff/xon handshake) }
1:     65.                                    xmit_timeout: longint;  { hardware handshake timeout }
1:     66.
1:     67.                                    {xmt_hs = hw_hs; (no handshake, or modem handshake) }
1:     68.                                    xmtrr0: int1;    { modem controls required }
1:     69.                                    xmtzrr0: int1; { modem required to = 0 }
1:     70.
1:     71.                                    {xmt_hs = xmit_delay. ( delay after CR, LF) }
```

```
1:     72.                                  waitb4next: boolean;  { cr or lf was xmitted }
1:     73.                                  crlfdelay: longint;  { timer count }
1:     74.
1:     75.                                  { RS232 input variables }
1:     76.
1:     77.                                  rec_hs: recv_hs;  { handshake for receiving }
1:     78.                                  no_block: boolean; { blocking/non-blocking read mode }
1:     79.                                  last_stopped: boolean; { DTR off or XOFF last sent }
1:     80.                                  size_typeah: 0..1024; { size of type-ahead input buffer }
1:     81.                                  prity_char: int1; { char to replace parity errs, or 01 if disabled }
1:     82.                                  num_typeah: integer; { number of chars currently in buffer }
1:     83.                                  full_thresh: integer; { threshold for sending "buffer-full" }
1:     84.                                  empty_thresh: integer; { threshold for sending "buffer-empty" }
1:     85.                                  start_typeah, get_typeah, put_typeah: int1ptr; { type-ahead ptrs }
1:     86.                                  end_typeah: int1ptr; { equals start_typeah+size_typeah }
1:     87.
1:     88.                                  {rec_hs = rec_xon. (send xoff/xon when threshold reached) }
1:     89.                                  hschar_pending: (no_char, xoff_char, xon_char);
1:     90.
1:     91.                              end;
1:     92.
1:     93.
1:     94.          procedure FINISH_REQ(port: portrec_ptr; ok_flag: boolean); forward;
1:     95.
1:     96.
1:     97.          procedure BYTEO(port: portrec_ptr);
1:     98.            {************************************************************}
1:     99.            {*
         *}
1: 100.      {* Description: Write one byte on RS232                                        *}
1: 101.      {*
       *}
1: 102.      {* Input Parameters: Port points to the current port in the          *}
1: 103.      {*             RS232 device control block.                                        *}
1: 104.      {*
       *}
1: 105.      {* Output Parameters: none                                                      *}
1: 106.      {*
       *}
1: 107.      {* Side Effects: Starts timer if hardware handshake requires *}
1: 108.      {*
       *}
1: 109.      {* Special Conditions of Use: RS232 interrupts must be off  *}
1: 110.      {*
       *}
1: 111.      {* Error Conditions:                                                           *}
1: 112.      {*
       *}
1: 113.      {************************************************************}
1: 114.
1: 115.    VAR
1: 116.      data_ptr: int1ptr;
1: 117.      status, err: integer;
1: 118.      real_addr: longint;
1: 119.
1: 120.    begin
1: 121.      with port^.hwcontrol^ do
1: 122.      begin
1: 123.             status := controlreg;     { read hardware control reg 0 }
1: 124.
1: 125.             if (hschar_pending <> no_char) then
1: 126.             begin   { need to send xoff or xon }
1: 127.               if ALLSET($04, status) then { output buffer is empty }
1: 128.               begin { send xoff or xon now }
1: 129.                      if hschar_pending = xoff_char then
1: 130.                      begin
1: 131.
1: 132.                        {$IFC debug2}
1: 133.                        if TRACE(DD, 1) then
1: 134.                               write('<xof>');
1: 135.                        {$ENDC}
1: 136.
1: 137.                        hwdata^ := $13; { xoff char }
1: 138.                      end
1: 139.                      else
1: 140.                      begin   { send xon }
```

```
1: 141.
1: 142.                       {$IFC debug2}
1: 143.                       if TRACE(DD, 1) then
1: 144.                             write('<xon>');
1: 145.                       {#ENDC}
1: 146.
1: 147.                       hwdata^ := $11;         { xon char }
1: 148.                     end;
1: 149.                     hschar_pending := no_char
1: 150.               end
1: 151.             end
1: 152.             else
1: 153.               if (xmt_hs <> hw_hs) or
1: 154.                     (ALLSET(xmtrr0, status) and ALLSET(xmtzrr0, -1-status)) then
1: 155.               begin { ready for xmit }
1: 156.                     if ALLSET($04, status) then { xmit buffer is empty }
1: 157.                       with cur_info_ptr^ do
1: 158.                       begin
1: 159.                             FREEZE_SEG(err, buff_rdb_ptr, buff_offset, ord(cur_req_ptr),
1: 160.                                   real_addr);
1: 161.                             if err <> 0 then
1: 162.                             begin
1: 163.                               controlreg := $28;  { clear output interrupts with $28 }
1: 164.                               restrt_out := true  { reqrestart will be called to continue }
1: 165.                             end
1: 166.                             else
1: 167.                             begin { freeze seg succeeded }
1: 168.                               if autolf and
1: 169.                                     ((prev_char = $0d) or (prev_char = -115{=$8D})) { bug 161 }
1: 170.                               then
1: 171.                                     prev_char := $0A { insert auto LF }
1: 172.                               else { no automatic line feed }
1: 173.                               begin
1: 174.                                     data_ptr := pointer(real_addr + xfer_count);
1: 175.                                     xfer_count := xfer_count + 1;
1: 176.                                     prev_char := data_ptr^;
1: 177.                               end;
1: 178.
1: 179.                               {$IFC debug2}
1: 180.                               if TRACE(DD, 1) then
1: 181.                                     write(CHR(prev_char));
1: 182.                               {$ENDC}
1: 183.
1: 184.                               hwdata^ := prev_char;  { do the write }
1: 185.
1: 186.                               if xmt_hs = xmit_delay then
1: 187.                                     if (prev_char = $0d) or (prev_char = $0a) then
1: 188.                                       waitb4next := true;     { start delay after CR or LF interrupts }
1: 189.
1: 190.                                     UNFREEZE_SEG(buff_rdb_ptr)  { allow buffer to move again }
1: 191.                             end
1: 192.                       end
1: 193.               end
1: 194.               else { not ready for xmit }
1: 195.               begin { allow delay for hardware handshake }
1: 196.
1: 197.                     {$IFC debug2}
1: 198.                     if TRACE(DD, 5) then
1: 199.                       writeln(' RS-232 stop xmit for hw handshake. ');
1: 200.                     {$ENDC}
1: 201.
1: 202.                     if xmit_timeout > 0 then
1: 203.                       ALARMRELATIVE(xmit_ref, xmit_timeout);
1: 204.                     xmit_wait := true;
1: 205.                     controlreg := $28         { clear output interrupts with $28 }
1: 206.               end
1: 207.     end
1: 208.   end;  {BYTEO}
1: 209.
1: 210.   procedureCOPYBYTES(port: portrec_ptr);
1: 211.     {*************************************************************}
1: 212.     {*
          *}
1: 213.     {* Description: Copy bytes from typeahead buffer to user buffer *}
1: 214.     {*
          *}
```

```
 1: 215.     {* Input Parameters: Port points to the current port in the            *}
 1: 216.     {*             RS232 device control block.                                    *}
 1: 217.     {*
           *}
 1: 218.     {* Output Parameters: none                                                    *}
 1: 219.     {*
           *}
 1: 220.     {* Side Effects:
*}
 1: 221.     {*
           *}
 1: 222.     {* Special Conditions of Use: RS232 interrupts must be off     *}
 1: 223.     {*
           *}
 1: 224.     {* Error Conditions:                                                          *}
 1: 225.     {*
           *}
 1: 226.     {************************************************************}
 1: 227.
 1: 228.   VAR
 1: 229.     i, errnum, cnt: integer;
 1: 230.     data_ptr: int1ptr;
 1: 231.     real_addr: longint;
 1: 232.
 1: 233.   begin
 1: 234.     with port^, cur_info_ptr^ do
 1: 235.           if num_typeah > 0 then
 1: 236.           begin
 1: 237.             FREEZE_SEG(errnum, buff_rdb_ptr, buff_offset, ord(cur_req_ptr),
 1: 238.                                real_addr);
 1: 239.             if errnum > 0 then
 1: 240.                   restrt_in := true
 1: 241.             else
 1: 242.             begin
 1: 243.                   data_ptr := pointer(real_addr + xfer_count);
 1: 244.                   cnt := num_bytes - xfer_count;
 1: 245.                   if cnt > num_typeah then
 1: 246.                     cnt := num_typeah;
 1: 247.
 1: 248.                   { transfer typed-ahead characters to user buffer }
 1: 249.
 1: 250.                   i := ord(get_typeah) + cnt - ord(end_typeah);        { wraparound chars }
 1: 251.                   if i < 0 then { transfer all chars in single move of length "cnt" }
 1: 252.                     i := cnt
 1: 253.                   else
 1: 254.                   begin { transfer requires two moves }
 1: 255.                     moveleft(get_typeah^, data_ptr^, cnt - i);
 1: 256.                     data_ptr := pointer(ord(data_ptr) + cnt - i);
 1: 257.                     get_typeah := start_typeah
 1: 258.                   end
 1: 259.                   moveleft(get_typeah^, data_ptr^, i);
 1: 260.                   get_typeah := pointer(ord(get_typeah) + i);
 1: 261.
 1: 262.                   UNFREEZE_SEG(buff_rdb_ptr);  { allow buffer to move }
 1: 263.                   num_typeah := num_typeah - cnt;
 1: 264.                   if last_stopped then
 1: 265.                     if num_typeah <= empty_thresh then { allow input }
 1: 266.                     begin
 1: 267.                           last_stopped := false;
 1: 268.                           if rec_hs = hwhs then
 1: 269.                           begin { toggle modem signals }
 1: 270.
 1: 271.                             {$IFC debug2}
 1: 272.                             if TRACE(DD, 5) then
 1: 273.                                   writeln(' RS-232 DTR turned on.');
 1: 274.                             {$ENDC}
 1: 275.
 1: 276.                             with hwcontrol^ do
 1: 277.                             begin
 1: 278.                                   controlreg := 5;
 1: 279.                                   i := 128;        { NOP to assure delay between accesses }
 1: 280.                                   controlreg := openwr5
 1: 281.                             end
 1: 282.                           end
 1: 283.                           else
 1: 284.                           begin   { send xon }
```

```
1: 285.                             hschar_pending := xon_char;
1: 286.                             BYTEO(port);
1: 287.                           end
1: 288.                     end;
1: 289.                   xfer_count := xfer_count + cnt
1: 290.             end;
1: 291.             if (xfer_count = num_bytes) or no_block then { read complete }
1: 292.                   FINISH_REQ(port, true)
1: 293.           end
1: 294.   end; { copybytes }
1: 295.
1: 296.
1: 297.   procedure START_NEW_REQUEST(port: portrec_ptr);
1: 298.     {*************************************************************}
1: 299.     {*
          *}
1: 300.     {* Description: Start a new read or write from the queue.      *}
1: 301.     {*
          *}
1: 302.     {* Input Parameters: Port points to the current port in the            *}
1: 303.     {*             RS232 device control block.                                          *}
1: 304.     {*
          *}
1: 305.     {* Output Parameters: none                                                          *}
1: 306.     {*
          *}
1: 307.     {* Side Effects:  Modem control signals are affected.                 *}
1: 308.     {*
          *}
1: 309.     {* Special Conditions of Use: RS232 interrupts must be off     *}
1: 310.     {*
          *}
1: 311.     {* Error Conditions:                                                                *}
1: 312.     {*
          *}
1: 313.     {*************************************************************}
1: 314.
1: 315.   begin
1: 316.     with port^ do
1: 317.     begin
1: 318.           cur_req_ptr := CHAIN_FORWARD(nullink);
1: 319.           cur_req_ptr^.reqstatus.reqsrv_f := in_service;
1: 320.           cur_info_ptr := pointer(cur_req_ptr^.req_extent);
1: 321.           with cur_info_ptr^ do
1: 322.           begin
1: 323.             cur_read_flag := read_flag;
1: 324.             if discon_detect and (hwcontrol^.controlreg < 0) then
1: 325.             begin  { assume disconnected cable on input framing error }
1: 326.                   xmit_wait := false;  { fixes bug }
1: 327.                   cur_req_ptr^.hard_error := errbase + 8;
1: 328.                   FINISH_REQ(port, false)
1: 329.             end
1: 330.             else { start up i/o for new request }
1: 331.                   if cur_read_flag then
1: 332.                     COPYBYTES(port)
1: 333.                   else { write }
1: 334.                   begin
1: 335.                     waitb4next := false;
1: 336.                     if (xmt_hs = xmt_xon) and xmit_wait then { last received xoff }
1: 337.                     begin
1: 338.                           if xmit_timeout > 0 then
1: 339.                             ALARMRELATIVE(xmit_ref, xmit_timeout)
1: 340.                     end
1: 341.                     else
1: 342.                           BYTEO(port)
1: 343.                   end
1: 344.           end
1: 345.     end
1: 346.   end; {START_NEW_REQUEST}
1: 347.
1: 348.   procedure FINISH_REQ(*port: portrec_ptr; ok_flag: boolean*);
1: 349.     {*************************************************************}
1: 350.     {*
          *}
1: 351.     {* Description: Complete the current request.                          *}
1: 352.     {*
```

```
          *}
1: 353.     {* Input Parameters: Port points to the current port in the            *}
1: 354.     {*             RS232 device control block. Ok_flag is param to         *}
1: 355.     {*             unlock request.                                                                  *}
1: 356.     {* Output Parameters: none                                                                      *}
1: 357.     {*
          *}
1: 358.     {* Side Effects:  Modem control signals are affected.                  *}
1: 359.     {*
          *}
1: 360.     {* Special Conditions of Use: Cur_req_ptr must be valid               *}
1: 361.     {*
          *}
1: 362.     {* Error Conditions:                                                                            *}
1: 363.     {*
          *}
1: 364.     {*************************************************************}
1: 365.
1: 366.   VAR
1: 367.     err: integer;
1: 368.     ptrsysg: ^longint;
1: 369.
1: 370.   begin
1: 371.     ptrsysg := pointer(bsysglob);  { set up pointer to b_sysglobal_ptr }
1: 372.     with port^, cur_info_ptr^ do
1: 373.     begin
1: 374.           ADJ_IO_CNT(false, buff_rdb_ptr);  { allow swapout }
1: 375.           UNBLK_REQ(cur_req_ptr, ok_flag);
1: 376.           DEQUEUE(cur_req_ptr^.dev_chain, ptrsysg^);
1: 377.           if xmit_wait then
1: 378.             ALARMOFF(xmit_ref);
1: 379.           if cur_read_flag then
1: 380.             restrt_in := false  { prevent reqrestart after completion. fixes bug }
1: 381.           else { write }
1: 382.             if hschar_pending = no_char then
1: 383.                   restrt_out := false;  { fixes bug }
1: 384.
1: 385.           {$IFC debug2}
1: 386.           if TRACE(DD, 10) then
1: 387.             writeln('RS-232 Completed. Success = ', ok_flag);
1: 388.           {$ENDC}
1: 389.
1: 390.           if (nullink.fwd_link + ptrsysg^) <> ord(@nullink) then
1: 391.             START_NEW_REQUEST(port)
1: 392.           else
1: 393.             cur_req_ptr := nil
1: 394.     end   { with }
1: 395.   end; { finish_req }
1: 396.
1: 397.
1: 398.   procedure XMIT_TO_HNDL(port: portrec_ptr);
1: 399.     {*************************************************************}
1: 400.     {*
          *}
1: 401.     {* Description: Timeout handler when waited too long for       *}
1: 402.     {*              resuming output to RS-232 channel A or B, or delaying *}
1: 403.     {*              after transmitting CR or LF.                                        *}
1: 404.     {*
          *}
1: 405.     {* Input Parameters: pointer to data for Port A or B                  *}
1: 406.     {*
          *}
1: 407.     {* Output Parameters: none                                                                      *}
1: 408.     {*
          *}
1: 409.     {* Side Effects:  Completes the request with an error                 *}
1: 410.     {*
          *}
1: 411.     {* Special Conditions of Use: Called with all ints off                *}
1: 412.     {*
          *}
1: 413.     {*
          *}
1: 414.     {* Error Conditions:                                                                            *}
1: 415.     {*
          *}
```

```
1: 416.     {*************************************************************}
1: 417.
1: 418.   VAR
1: 419.     ptrsysg: ^longint;
1: 420.
1: 421.   begin
1: 422.     with port^, cur_info_ptr^ do
1: 423.           if in_break then
1: 424.           begin   { remove from break_state }
1: 425.             in_break := false;
1: 426.             openwr5 := openwr5 - $10;  { disable break }
1: 427.             with hwcontrol^ do
1: 428.             begin
1: 429.                   controlreg := 5;
1: 430.                   if (rec_hs = hwhs) and last_stopped then
1: 431.                     controlreg := openwr5 - (-128)  { no DTR }
1: 432.                   else
1: 433.                     controlreg := openwr5
1: 434.             end { with }
1: 435.             ptrsysg := pointer(bsysglob);
1: 436.             if (nullink.fwd_link + ptrsysg^) <> ord(@nullink) then
1: 437.                   START_NEW_REQUEST(port)  { start next pending request }
1: 438.           end
1: 439.           else
1: 440.             if xmit_wait then
1: 441.                   if cur_req_ptr <> nil then
1: 442.                   begin
1: 443.                     if xmit_hs <> xmt_xon then
1: 444.                           xmit_wait := false;
1: 445.                     if xmt_hs = xmit_delay then      { time up after CR or LF }
1: 446.                           if xfer_count < num_bytes then
1: 447.                             BYTEO(port)    { send next byte out }
1: 448.                           else  {complete previous request }
1: 449.                             FINISH_REQ(port, true)
1: 450.                     else
1: 451.                     begin  { handshake has been delayed too long }
1: 452.                           cur_req_ptr^.hard_error := errbase + 7;
1: 453.                           FINISH_REQ(port, false)
1: 454.                     end
1: 455.                   end
1: 456.   end; { xmit_to_hndl }
1: 457.
1: 458.
1: 459.   function DRIVER(* parameters: param_ptr): integer *);
1: 460.     {*************************************************************}
1: 461.     {*
          *}
1: 462.     {* Description: RS-232 external interface via DRIVERCALL       *}
1: 463.     {*
          *}
1: 464.     {* Input Parameters: Parameters depend on function code                *}
1: 465.     {*
          *}
1: 466.     {* Output Parameters: none                                                    *}
1: 467.     {*
          *}
1: 468.     {* Side Effects:  none                                                        *}
1: 469.     {*
          *}
1: 470.     {* Special Conditions of Use: none                                                 *}
1: 471.     {*
          *}
1: 472.     {* Error Conditions:                                                          *}
1: 473.     {*
          *}
1: 474.     {*************************************************************}
1: 475.
1: 476.   VAR
1: 477.     prevints: intson_type;
1: 478.     port_ptr: portrec_ptr;
1: 479.     i: integer;
1: 480.     char_in: int1;
1: 481.     ptrsysg: ^longint;
1: 482.
1: 483.
1: 484.     procedure WR_SCC(regno: int1; cal: int1);
```

```
1: 485.           {*************************************************************}
1: 486.           {*  Internal procedure to write to the SCC control registers   *}
1: 487.           {*    Assures necessary delay between accesses.                  *}
1: 488.           {*************************************************************}
1: 489.     VAR
1: 490.           i: integer;
1: 491.
1: 492.     begin
1: 493.
1: 494.           {$R-} { rangecheck off }
1: 495.
1: 496.           with port_ptr^.hwcontrol do
1: 497.           begin
1: 498.             controlreg := regno;
1: 499.             i := 128;  { NOP to assure delay between accesses }
1: 500.             controlreg := val
1: 501.           end
1: 502.
1: 503.           {$IFC debug2}
1: 504.           {$R+}   { rangecheck back on if debug }
1: 505.           {$ENDC}
1: 506.
1: 507.     end; {WR_SCC}
1: 508.
1: 509.
1: 510.     procedure INITIT;
1: 511.           {*************************************************************}
1: 512.           {*  Internal procedure to handle initialize "case" and reduce          *}
1: 513.           {*    code size of main DRIVER procedure                                 *}
1: 514.           {*************************************************************}
1: 515.
1: 516.     VAR
1: 517.           ptrsysg: ^longint;
1: 518.           err, i, chan: integer;
1: 519.           temp: longint;
1: 520.           portacontrol, portbcontrol: int1recptr;
1: 521.           p: params;
1: 522.           dcrec: dc_rec;
1: 523.
1: 524.     begin
1: 525.           INTSOFF(rsints, prevints);
1: 526.
1: 527.           err := 0;
1: 528.           portacontrol := pointer(iospacemmu*$20000 + $00203);
1: 529.           portbcontrol := pointer(ord(portacontrol) - 2);
1: 530.           ptrsysg := pointer(bsysglob);      { set up pointer to b_sysglobal_ptr }
1: 531.           chan := parameters^.configptr^.iochannel;
1: 532.           if not GETSPACE(sizeof(portrec), ptrsysg^, temp) then
1: 533.             err := errbase + 6
1: 534.           else
1: 535.           begin
1: 536.             parameters^.configptr^.cb_addr := temp;
1: 537.             port_ptr := pointer(temp)
1: 538.           end;
1: 539.
1: 540.           { initialize either port A or B }
1: 541.
1: 542.           if err = 0 then
1: 543.           with port^ do
1: 544.           begin
1: 545.             cur_req_ptr := nil;
1: 546.
1: 547.             if chan = 0 then
1: 548.             begin { chan A initializations }
1: 549.                   xmtrr0 := $10;  { bit position for DSR signal }
1: 550.                   xmtzrr0 := 0;
1: 551.                   openwr14:= 3;    { port A uses baud rate generator }
1: 552.                   openwr5 := -22;
1: 553.                   hwcontrol := portacontrol;
1: 554.                   i := hwcontrol^.controlreg;  { make sure initially writes to reg 0 }
1: 555.                   WR_SCC(9, -118)  { = $8a, reset chan A }
1: 556.             end
1: 557.             else
1: 558.             begin { chan B initializations }
1: 559.                   xmtrr0 := 0;
1: 560.                   xmtzrr0 := $20; { bit position for DSR signal }
```

```
1: 561.                 openwr14:= 1;
1: 562.                 openwr5 := -24;
1: 563.                 hwcontrol := portbcontrol;
1: 564.                 i := hwcontrol^.controlreg;  { make sure initially writes to reg 0 }
1: 565.                 WR_SCC(9, $4a)  { reset chan B }
1: 566.             end;
1: 567.
1: 568.             WR_SCC(4, $44);         { set operating mode }
1: 569.
1: 570.             if chan = 0 then
1: 571.                 WR_SCC(11, $50)  { Port A uses baud rate generator }
1: 572.             else
1: 573.             begin
1: 574.                 WR_SCC(11, -48);  { = $D0 for oscillator source on Port B }
1: 575.                 for i := 1 to 1000 do ;  { kill time while oscillator starts }
1: 576.             end;
1: 577.
1: 578.             { set baud rate }
1: 579.
1: 580.             p.configptr := parameters^.configptr;
1: 581.             p_fnctn_code := dcontrol;
1: 582.             dcrec.dversion := 2;
1: 583.             dcrec.dcode := 5;
1: 584.             dcrec.ar10[0] := 1200;  { set to 1200 baud }
1: 585.             p.parptr := ord(@dcrec);
1: 586.             i := DRIVER(@p);  { call internal device control routine }
1: 587.
1: 588.             WR_SCC(10, 0);
1: 589.
1: 590.             WR_SCC(3, -63);         { set receiver state to $41 or $C1 }
1: 591.
1: 592.             WR_SCC(5, openwr5);     { set to standard modem signals }
1: 593.
1: 594.             if chan = 0 then
1: 595.                 WR_SCC(15, $10)  { enable ints in SYNC A modem change }
1: 596.             else
1: 597.                 WR_SCC(15, $20);  { or enable ints on CTS B modem change }
1: 598.
1: 599.             WR_SCC($11, $13);  { enable desired interrupts: $13 or $17 }
1: 600.
1: 601.             hwdata := pointer(ord(hwcontrol) + 4);
1: 602.             in_break := false;
1: 603.             restrt_in := false;
1: 604.             restrt_out := false;
1: 605.             prev_char := 0;
1: 606.             autolf := false;
1: 607.             nullink.fwd_link := ord(@nullink) - ptrsysg^;
1: 608.             nullink.bkwd_link := nullink.fwd_link;
1: 609.             discon_detect := false;
1: 610.             xmit_wait := false;
1: 611.             xmt_hs := hw_hs;
1: 612.
1: 613.             { receiver initializations }
1: 614.
1: 615.             no_block := true;
1: 616.             rec_hs := hwhs;
1: 617.             prity_char := 1;
1: 618.             last_stopped := false;
1: 619.             xmit_timeout := 20000;  { 20 seconds }
1: 620.             ALARM_ASSIGN(xmit_ref, parameters^.configptr, rsints);
1: 621.             if xmit_ref = 0 then
1: 622.             begin
1: 623.                 err := 602;  { timer table full }
1: 624.                 RELSPACE(ord(port_ptr), ptrsysg^)
1: 625.             end
1: 626.             else
1: 627.             begin  { initialize type-ahead buffer }
1: 628.                 size_typeah := 0;         { temporary value }
1: 629.                 p.configptr := parameters^.configptr;
1: 630.                 p.fnctn_code := dcontrol;
1: 631.                 dcrec.dversion := 2;
1: 632.                 dcrec.dcode := 9;
1: 633.                 dcrec.ar10[0] := 64;  { size of type-shead buffer }
1: 634.                 dcrec.ar10[1] := 16;  { low threshold }
1: 635.                 dcrec.ar10[2] := 32;  { high threshold }
1: 636.                 p.parptr := ord(@dcrec);
```

```
1: 637.
1: 638.                   { NOTE: There is code missing in the listing here. }
1: 639.                   {                The missing parts are from here to the VAR }
1: 640.                   {                of the next procedure.                                          }
1: 641.
1: 642.             end
1: 643.           end { with }
1: 644.     end; {INITIT}
1: 645.
1: 646.     procedure CONTROLIT;
1: 647.           {*****************************************************************}
1: 648.           {*  Internal procedure to handle control "case" and reduce     *}
1: 649.           {*    code size of main DRIVER procedure                                                 *}
1: 650.           {*****************************************************************}
1: 651.
1: 652.     VAR
1: 653.           speed, i: integer;
1: 654.           ptrsysg: ^longint;
1: 655.           temp: longint;
1: 656.           dc_rec_ptr: ^dc_rec;
1: 657.
1: 658.     begin
1: 659.           with parameters^ do
1: 660.           begin
1: 661.             DRIVER := 0;
1: 662.             if configptr^.iochannel = 0 then
1: 663.                   temp := 125000  { constant for baud rate computation }
1: 664.             else
1: 665.                   temp := 115200;  { for baud rates }
1: 666.
1: 667.             dc_rec_ptr := pointer(parptr);
1: 668.             INTSOFF(rsints, prevints);
1: 669.             with port_ptr^, dc_rec_ptr^ do
1: 670.                   if dversion <> 2 then
1: 671.                     DRIVER := errbase + 0  { wrong application version }
1: 672.                   else
1: 673.                   case dcode of
1: 674.                   1: begin  { set parity and leave break state }
1: 675.                             if configptr^.iochannel = 0 then
1: 676.                                   openwr5 := -86    { = $AA, for port A }
1: 677.                             else
1: 678.                                   openwr5 := -88; { = $A8, port B always has RTS off }
1: 679.                             in_break := false;  { removing break state }
1: 680.
1: 681.                             case ar10[0] of
1: 682.                             0: begin        { no parity, 8 data, 1 stop }
1: 683.                                  WR_SCC(4, $44);
1: 684.                                  WR_SCC(2, -63);         { = $C1 }
1: 685.                                  openwr5 := openwr5 + $40;
1: 686.                                  prity_char := 1         { disable parity err replacement }
1: 687.                                end;
1: 688.                             1, 2: begin  { odd parity, 7 data, 1 stop }
1: 689.                                  WR_SCC(4, $45);
1: 690.                                  WR_SCC(3, $41);
1: 691.                                  prity_char := 0
1: 692.                                end;
1: 693.                             3, 4: begin  { even parity, 7 data, 1 stop }
1: 694.                                  WR_SCC(4, $47);
1: 695.                                  WR_SCC(3, $41);
1: 696.                                  prity_char := -128  { = $80 }
1: 697.                                end;
1: 698.                             end; { case ar10[0] }
1: 699.
1: 700.                             if (rec_hs = hwhs) and last_stopped then
1: 701.                                   WR_SCC(5, openwr5 - (-128))  { no DTR }
1: 702.                             else
1: 703.                                   WR_SCC(5, openwr5);
1: 704.
1: 705.                             if odd(ar10[0]) then
1: 706.                             begin
1: 707.                                   WR_SCC($11, $13);  { ignore parity on input }
1: 708.                                   prity_char := 1  { disable input parity err replacement }
1: 709.                             end
1: 710.                             else
1: 711.                             begin
1: 712.                                   WR_SCC($11, $17);  { enable input parity checking }
```

```
1: 713.                                 hwcontrol^.controlreg := $30  { ignore prior parity errors }
1: 714.                           end
1: 715.                         end;
1: 716.
1: 717.                 2: begin  { set up for DSR handshake on output }
1: 718.                           xmit_hs := hw_hs;
1: 719.                           if configptr^.iochannel = 0 then
1: 720.                                 xmtrr0 := $10
1: 721.                           else
1: 722.                                 xmtzrr0 := $20;  { port B }
1: 723.                           xmit_wait := false  { clear XOFF state. fixes bug }
1: 724.                         end;
1: 725.
1: 726.                 3: if cur_req_ptr <> nil then
1: 727.                           DRIVER := errbase + 9  { don't change xmit_hs during I/O }
1: 728.                         else
1: 729.                         begin  { set up for xon, xoff handshake on output }
1: 730.                           xmit_hs := xmt_xon;
1: 731.                           xmit_wait := false  { put in xon state. fixes bug }
1: 732.                         end;
1: 733.
1: 734.                 4: begin  { set up for delay on cr, lf }
1: 735.                           xmt_hs := xmit_delay;
1: 736.                           crlfdelay := ar10[0];
1: 737.                           xmit_wait := false  { clear xoff state. fixes bug }
1: 738.                         end;
1: 739.
1: 740.                 5: begin  { set baud rate }
1: 741.                           speed := (temp div ar10[0]) - 2;
1: 742.                           WR_SCC(14, 0);  { turn off baud rate generator }
1: 743.                           {$R-}  {rangecheck off}
1: 744.                           WR_SCC(12, speed);  { lsb }
1: 745.                           {$IFC debug2}
1: 746.                           {$R+}  { rangecheck back on if debug }
1: 747.                           {$ENDC}
1: 748.                           WR_SCC(13, speed div 256);  { msb }
1: 749.                           WR_SCC(14, openwr14)
1: 750.                         end;
1: 751.                 6: no_block := (ar10[0] <> 0);  { set input blocking/not-blocking mode }
1: 752.
1: 753.                 7: begin  { set input mode to hardware handshake }
1: 754.                           if num_typeah >= full_thresh then
1: 755.                           begin
1: 756.                                 WR_SCC(5, openwr5 - (-128));  { no DTR }
1: 757.                                 last_stopped := true;
1: 758.
1: 759.                                 {$IFC debug2}
1: 760.                                 if TRACE(DD, 5) then
1: 761.                                   writeln('  RS-232 DTR turned off.');
1: 762.                                 {$ENDC}
1: 763.
1: 764.                           end
1: 765.                           else
1: 766.                           begin
1: 767.                                 WR_SCC(5, openwr5);  { enable receive handshake signals }
1: 768.                                 last_stopped := false;
1: 769.
1: 770.                                 {$IFC debug2}
1: 771.                                 if TRACE(DD, 5) then
1: 772.                                   writeln(' RS-232 DTR turned on.');
1: 773.                                 {$ENDC}
1: 774.
1: 775.                           end;
1: 776.                           rec_hs := hwhs;
1: 777.                         end;
1: 778.
1: 779.                 8: begin { set input mode to xon/xoff }
1: 780.                           rec_hs := rec_xon;
1: 781.                           if num_typeah >= full_thresh then
1: 782.                           begin
1: 783.                                 last_stopped := true;
1: 784.                                 hschar_pending := xoff_char;
1: 785.                                 BYTEO(port_ptr)
1: 786.                           end
1: 787.                           else
1: 788.                           begin
```

```
1: 789.                                 last_stopped := false;
1: 790.                                 hschar_pending := xon_char;
1: 791.                                 BYTEO(port_ptr)
1: 792.                           end
1: 793.                         end;
1: 794.
1: 795.                   9: begin  { flush and redefine typeahead buffer }
1: 796.                           num_typeah := 0;
1: 797.                           get_typeah := put_typeah;
1: 798.                           hschar_pending := no_char;
1: 799.                           if last_stopped then
1: 800.                           begin
1: 801.                                 last_stopped := false;
1: 802.                                 if rec_hs = hwhs then
1: 803.                                 begin  { toggle modem signals }
1: 804.
1: 805.                                   {$IFC debug2}
1: 806.                                   if TRACE(DD, 5) then
1: 807.                                         writeln(' RS-232 DTR turned on.');
1: 808.                                   {$ENDC}
1: 809.
1: 810.                                   WR_SCC(5, openwr5)  { enable receive handshake signals }
1: 811.                                 end
1: 812.                                 else
1: 813.                                 begin  { send xon }
1: 814.                                   hschar_pending := xon_char;
1: 815.                                   BYTEO(port_ptr);
1: 816.                                 end;
1: 817.                           end;
1: 818.                           ptrsysg := pointer(bsysglob);
1: 819.                           if ar10[0] >= 0 then
1: 820.                           begin { allocate new buffer }
1: 821.                                 if size_typeah > 0 then
1: 822.                                   RELSPACE(ord(start_typeah), ptrsysg^);
1: 823.                                 size_typeah := ar10[0];
1: 824.                                 if size_typeah > 0 then
1: 825.                                   if not GETSPACE(size_typeah, ptrsysg^, temp) then
1: 826.                                         DRIVER := errbase + 6
1: 827.                                   else
1: 828.                                   begin
1: 829.                                         start_typeah := pointer(temp);
1: 830.                                         get_typeah := start_typeah;
1: 831.                                         put_typeah := get_typeah;
1: 832.                                         end_typeah := pointer(temp + size_typeah)
1: 833.                                   end
1: 834.                           end;
1: 835.
1: 836.                           if ar10[1] >= -1 then { -2 means don't change, -1 means disable }
1: 837.                                 empty_thresh := ar10[1];
1: 838.                           if ar10[2] >= -1 then { -2 means don't change, too big means disable }
1: 839.                                 full_thresh := ar10[2]
1: 840.                         end;
1: 841.
1: 842.                    10: begin { enable framing err as discon }
1: 843.                                 with hwcontrol^ do
1: 844.                                 begin
1: 845.                                   controlreg := 15;
1: 846.                                   i := 128;  { NOP to assure delay between accesses }
1: 847.                                   i := controlreg  { get current reg15 value }
1: 848.                                 end; { with }
1: 849.                                 if ar10[1] <> 0 then
1: 850.                                 begin
1: 851.                                   discon_detect := true;
1: 852.                                   if i >= 0 then
1: 853.                                         i := i + (-128)
1: 854.                                 end
1: 855.                                 else
1: 856.                                 begin
1: 857.                                   discon_detect := false;
1: 858.                                   if i < 0 then
1: 859.                                         i := i - (-128)
1: 860.                                 end
1: 861.                                 WR_SCC(15, i)  { set updated reg15 value }
1: 862.                           end;
1: 863.
1: 864.                   11: begin       { set up for no handshake on output }
```

```
 1: 865.                                 xmt_hs := hw_hs;
 1: 866.                                 xmtrr0 := 0;
 1: 867.                                 xmtzrr0 := 0;
 1: 868.                                 xmit_wait := false  { clear xoff state. fixes bug }
 1: 869.                               end;
 1: 870.
 1: 871.                     12: xmit_timeout := ar10[0] * 1000;
 1: 872.                               { set transmit-handshake timeout in seconds. <= 0 is infinite }
 1: 873.
 1: 874.                     13:  { put into break state for specified period of time }
 1: 875.                               if cur_req_pre <> nil then
 1: 876.                                 DRIVER := errbase + 9  { don't start break during I/O }
 1: 877.                               else
 1: 878.                               begin
 1: 879.                                 ALARMRELATIVE(xmit_ref, ar10[0]);  { remain in break state until
 1: 880.
alarm or dcontrol 1 }
 1: 881.
 1: 882.                                 if not in_break then
 1: 883.                                 begin
 1: 884.                                       in_break := true;
 1: 885.                                       openwr5 := openwr5 + $10;          { enable break }
 1: 886.                                       if ((rec_hs = hwhs) and last_stopped) or (ar10[1] <> 0) then
 1: 887.                                         WR_SCC(5, openwr5 - (-128))      { no DTR }
 1: 888.                                       else
 1: 889.                                         WR_SCC(5, openwr5)  { enable receive handshake signals }
 1: 890.                                 end;
 1: 891.                               end;
 1: 892.
 1: 893.                     17: autolf := (ar10[0] <> 0);      { enable/disable auto LF }
 1: 894.
 1: 895.                     otherwise
 1: 896.                       DRIVER := errbase + 9;
 1: 897.                     end; { case dcode }
 1: 898.
 1: 899.                     INTSON(prevints)
 1: 900.             end { with }
 1: 901.     end; { controlit }
 1: 902.
 1: 903.     procedure STARTIT;
 1: 904.             {*************************************************}
 1: 905.             {*  Internal procedure to handle SEQIO "case"                *}
 1: 906.             {*************************************************}
 1: 907.
 1: 908.     VAR
 1: 909.             ptrsysg: ^longint;
 1: 910.             leftlink: link_ptr;
 1: 911.             ext_ptr: seqextptr_type;
 1: 912.
 1: 913.     begin
 1: 914.             with parameters^ do
 1: 915.             begin
 1: 916.               ext_ptr := pointer(req^.req_extent);
 1: 917.               if ext_ptr^.num_bytes <= 0 then
 1: 918.                     DRIVER := errbase + 1     { illegal parameters }
 1: 919.               else
 1: 920.               begin
 1: 921.
 1: 922.                     {$IFC debug2}
 1: 923.                     if TRACE(DD, 10) then
 1: 924.                       if ext_ptr^.read_flag then
 1: 925.                             writeln('Reading RS-232.')
 1: 926.                       else
 1: 927.                             writeln(' Writing RS-232.');
 1: 928.                     {#ENDC}
 1: 929.
 1: 930.                     with port_ptr^ do
 1: 931.                     begin   { add request to the device queue or finish the request now }
 1: 932.                       DRIVER := 0;  { no errors }
 1: 933.                       ptrsysg := pointer(bsysglob);  { set pointer to b_sysglobal_ptr }
 1: 934.                       INTSOFF(rsints, prevints);
 1: 935.                       leftlink := pointer(nullink.bkwd_link + ptrsysg^);
 1: 936.                       if ext_ptr^.read_flag and no_block and
 1: 937.                             ((num_typeah = 0) or (leftlink <> @nullink)) then
 1: 938.                       begin  { ok to complete request since non-blocking read w/o char }
 1: 939.                             ADJ_IO_CNT(false, ext_ptr^.buff_rdb_ptr);
```

```
1: 940.                         if discon_detect and (hwcontrol^.controlreg < 0) then
1: 941.                         begin   { assume disconnected cable on input framing error }
1: 942.                           xmit_wait := false;
1: 943.                           req^.hard_error := errbase + 8;
1: 944.                           UNBLK_REQ(req, false)
1: 945.                         end
1: 946.                         else
1: 947.                           UNBLK_REQ(req, true)
1: 948.                     end
1: 949.                     else
1: 950.                     begin
1: 951.                         ENQUEUE(req^.dev_chain, leftlink^, ptrsysg^);
1: 952.                         if cur_req_ptr = nil then
1: 953.                           if not in_break then
1: 954.                               START_NEW_REQUEST(port_ptr);
1: 955.                     end;
1: 956.                     INTSON(prevints)
1: 957.                   end { with port_ptr }
1: 958.           end; {else}
1: 959.         end { with parameters }
1: 960.   end; { startit }
1: 961.
1: 962.
1: 963.   procedure BYTEIN;
1: 964.         {*********************************************************}
1: 965.         {* Put received char in input buffer or type-ahead buffer *}
1: 966.         {*********************************************************}
1: 967.   VAR
1: 968.         i: integer;
1: 969.
1: 970.   begin
1: 971.         with port_ptr^ do
1: 972.         begin
1: 973.           if char_in < 0 then { set "i" to char_in w/o parity bit set }
1: 974.                 i := char_in + $0080
1: 975.           else
1: 976.                 i := char_in;
1: 977.           if (xmit_hs = xmt_xon) and (i = $13) then
1: 978.           begin { got xoff }
1: 979.
1: 980.                 {$IFC debug2}
1: 981.                 if TRACE(DD, 5) then
1: 982.                   writeln(' RS-232 xoff received.');
1: 983.                 {$ENDC}
1: 984.
1: 985.                 xmit_wait := true;
1: 986.                 if cur_req_ptr <> nil then
1: 987.                   if not cur_read_flag then { fixes bug }
1: 988.                         if xmit_timeout > 0 then
1: 989.                           ALARMRELATIVE(xmit_ref, xmit_timeout);
1: 990.           end
1: 991.           else
1: 992.                 if (xmit_hs = xmt_xon) and (i = $11) then
1: 993.                 begin
1: 994.                   if xmit_wait then
1: 995.                   begin  { got xon }
1: 996.
1: 997.                         {$IFC debug2}
1: 998.                         if TRACE(DD, 5) then
1: 999.                           writeln(' RS-232 xon received.');
1:1000.                         {$ENDC}
1:1001.
1:1002.                         xmit_wait := false;
1:1003.                         if cur_req_ptr <> nil then
1:1004.                           if not cur_read_flag then { fixes bug }
1:1005.                           begin
1:1006.                                 ALARMOFF(xmit_ref);
1:1007.                                 BYTEO(port_ptr)
1:1008.                           end
1:1009.                   end
1:1010.                 end
1:1011.                 else  { have real input character, put into typeahead buffer }
1:1012.                   if (num_typeah + 1) <= size_typeah then
1:1013.                   begin  {still room in buffer}
1:1014.                         put_typeah^ := char_in;
1:1015.                         put_typeah := pointer(ord(put_typeah) + 1);
```

```
1:1016.                           if ord(put_typeah) = ord(end_typeah) then
1:1017.                             put_typeah := start_typeah;      { wrap around }
1:1018.                           num_typeah := num_typeah + 1;
1:1019.                           if num_typeah = full_thresh then
1:1020.                           begin
1:1021.                             last_stopped := true;
1:1022.                             if rec_hs = hwhs then
1:1023.                             begin  { toggle modem signals }
1:1024.
1:1025.                                     {$IFC debug2}
1:1026.                                     if TRACE(DD, 5) then
1:1027.                                       writeln(' RS-232 DTR turned off.');
1:1028.                                     {$ENDC}
1:1029.
1:1030.                                     WR_SCC(5, openwr5 - (-128))  { enable receive handshake signals }
1:1031.                             end
1:1032.                             else
1:1033.                             begin  { send xoff }
1:1034.                                     hschar_pending := xoff_char;
1:1035.                                     BYTEO(port_ptr)
1:1036.                             end
1:1037.                           end;
1:1038.                           if cur_req_ptr <> nil then
1:1039.                             if cur_read_flag then
1:1040.                                     COPYBYTES(port_ptr)
1:1041.                     end
1:1042.                     else  { char gets thrown away }
1:1043.                     begin
1:1044.                           if rec_hs = rec_xon then
1:1045.                           begin { send xoff again }
1:1046.                             hschar_pending := xoff_char;
1:1047.                             BYTEO(port_ptr);
1:1048.                             last_stopped := true;
1:1049.                           end
1:1050.                     end
1:1051.           end; { with port_ptr }
1:1052.     end;  { bytein }
1:1053.
1:1054.   {*********************************************************************}
1:1055.
1:1056.   begin { DRIVER }
1:1057.     port_ptr := pointer(parameters^.configptr^.cb_addr);
1:1058.
1:1059.     case parameters^.fnctn_code of
1:1060.     seqio: STARTIT;
1:1061.
1:1062.     dinit: INITIT;  { call subroutine to reduce size of DRIVER }
1:1063.
1:1064.     dcontrol: CONTROLIT;  { call subroutine to reduce size }
1:1065.
1:1066.     dalarms: XMT_TO_HNDL(port_ptr);  { alarm routine }
1:1067.
1:1068.     dinterrupt:    begin  { ignore call version & returned function value for ints }
1:1069.
1:1070.           with port_ptr^, hwcontrol^ do
1:1071.             case parameters^.intpar of
1:1072.
1:1073.             axmit:
1:1074.                   begin
1:1075.                     if hschar_pending <> no_char then
1:1076.                           BYTEO(port_ptr)
1:1077.                     else
1:1078.                           if (cur_req_ptr = nil) or cur_read_flag or xmit_wait then
1:1079.                             controlreg := $28  { ignore output interrupts }
1:1080.                           else
1:1081.                           with cur_info_ptr^ do
1:1082.                             if waitb4next then
1:1083.                             begin  { start up delay following CR or LF }
1:1084.                                     waitb4next := false;
1:1085.                                     ALARMRELATIVE(xmit_ref, crlfdelay);
1:1086.                                     xmit_wait := true;
1:1087.                                     controlreg := $28;  { start timer & kill interrupt }
1:1088.                             end
1:1089.                             else
1:1090.                                     if xfer_count < num_bytes then
1:1091.                                       BYTEO(port_ptr)         { send next byte out }
```

```
1:1092.                                 else
1:1093.                                 begin   { complete previous request }
1:1094.                                   controlreg := $28;  { disable transmit interrupts with $28 }
1:1095.                                   FINISH_REQ(port_ptr, true)
1:1096.                                 end
1:1097.                 end; {axmit}
1:1098.
1:1099.               amodem: begin
1:1100.                         if cur_req_ptr <> nil then
1:1101.                                 if discon_detect and (controlreg < 0) then
1:1102.                                 begin   { assume disconnected cable on input framing error }
1:1103.                                   if xmit_wait then  { fixes bug }
1:1104.                                   begin
1:1105.                                         ALARMOFF(xmit_ref);
1:1106.                                         xmit_wait := false
1:1107.                                   end;
1:1108.                                   cur_req_ptr^.hard_error := errbase + 8;
1:1109.                                   FINISH_REQ(port_ptr, false)
1:1110.                                 end
1:1111.                                 else
1:1112.                                   if xmit_wait then
1:1113.                                         if xmit_hs = hw_hs then
1:1114.                                         begin   { we've been waiting for modem handshake to change }
1:1115.
1:1116.                                           {$IFC debug2}
1:1117.                                           if TRACE(DD, 5) then
1:1118.                                                 writeln('        RS-232 resume xmit after hw handshake. ');
1:1119.                                           {$ENDC}
1:1120.
1:1121.                                           xmit_wait := false;
1:1122.                                           ALARMOFF(xmit_ref);
1:1123.                                           BYTEO(port_ptr)
1:1124.                                         end;
1:1125.                                 controlreg := $10         { reset the modem interrupt state }
1:1126.                 end; {amodem}
1:1127.
1:1128.               arecv:  begin
1:1129.                         char_in := hwdata^;     { get character }
1:1130.                         BYTEIN;         { put received char in input buffer or typeahead buffer }
1:1131.                 end;  {arecv}
1:1132.
1:1133.               aerr: begin
1:1134.                         controlreg := 1;  { prepare to read register 1 with overrun error stat }
1:1135.                         i := 128;  { NOP to assure delay between accesses }
1:1136.                         i := controlreg;
1:1137.                         if prity_char <= 0 then          { testing for parity errs }
1:1138.                                 if ALLSET($10, i) then
1:1139.                                 begin   { parity error }
1:1140.                                   i := hwdata^;  { throw away real char }
1:1141.                                   char_in := prity_char;  { replace with parity error char: 00 or 80 }
1:1142.                                   BYTEIN;
1:1143.                                 end;
1:1144.                         controlreg := $30;  { reset input error }
1:1145.                 end; {aerr}
1:1146.
1:1147.               end { case parameters^.intpar }
1:1148.
1:1149.               port_ptr^.hwcontrol^.controlreg := $38;   { reset interrupt latch }
1:1150.             end; { dinterrupt case }
1:1151.
1:1152.             reqrestart:
1:1153.               begin
1:1154.                 DRIVER := 0;
1:1155.
1:1156.                 {$IFC debug2}
1:1157.                 if TRACE(DD, 5) then
1:1158.                   writeln('  RS-232 restarting request.');
1:1159.                 {$ENDC}
1:1160.
1:1161.                 INTSOFF(rsints, prevints);
1:1162.
1:1163.                 with port_ptr^ do
1:1164.                 begin
1:1165.                   if restrt_in then
1:1166.                   begin
1:1167.                         COPYBYTES(port_ptr);
```

```
 1:1168.                          restrt_in := false
 1:1169.                        end;
 1:1170.                        if restrt_out then
 1:1171.                        begin
 1:1172.                          BYTEO(port_ptr);
 1:1173.                          restrt_out := false
 1:1174.                        end
 1:1175.                      end;
 1:1176.
 1:1177.                      INTSON(prevints);
 1:1178.              end; {reqrestart}
 1:1179.
 1:1180.            ddown: begin
 1:1181.                      INTSOFF(rsints, prevints);
 1:1182.                      DRIVER := 0;
 1:1183.                      with port_ptr^ do
 1:1184.                      begin
 1:1185.                        if cur_req_ptr <> nil then
 1:1186.                          DRIVER := errbase { + ? }
 1:1187.                        else
 1:1188.                        begin
 1:1189.                          ptrsysg := pointer(bsysglob);      { set up pointer to b_sysglobal_ptr }
 1:1190.                          if size_typeah > 0 then
 1:1191.                            RELSPACE(ord(start_typeah), ptrsysg^);
 1:1192.                          ALARMRETURN(xmit_ref);
 1:1193.                          if parameters^.configptr^.iochannel = 0 then { reset channel }
 1:1194.                            WR_SCC(9, -118)         {=$8A}
 1:1195.                          else
 1:1196.                            WR_SCC(9, $4A);
 1:1197.                          parameters^.configptr^.cb_addr := ord(nil);
 1:1198.                          RELSPACE(ord(port_ptr), ptrsysg^);
 1:1199.                        end
 1:1200.                      end;
 1:1201.                      INTSON(prevints)
 1:1202.              end; {ddown}
 1:1203.
 1:1204.            otherwise
 1:1205.              DRIVER := errbase + 2;  { currently unimplemented w/ error }
 1:1206.     end; { case parameters^.fnctn_code }
 1:1207.   end; { DRIVER }
 1:1208.
 1:1209.  end.
 1:1210.

End of File: RS232.TEXT


Directory of files in Cross Reference:

 1: RS232.TEXT
level =  1.

a         1: 171,   1: 187,          1: 565,   1:1196.
adj_io_c 1: 374,   1: 939.
aerr    1:         29*,  1:1133.
alarm_as 1: 620.
alarmoff 1: 378,   1:1006,  1:1105,   1:1122.
alarmrel 1: 203,   1: 339,  1: 879,   1: 989,          1:1085.
alarmret 1:1192.
allset  1: 127,   1: 154,   1: 154,   1: 156,          1:1138.
amodem  1:         27*,  1:1099.
ar10    1: 584=,  1: 633=,  1: 634=,  1: 635=,         1: 681,   1: 705,          1: 736,   1: 741,
          1: 751,   1: 819,          1: 823,   1: 836,          1: 837,   1: 838,          1: 839,   1: 849,
          1: 871,   1: 879,          1: 886,   1: 893.
arecv   1:         28*,  1:1128.
autolf  1:         55*,  1: 168,     1: 606=,  1: 893=.
axmit   1:         26*,  1:1073.
bkwd_lin 1: 608=,  1: 935.
bsysglob 1: 371,   1: 435,  1: 530,   1: 818,          1: 933,   1:1189.
buff_off 1: 159,   1: 237.
buff_rdb 1: 159,   1: 190,  1: 237,   1: 262,          1: 374,   1: 939.
cal       1: 484*.
cb_addr 1: 536=,  1:1057,   1:1197=.
chain_fo 1: 318.
chan    1: 518*,  1: 531=,  1: 547,   1: 570,          1: 594.
char_in 1: 480*,  1: 973,   1: 974,   1: 976,          1:1014,   1:1129=,         1:1141=.
```

```
size_typ 1:       80,   1: 628=,    1: 821,   1: 823=,         1: 824,   1: 825,          1: 832,   1:1012,
         1:1190.
sizeof  1: 532.
speed   1: 653*,  1: 741=,  1: 744,   1: 748.
start_ty 1:       85*,  1: 257,     1: 822,   1: 829=,         1: 830,   1:1017,          1:1191.
status  1: 117*,  1: 123=,  1: 127,   1: 154,          1: 154,   1: 156.
temp    1: 519*,  1: 532,   1: 536,   1: 537,          1: 655*,  1: 663=,         1: 665=,  1: 741,
          1: 825,   1: 829,          1: 832.
trace   1: 133,   1: 143,   1: 180,   1: 198,          1: 272,   1: 386,          1: 760,   1: 771,
          1: 806,   1: 923,          1: 981,   1: 998,          1:1026,   1:1117,          1:1157.
unblk_re 1: 375,   1: 944,  1: 947.
unfreeze 1: 190,   1: 262.
unit    1:         2.
val      1: 500.
waitb4ne 1:       72*,  1: 188=,    1: 335=,  1:1082,          1:1084=.
xfer_cou 1: 174,   1: 175=,          1: 175,   1: 243,          1: 244,   1: 289=,         1: 289,   1: 291,
          1: 446,   1:1090.
xmit_del 1:       32,   1: 186,     1: 445,   1: 735.
xmit_hs 1: 443,   1: 718=,  1: 730=,  1: 977,          1: 992,   1:1113.
xmit_ref 1:       62*,  1: 203,     1: 339,   1: 378,          1: 620,   1: 621,          1: 879,   1: 989,
         1:1006,   1:1085,          1:1105,   1:1122,          1:1192.
xmit_tim 1:       65*,  1: 202,     1: 203,   1: 338,          1: 339,   1: 619=,         1: 871=,  1: 988,
         1: 989.
xmit_wai 1:       61*,  1: 204=,    1: 326=,  1: 336,          1: 377,   1: 440,          1: 444=,  1: 610=,
         1: 723=,  1: 731=,          1: 737=,  1: 868=,         1: 942=,  1: 985=,         1: 994,   1:1002=,
         1:1078,   1:1086=,          1:1103,   1:1106=,         1:1112,   1:1121=.
xmit_xon 1:       32*.
xmt_hs  1:        59*,  1: 153,     1: 186,   1: 336,          1: 445,   1: 611=,         1: 735=,  1: 865=.
xmt_to_h 1:1066.
xmt_xon 1: 336,   1: 443,   1: 730,   1: 977,          1: 992.
xmtrr0  1:        68*,  1: 154,     1: 549=,  1: 559=,         1: 720=,  1: 866=.
xmtzrr0 1:        69*,  1: 154,     1: 550=,  1: 560=,         1: 722=,  1: 867=.
xoff_cha 1:       89*,  1: 129,     1: 784,   1:1034,          1:1046.
xon_char 1:       89,   1: 285,     1: 790,   1: 814.

Procedures

bytein  1: 963*,  1:1130,   1:1142.
byteo   1:        97*,  1: 286,     1: 342,   1: 447,          1: 785,   1: 791,          1: 815,   1:1007,
         1:1035,   1:1047,          1:1076,   1:1091,          1:1123,   1:1172.
controli 1: 646*,  1:1064.
finish_r 1:       94*,  1: 292,     1: 328,   1: 348*,         1: 449,   1: 453,          1:1095,   1:1109.
initit  1: 510*,  1:1062.
port    1:        94*,  1:  97*,    1: 121,   1: 210*,         1: 234,   1: 286,          1: 292,   1: 297*,
          1: 316,   1: 328,          1: 332,   1: 342,          1: 372,   1: 391,          1: 398*,  1: 422,
          1: 437,   1: 447,          1: 449,   1: 453,          1: 543.
start_ne 1: 297*,  1: 391,  1: 437,   1: 954.
startit 1: 903*,  1:1060.
wr_scc  1: 484*,  1: 555,   1: 565,   1: 568,          1: 571,   1: 574,          1: 588,   1: 590,
          1: 592,   1: 595,          1: 597,   1: 599,          1: 683,   1: 684,          1: 689,   1: 690,
          1: 694,   1: 695,          1: 701,   1: 703,          1: 707,   1: 712,          1: 742,   1: 744,
          1: 748,   1: 749,          1: 756,   1: 767,          1: 810,   1: 861,          1: 887,   1: 889,
          1:1030,   1:1194,          1:1196.
xmit_to_ 1: 398*.

Functions

driver  1:        15*,  1: 459*,    1: 586,   1: 661=,         1: 671=,  1: 727=,         1: 826=,  1: 876=,
          1: 896=,  1: 918=,         1: 932=,  1:1154=,         1:1182=,  1:1186=,         1:1205=.

Declaration Character : '*'
Assignment Character :  '='
```

```
Cross Reference Listing:


Beginning of file: RS232MAIN.TEXT

 1:      1.
 1:      2.  PROGRAM DUMMYMAIN;
 1:      3.
 1:      4.    USES
 1:      5.         {$U object/driverdefs.obj}
 1:      6.           driverdefs,
 1:      7.         {$U object/driversubs.obj}
 1:      8.           driversubs,
 1:      9.         {$U object/rs232.obj}                    { these 2 lines vary from driver }
 1:     10.           rs232;                                                 {                          to driver
         }
 1:     11.
 1:     12.    VAR
 1:     13.         i: integer;
 1:     14.         p: param_ptr;
 1:     15.
 1:     16.    begin
 1:     17.         i := driver(p);
 1:     18.    end;

End of File: RS232MAIN.TEXT


Directory of files in Cross Reference:

 1: RS232MAIN.TEXT
level =  1.

driver  1:         17.
driverde 1:         6.
driversu 1:         8.
dummymai 1:         2.
i         1:       13*,  1:  17=.
p         1:       14*,  1:  17.
param_pt 1:        14.
program 1:          2.
rs232   1:         10.

Declaration Character : '*'
Assignment Character :  '='
```

Cross Reference Listing:

Beginning of file: DRIVERDEFS.TEXT

```
 1:      1.
 1:      2.  UNIT DRIVERDEFS;         { UNIT NEEDED BY CONFIGURABLE DRIVERS }
 1:      3.
 1:      4.          { By Dave Offen }
 1:      5.          { Copyright 1983, Apple Computer Inc. }
 1:      6.
 1:      7.  INTERFACE
 1:      8.
 1:      9.  {$U-} { DON'T use any iospaslib definitions in place of explicitly USE -d modules }
 1:     10.  {$X-} { Disallow automatic stack expansion in system code }
 1:     11.
 1:     12.  {$SETC DEBUG:=TRUE}
 1:     13.  {$SETC DEBUG1:=FALSE}
 1:     14.  {$SETC DEBUG2:=FALSE}
 1:     15.  {$SETC DEBUG3:=FALSE}
 1:     16.  {$SETC OS1S:=TRUE}
 1:     17.
 1:     18.
 1:     19.  CONST
 1:     20.
 1:     21.    {*************************************************************************
 1:     22.    {**
 1:     23.    {** The delineated values are also defined in PASCALDEFS assembly language routine.
 1:     24.    {**
 1:     25.    {*************************************************************************
 1:     26.
 1:     27.         { valid values in INTSOFF_TYPE }
 1:     28.    {**} allints = $700;   { all interrupts off}
 1:     29.                 rsints   = $600;   { rs232 interrupts off }
 1:     30.                 slotints = $500;   { 3 I/O slots interrupts off }
 1:     31.                 copsints = $200;   { cops interrupts off }
 1:     32.                 vertints = $100;   { vertical retrace interrupts off }
 1:     33.                 twigints = $100;   { twiggy interrupts off }
 1:     34.                 winints  = $100;   { winchester disk interrupts off }
 1:     35.                 clokints = $100;   { clock interrupts off }
 1:     36.                 clkonints = 0;             { clock interrupts on; lower off }
 1:     37.
 1:     38.    {**} bsysglob = $200;           { memory addr of sysglobal base -- same as PASCALDEFS }
 1:     39.    {**} portcbofset = -24609;      { this MUST be the same as port_cb_ptrs in PASCALDEFS }
 1:     40.
 1:     41.    {**} maxdev = 39;               { size of device table }
 1:     42.
 1:     43.                 size_rspec_info = 3; { size of specific info in reqblk }
 1:     44.
 1:     45.
 1:     46.                 max_ename = 32;    { size of e_name string }
 1:     47.
 1:     48.                 iospacemmu = 126;          { mmu used to map the system I/O space }
 1:     49.
 1:     50.
 1:     51.  {**} dinterrupt = 0;             { valid function code values }
 1:     52.          dinit = 1;
 1:     53.          ddown = 2;
 1:     54.          dskunclamp = 3;
 1:     55.          dskformat = 4;
 1:     56.          seqio = 5;
 1:     57.          dskio = 6;
 1:     58.          dcontrol = 7;
 1:     59.          reqrestart = 8;
 1:     60.          ddiscon = 9;
 1:     61.
 1:     62.          dattach = 12;
 1:     63.          hdinit = 13;
 1:     64.          hdskio = 14;
 1:     65.          dunattach = 16;
 1:     66.          dalarms = 17;
 1:     67.          hddown = 18;
 1:     68.
 1:     69.         { Definition of 'slot' # for Lisa Devices }
 1:     70.          cd_slot1 = 0;             { the basic 3 slots }
 1:     71.          cd_slot2 = 1;
```

```
1:     72.          cd_slot3 = 2;
1:     73.          cd_slot4 = 3;             { 1.75's 2 expansion slots }
1:     74.          cd_slot5 = 4;
1:     75.          cd_scc = 5;                       { scc chip controlling two serial channels }
1:     76.          cd_soft = 6;                      { twiggy/sony or whatever else serves as built-in floppy }
1:     77.          cd_hard = 7;                      { widget or whatever serves as built-in hard disk }
1:     78.          cd_paraport = 8;                  { parallel port }
1:     79.          cd_console = 9;                   { alternate and primary console }
1:     80.
1:     81.                                                          { 10-14 are still available }
1:     82.
1:     83.
1:     84.  TYPE
1:     85.    int1 = -128..127; { 1 byte integer }
1:     86.    int2 = integer;        { 2 byte integer }
1:     87.    int4 = longint;        { 4 byte integer }
1:     88.    relptr = int2;         { 2 byte pointer }
1:     89.    absptr = int4;         { 4 byte pointer }
1:     90.
1:     91.    OSPORTION=(MM, PM, EM, EC, AC, FS, DD, INIT, TRECORD, SPARE1, SPARE2, SPARE3, SPARE4);
1:     92.    link_ptr = ^linkage;  {pointer to a linkage record }
1:     93.    linkage = record
1:     94.                              fwd_link: relptr;        { forward link }
1:     95.                              bkwd_link: relptr;       { backward link }
1:     96.                            end;
1:     97.
1:     98.    intsoff_type = $100..$700;              { interrupts off parameter }
1:     99.    intson_type = 0..$700;                  { interrupts on parameter }
1: 100.
1: 101.    {*******************************************}
1: 102.    {*                                                                                 *}
1: 103.    {* COMMON HEADER FOR REQUEST BLOCKS AND PCBS *}
1: 104.    {*                                                                                 *}
1: 105.    {*******************************************}
1: 106.
1: 107.    rb_type = (pcb_type, reqblk_type, ecm_type, spr1, spr2, spr3);
1: 108.    rbheader_type = ^rb_headT;
1: 109.    rb_headT = record
1: 110.                             header: linkage;
1: 111.                             kind: rb_type;
1: 112.                       end;
1: 113.
1: 114.
1: 115.    {*****************************}
1: 116.    {*                                                 *}
1: 117.    {* I/O STRUCTURES FOR DRIVERS *}
1: 118.    {*                                                 *}
1: 119.    {*****************************}
1: 120.
1: 121.    {**} rec_port_cb = record
1: 122.    {**}                               slotx: array[0..2] of absptr;
1: 123.    {**}                               rs232: absptr;     { 2 channels }
1: 124.    {**}                               via2: absptr;      { COPS, SPEAKER, TIMER }
1: 125.    {**}                               via1: absptr;      { hard disk, parallel printer }
1: 126.    {**}                               screen: absptr;
1: 127.    {**}                               floppy: absptr;  { 2 drives & parameter memory }
1: 128.    {**}                          end;
1: 129.
1: 130.    array10ptr = ^array10;
1: 131.    array10 = array[0..9] of longint; { used for passing arbitrary-length parameters }
1: 132.
1: 133.    reqptr_type = ^reqblk;
1: 134.
1: 135.    e_name = string[max_ename];
1: 136.
1: 137.    {**} devtype = (diskdev, pascalbd, seqdev, bitbkt, non_io, spar1, spar2, spar3);
1: 138.    {**} ptrdevrec = ^devrec;
1: 139.
1: 140.    {**} devrec = record
1: 141.    {**}               entry_pt: absptr;        { driver address - nil if uninitialized }
1: 142.    {**}               cb_addr: absptr;         { ptr to control block for this device }
1: 143.    {**}               ext_addr: absptr;        { ptr to additional device info }
1: 144.                               drvrec_ptr: absptr;      { ptr to driver-loading control record.
1: 145.                                                                  Nil means non-configurable. Defined
in genio }
1: 146.                               devname: e_name;         { device name }
```

```
1: 147.                              slot_no: int1;                  { 0-2 or -1 if built-in }
1: 148.                              iochannel: int1;         { channel on hardware controller }
1: 149.                              device_no: int1;         { device on channel }
1: 150.                              devt: devtype;                  { device type }
1: 151.                              blockstructured: boolean; { true if device is block structured }
1: 152.                              permanent: boolean;      { don't unload when DOWNing }
1: 153.                              self_ident: integer;  { from controller card - for debugging }
1: 154.                              required_drve: ptrdevrec; { ptr to "higher" configinfo - nil if none }
1: 155.                              permreq_ptr: reqptr_type; { ptr to pre-allocated req blk - nil if none }
1: 156.                              preq_avail: boolean;  { true when permreq_ptr currently avail for use }
1: 157.                            end;
1: 158.
1: 159.    {**} ext_diskconfig = record  { extension info for disk devrec }
1: 160.    {**}                                hentry_pt: absptr;       { entry pt for Hdisk calls }
1: 161.    {**}                                num_bloks: longint;      { size of device }
1: 162.    {**}                                strt_blok: longint;      { virtual volume start address }
1: 163.    {**}                                fs_strt_blok: longint;   { offset within virtual volume to }
1: 164.    {**}                                                                                    { file system }
1: 165.                                                  removable, ejectable: boolean;
1: 166.    {**}                              end;
1: 167.
1: 168.    {**} configtype = array[0..maxdev] of ptrdevrec;
1: 169.
1: 170.    {**} timestmp_interval = record
1: 171.    {**}                                      sec: longint;
1: 172.    {**}                                      msec: 0..999;
1: 173.    {**}                                    end;
1: 174.
1: 175.          reqsts_type = record                 { request status }
1: 176.                                             reqsrv_f: (active, in_service, complete);
1: 177.                                             reqsuccess_f: boolean; { complete successfully or not }
1: 178.                                             reqabt_f: boolean;          { abort pending or not }
1: 179.                                          end;
1: 180.
1: 181.          reqblk = record                       { request block }
1: 182.                               pcb_chain: rb_headT;  { pcb chain, block header }
1: 183.                               dev_chain: linkage;      { device or pipe chain }
1: 184.                               list_chain: linkage;  { list of request blocks for blocking }
1: 185.                               block_p_f: boolean;      { block process flag }
1: 186.                               blk_in_pcb: int1;        { generic block type for future changes }
1: 187.                               reqstatus: reqsts_type;          { request status }
1: 188.                               operatn: int1;                    { 0=write, 1=read, 2=format, 3=unclamp... }
1: 189.                               cfigptr: ptrdevrec;      { identifies to which device this request
1: 190.                                                                                  belongs if it's an I/O request }
1: 191.                               req_extent: absptr;      { extdptr_type when devt=diskdev
1: 192.                                                                                  seqextptr_type when devt=seqdev }
1: 193.                               hard_error: integer;  { error code when reqstatus.reqsuccess_f
1: 194.                                                                                  = FALSE }
1: 195.                               reqspec_info: longint;   { specific information such as sort
1: 196.                                                                                       key for device's queue }
1: 197.                             end;
1: 198.
1: 199.          disk_io_type = (with_header, without_header, raw_io, chained_hdrs);
1: 200.          pagelabel = packed record
1: 201.                                    version: integer;
1: 202.                                    datastat: (dataok, datamaybe, databad);
1: 203.                                    filler: -32..31;
1: 204.                                    volume: int1;
1: 205.                                    fileid: integer;
1: 206.                                    dataused: integer;
1: 207.                                    abspage: int4;
1: 208.                                    relpage: int4;
1: 209.                                    fwdlink: int4;
1: 210.                                    bkwdlink: int4;
1: 211.                                    end;
1: 212.
1: 213.          extdptr_type = ^disk_extend;
1: 214.          disk_extend = record
1: 215.                                    xfer_count: longint;
1: 216.                                    read_flag: boolean;
1: 217.                                    buff_rdb_ptr: absptr;
1: 218.                                    buff_offset: absptr;
1: 219.                                    resolved_addr: absptr;      { results of freeze_seg }
1: 220.                                    blkno: longint;
1: 221.                                    soft_hdr: pagelabel;
1: 222.                                    last_fwd_link: int4;
```

```
1: 223.                                 last_data_used: integer;
1: 224.                                 io_mode: disk_io_type;
1: 225.                                 num_chunks: integer;
1: 226.                                 misc1: integer;
1: 227.                                 misc2: longint;
1: 228.                                 end;
1: 229.
1: 230.         seqextptr_type = ^seq_extend;
1: 231.         seq_extend = record
1: 232.                                 xfer_count: longint;
1: 233.                                 read_flag: boolean;
1: 234.                                 buff_rdb_ptr: absptr;
1: 235.                                 buff_offset: absptr;
1: 236.                                 resolved_addr: absptr;      { results of freeze_seg }
1: 237.                                 num_bytes: longint;
1: 238.                                 end;
1: 239.
1: 240.         param_ptr = ^params;
1: 241.         params = record
1: 242.                                 configptr: ptrdevrec;
1: 243.                                 case fnctn_code: integer of
1: 244.                                   dskunclamp, ddown, hddown, dskformat, dinit, hdinit, ddiscon: ();
1: 245.                                   seqio, reqrestart, dskio: (req: reqptr_type);
1: 246.                                   dinterrupt, dalarms: (intpar: integer);
1: 247.                                   dcontrol: (parptr: absptr);
1: 248.                                   dattach: (n_configptr: ptrdevrec);
1: 249.                                   dunattach: (o_configptr: ptrdevrec; still_inuse: boolean);
1: 250.                                   hdskio: (c_cmd: integer; c_sector: longint);
1: 251.                                 end;
1: 252.
1: 253.         dc_rec = record  { for dcontrol }
1: 254.                            dversion: integer;
1: 255.                            dcode: integer;
1: 256.                            ar10: array[0..9] of longint;
1: 257.                          end;
1: 258.
1: 259.
1: 260.         { Control Block for hard disk device }
1: 261.         hdiskcb_ptr = ^hdisk_cb;
1: 262.
1: 263.         { NOTE: Definitions marked with ** may have corresponding definitions in }
1: 264.         {                 Assembly language portions of drivers.
  }
1: 265.
1: 266.         hdisk_cb = record           { device/driver info for all Apple format hard disks }
1: 267.                   {**}     config_addr: ptrdevrec;    { config entry address - must be 1st }
1: 268.                   {**}     ext_ptr: absptr;                              { ptr to driver specific info }
1: 269.                   {**}     raw_data_ptr: longint;     { points to start of data }
1: 270.                   {**}     raw_header_ptr: ^pagelabel; { points to start of header }
1: 271.                   {**}     x_leng: longint;                              { offset to data }
1: 272.                   {**}     sect_left: integer;                 { for current request }
1: 273.                   {**}     v_flag: boolean;                              { re-read all writes? }
1: 274.                                    restrt_count: integer;     { the retry count for this req }
1: 275.                                    restrt_limit: integer;     { max number of restarts allowed }
1: 276.                                    total_restarts: longint;   { # of restarts of state machine }
1: 277.                                    soft_header: pagelabel;    { the buffer used to copy header }
1: 278.                                    int_prio: intsoff_type;    { the interrupt priority of this }
1: 279.                                                                                                  { drive }
1: 280.                                    cur_info_ptr: extdptr_type; { ptr to cur req's extension }
1: 281.                                    req_hd_ptr: reqptr_type;   { next request to be serviced }
1: 282.                                    dummy_req_ptr: reqptr_type; { phoney request at cyl -1 or 32767 }
1: 283.                                    cur_num_requests: integer;  { not counting dummy request }
1: 284.                                    worstwarning: integer;    { worst warning encountered in }
1: 285.                                                                                                  { this request }
1: 286.                                  end;
1: 287.
1: 288.
1: 289.
1: 290.         {*********************************}
1: 291.         {*                                                          *}
1: 292.         {* DEFINITION OF SYSGLOBAL CELLS *}
1: 293.         {*                                                          *}
1: 294.         {*********************************}
1: 295.
1: 296.         { port_cb_ptrs: rec_port_cb;         port control block for drivers }
1: 297.         { configinfo: configtype;            table of configured devices }
```

```
 1: 298.          { sysa5: longint;                                  A5 required for pascal }
 1: 299.
 1: 300.    IMPLEMENTATION
 1: 301.
 1: 302.    end.
 1: 303.

End of File: DRIVERDEFS.TEXT


Directory of files in Cross Reference:

 1: DRIVERDEFS.TEXT
level =  1.

abspage 1: 207*.
absptr  1:        89*,  1: 122,     1: 123,   1: 124,         1: 125,   1: 126,         1: 127,   1: 141,
         1: 142,   1: 143,          1: 144,   1: 160,         1: 191,   1: 217,         1: 218,   1: 219,
         1: 234,   1: 235,          1: 236,   1: 247,         1: 268.
ac       1:       91*.
active  1: 176*.
allints 1:        28*.
ar10    1: 256*.
array10 1: 130,   1: 131*.
array10p 1: 130*.
bitbkt  1: 137*.
bkwd_lin 1:       95*.
bkwdlink 1: 210*.
blk_in_p 1: 186*.
blkno   1: 220*.
block_p_ 1: 185*.
blockstr 1: 151*.
bsysglob 1:       38*.
buff_off 1: 218*,  1: 235*.
buff_rdb 1: 217*,  1: 234*.
c_cmd   1: 250*.
c_sector 1: 250*.
cb_addr 1: 142*.
cd_conso 1:       79*.
cd_hard 1:        77*.
cd_parap 1:       78*.
cd_scc  1:        75*.
cd_slot1 1:       70*.
cd_slot2 1:       71*.
cd_slot3 1:       72*.
cd_slot4 1:       73*.
cd_slot5 1:       74*.
cd_soft 1:        76*.
cfigptr 1: 189*.
chained_ 1: 199.
clkonint 1:       36*.
clokints 1:       35*.
complete 1: 176.
config_a 1: 267*.
configpt 1: 242*.
configty 1: 168*.
copsints 1:       31*.
cur_info 1: 280*.
cur_num_ 1: 283*.
dalarms 1:        66*,  1: 246*.
databad 1: 202.
datamayb 1: 202*.
dataok  1: 202*.
datastat 1: 202*.
dataused 1: 206*.
dattach 1:        62*,  1: 248*.
dc_rec  1: 253*.
dcode   1: 255*.
dcontrol 1:       58*,  1: 247*.
dd       1:       91*.
ddiscon 1:        60*,  1: 244.
ddown   1:        53*,  1: 244*.
dev_chai 1: 183*.
device_n 1: 149*.
devname 1: 146*.
devrec  1: 138,   1: 140*.
```

```
vertints 1:        32*.
via1    1: 125*.
via2    1: 124*.
volume  1: 204*.
winints 1:         34*.
with_hea 1: 199*.
without_ 1: 199*.
worstwar 1: 284*.
x_leng  1: 271*.
xfer_cou 1: 215*,  1: 232*.

Declaration Character : '*'
Assignment Character :  '='
```

THE END